

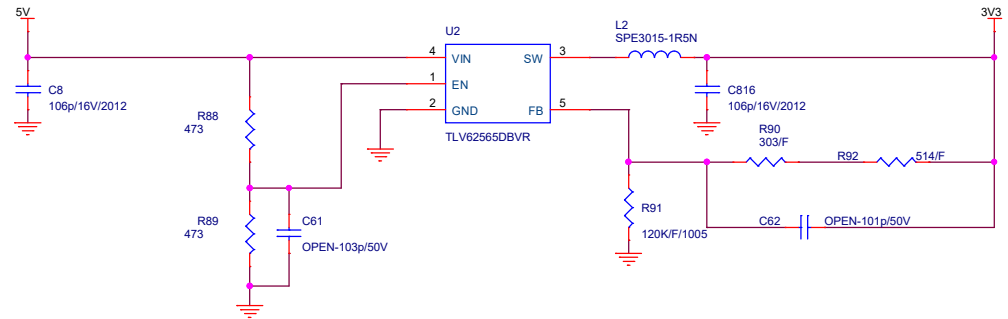
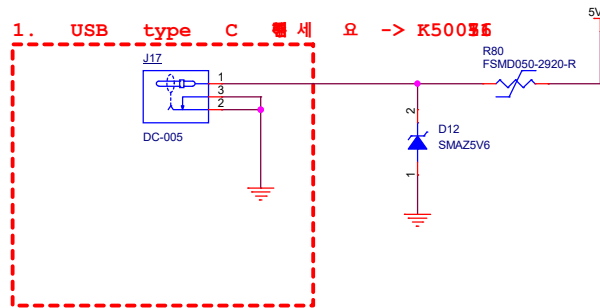
청화

RF GENERATOR

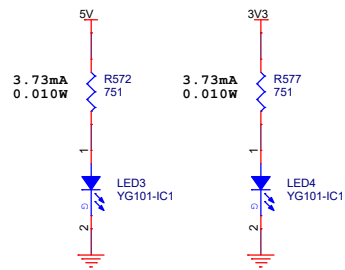
RF GENERATOR		neurosona	
NS-US300			
Title			
TITLE			
Size	Document	Number	Rev
	<Doc>		0.1
Date: Wednesday, August 11, 2021		Sheet 1 of 10	

5V DC INPUT

1. USB type C 회로 설계 요 -> K50015



2. 12V 출 회로 설계 필요



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3. 배리 충전 회로 설계 필요

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TitlePOWER : BATTERY

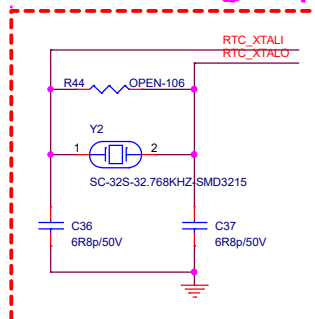
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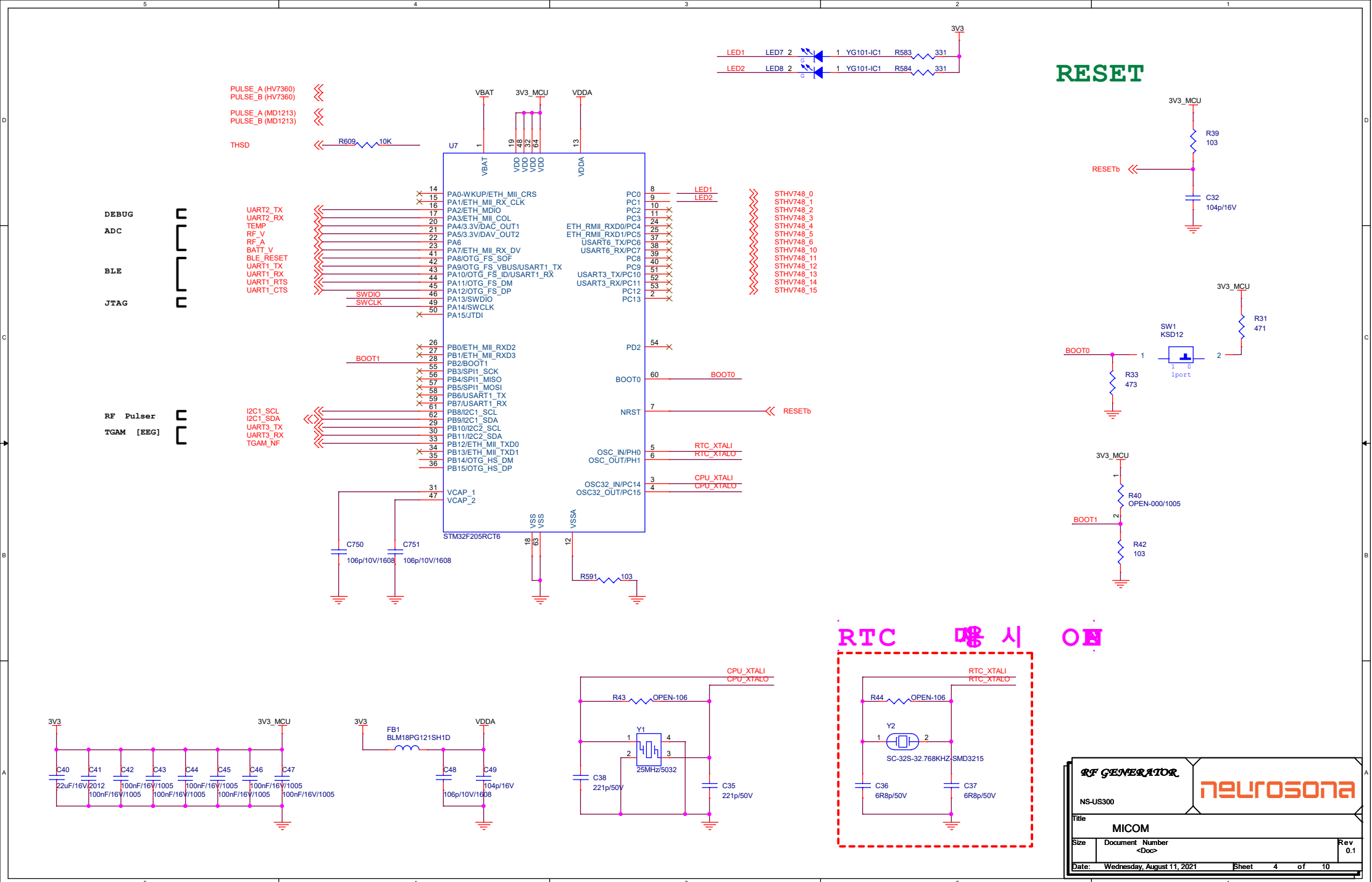
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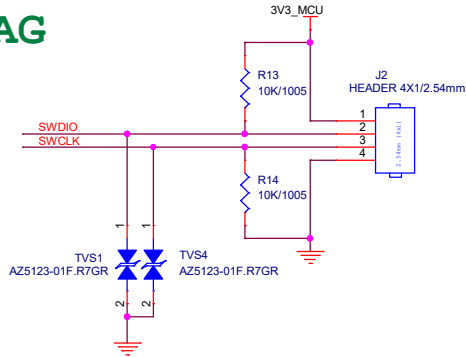


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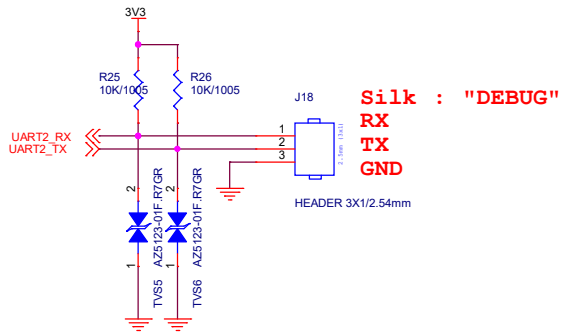


4. 회로 구성 필요 니 다

JTAG



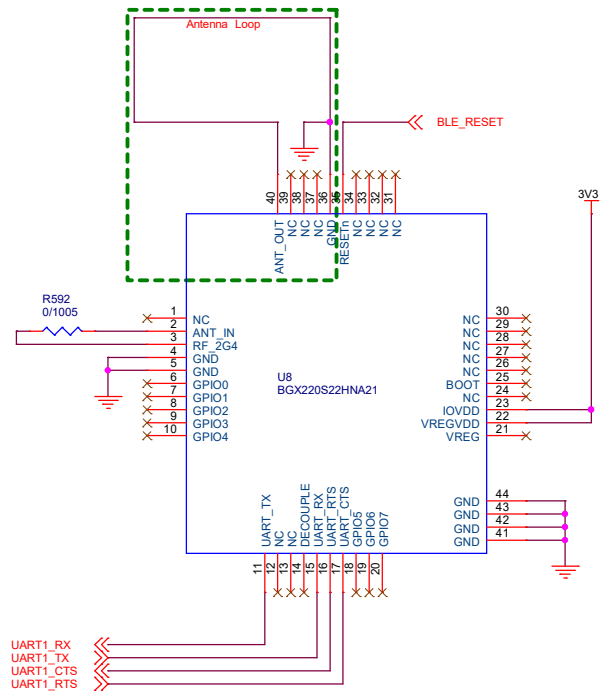
DEBUG



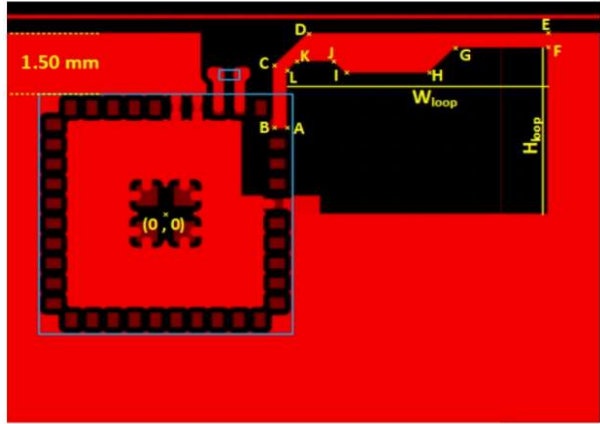
Silk : "DEBUG"
RX
TX
GND

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BLE



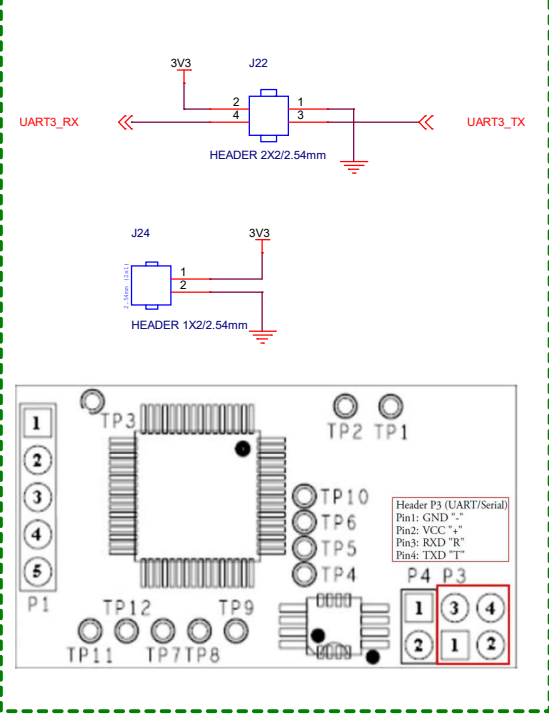
Design Guidelines



Point	BGX220S22HNA21
A	(2.87, 2.13)
B	(2.54, 2.13)
C	(2.54, 3.69)
D	(3.36, 4.51)
E	(7.75, 4.51)
F	(7.75, 4.15)
G	(6.84, 4.15)
H	(6.21, 3.52)
I	(4.26, 3.52)
J	(3.97, 3.81)
K	(3.10, 3.81)
L	(2.87, 3.58)
Wloop	4.88
Hloop	4.15

Note:
1. All coordinates and dimensions listed in mm.

TGAM Module (EEG)



RF GENERATOR

neurosona

NS-US300

Title
EEG, BLE

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Document Number
<Doc>

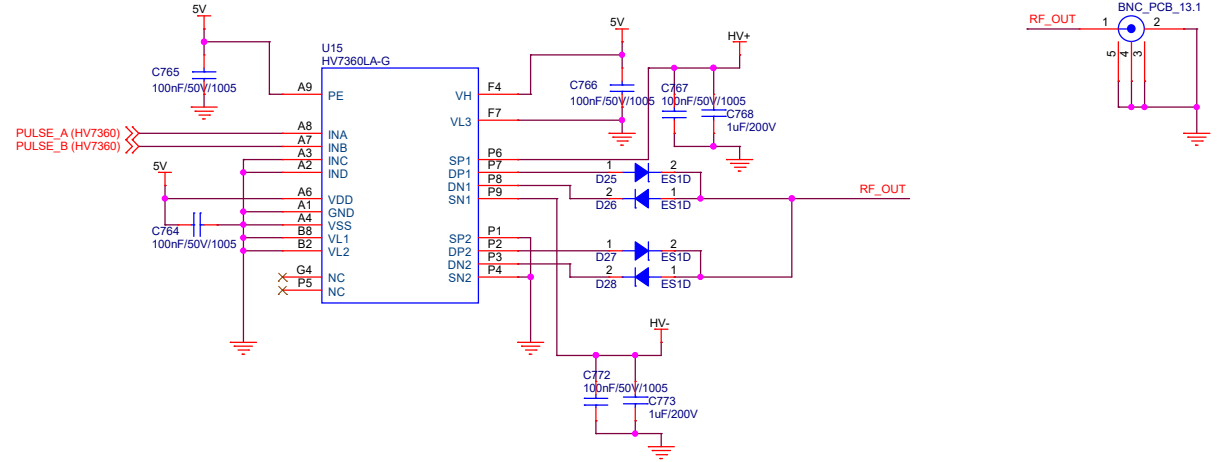
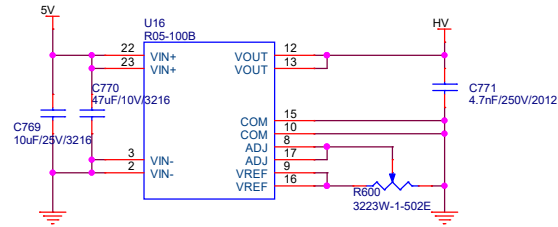
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HV7360

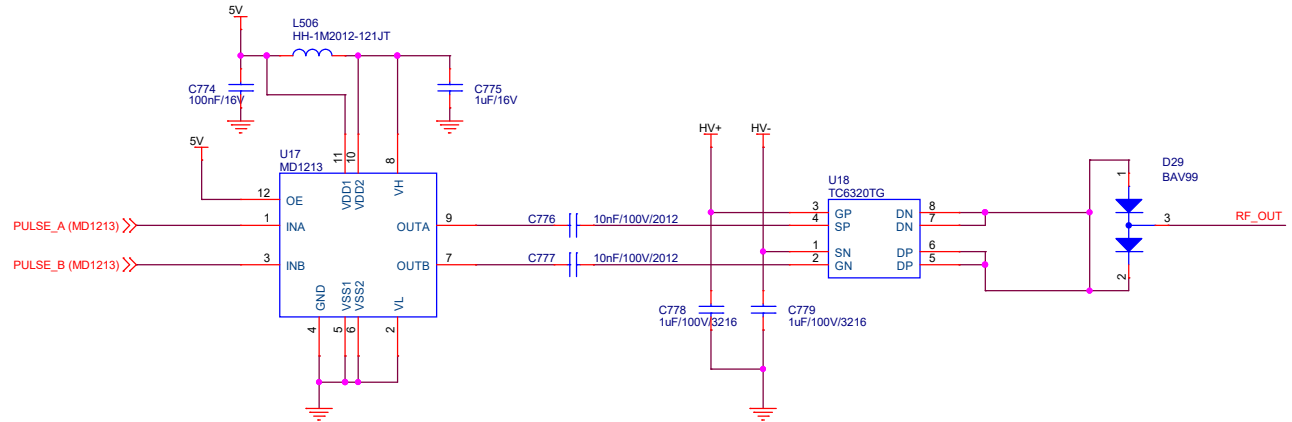
5. 둘 중 소형화 단일에 적용 가능한 옵션으로 선택 부탁드립니다



MD1213 + TC6320

Note

HV, HV+, HV-
High Voltage block 확인 상태




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RF GEN			
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6. 대실 pow er blo k에 필 요

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Title

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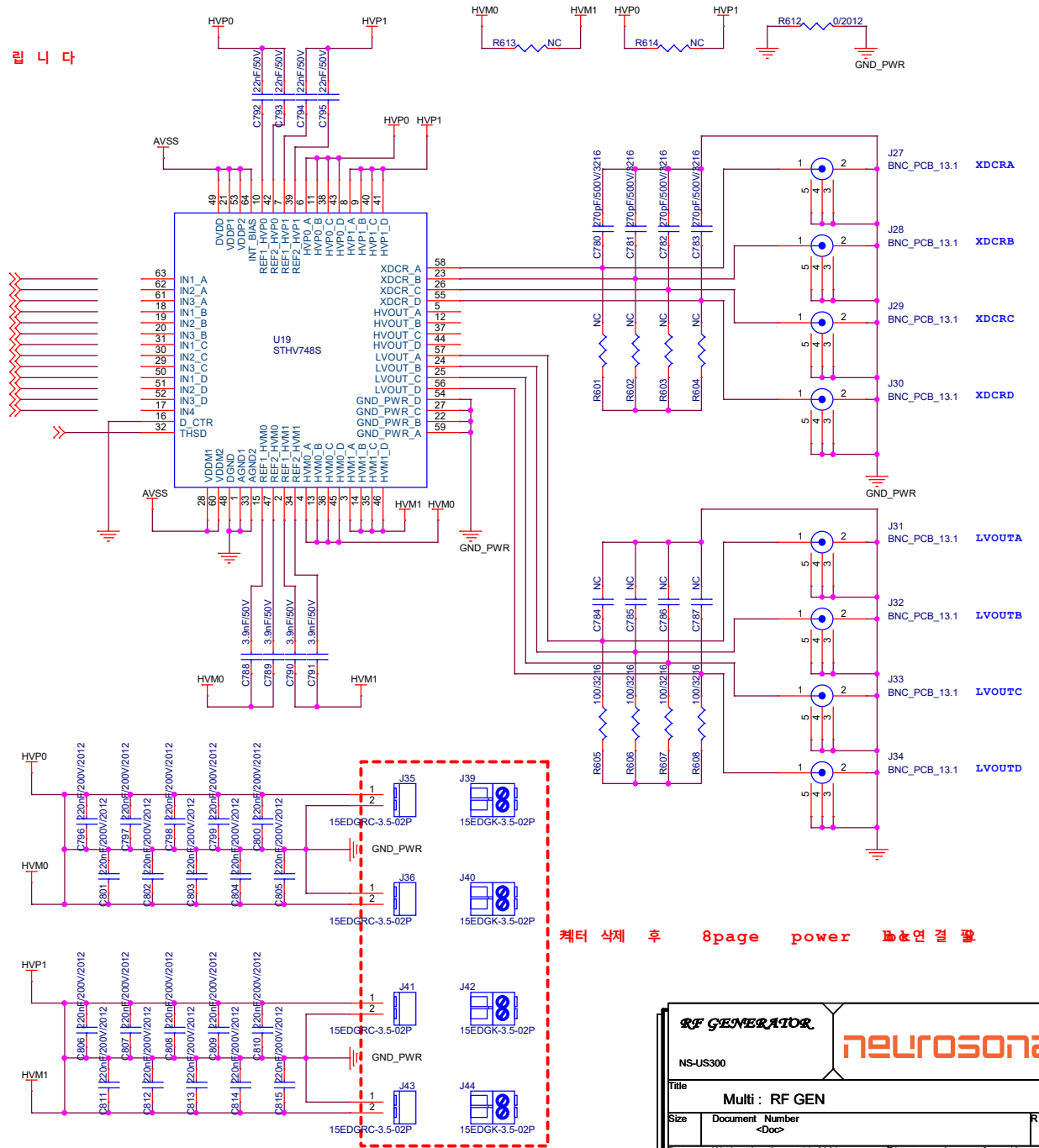
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7. 메인 결 박피머 앵 케터 분 지운 설계 되 는 파워블럭으로 연결 부탁드립니다

STHV748_0
STHV748_1
STHV748_2
STHV748_3
STHV748_4
STHV748_5
STHV748_6
STHV748_10
STHV748_11
STHV748_12
STHV748_13
STHV748_14
STHV748_15

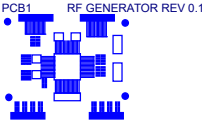
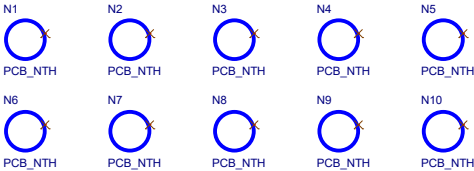
THSD



케터 삭제 후 8page power 블&연 결 팔

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NTH 3.5PI=>



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