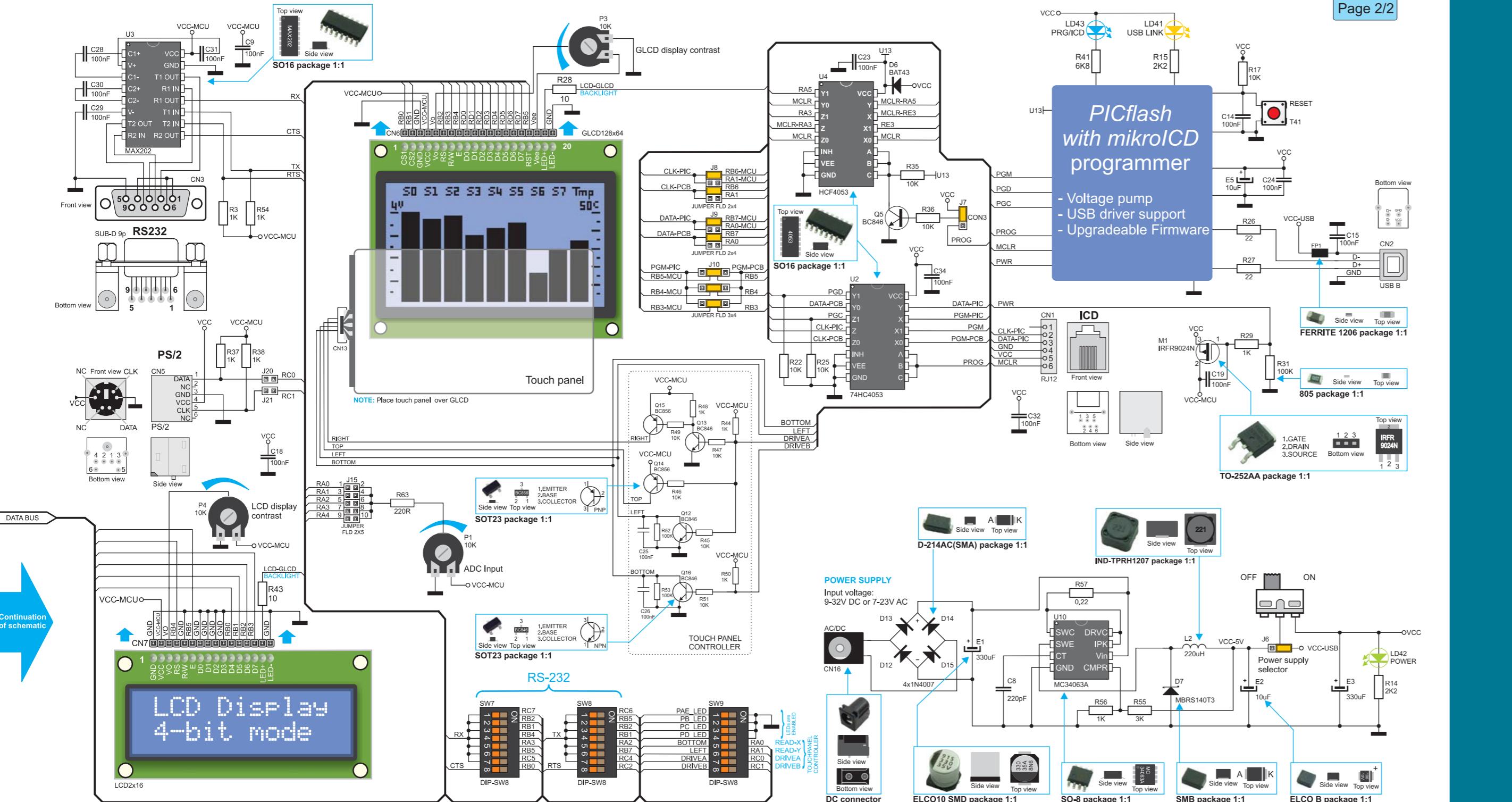


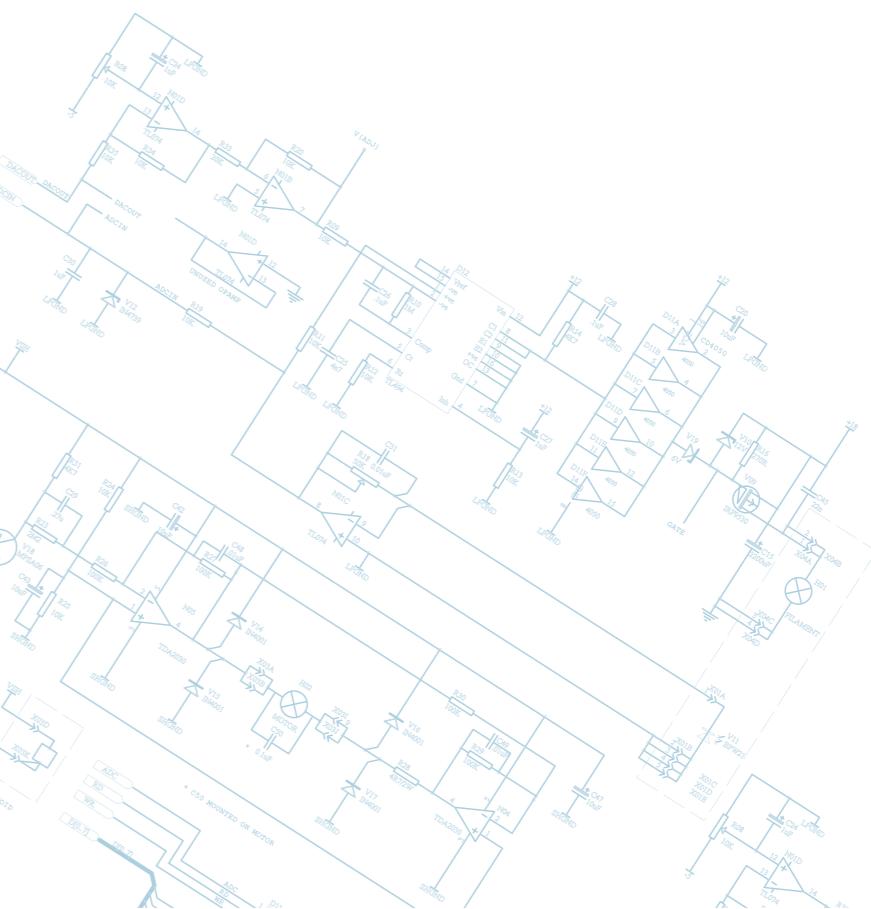
EasyPIC® 6

Schematic diagram

Development system

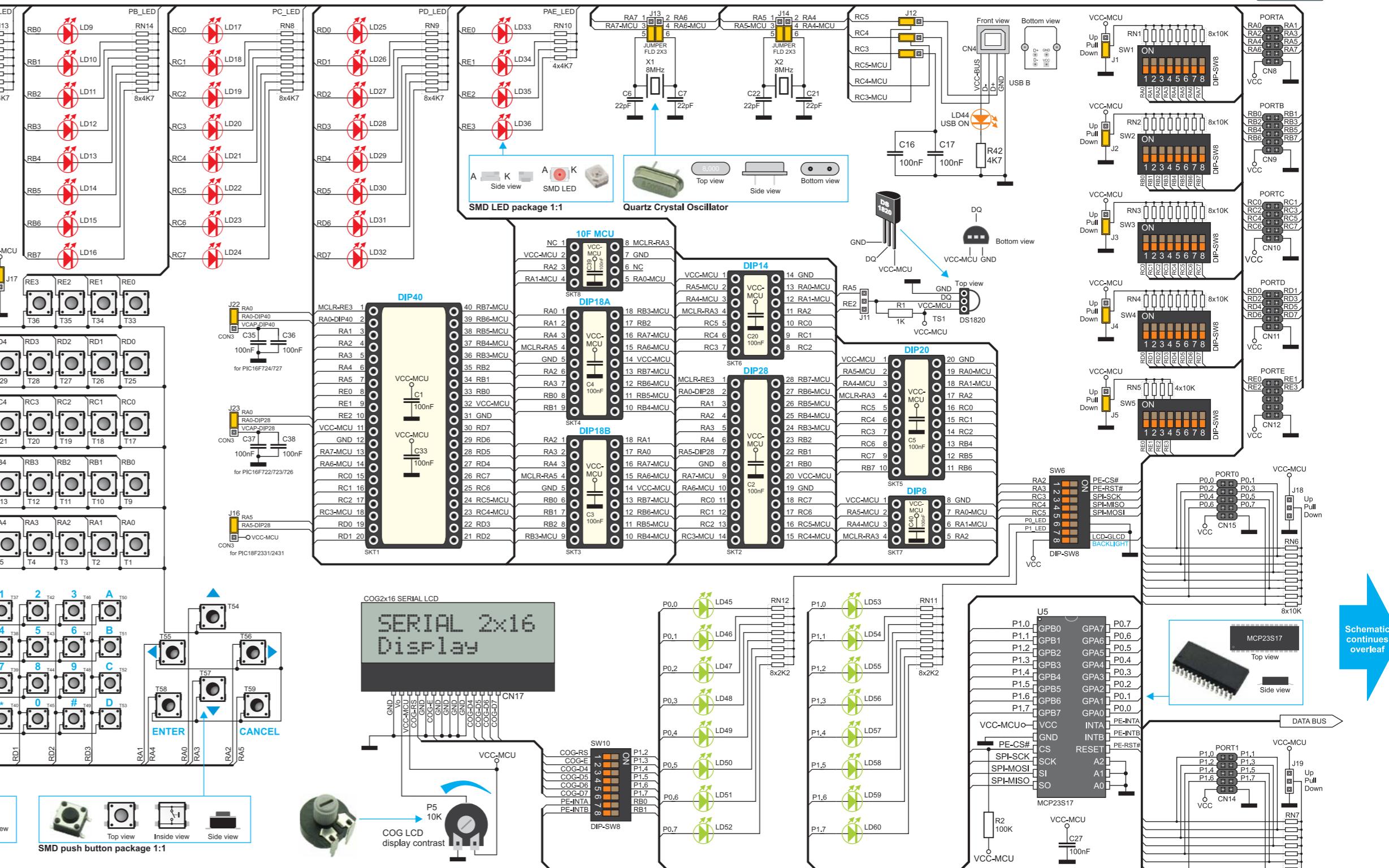


This document provides a electrical schematic of the EasyPIC6 development system. This device has been designed carefully and due attention has been paid to choosing components thereof. This is why the most important parts of the system are manufactured in SMD (Surface Mounting Device) technology. Besides, components are mounted by machines of the last generation using lead-free alloy for soldering. What we have now as a result is a high quality and reliable product that meets the world's highest standards being applied in electronics industry and environmental protection.



Built-in SMD component cases overview

Symbol	
1:1	Side view Top view
0805	
Symbol	
1:1	Side view Top view
0805	
ELCO10 SMD	
Symbol	
1:1	Side view Top view
ELCO B	
Symbol	
1:1	Side view Top view
Potentiometer	
Symbol	
1:1	Side view Top view
Ferrite 1206	
Symbol	
1:1	Side view Top view
IND-TPRH1207	
Symbol	
1:1	Side view Top view
IND 1812	
Symbol	
1:1	Side view Inside view Top view
PUSH BUTTON	
Symbol	
1:1	Side view Inside view Top view
Small push button	
Symbol	
1:1	Side view A K Side view A K Top view
SMD Quartz	
Symbol	
1:1	Side view A K Side view A K Top view
D-214AC(SMA)	
Symbol	
1:1	A K Side view A K Top view
SMD LED	
Symbol	
1:1	Side view A K Side view A K Top view
SMB	
Symbol	
1:1	Side view A K Side view A K Top view
MINIMELF	
Symbol	
1:1	Side view A K Side view A K Top view
SOT23	
Symbol	
1:1	1.EMITTER 2.BASE 3.COLLECTOR Side view
SOT23	
Symbol	
1:1	1.EMITTER 2.BASE 3.COLLECTOR Side view
TO-252AA	
Symbol	
1:1	1.GATE 2.DRAIN 3.SOURCE Side view Top view
SO8 - MC34063A	
Symbol	
1:1	Side view Top view
SO16 - 74HC4053 HCF4053	
Symbol	
1:1	Side view Top view
SOIC28 - MCP23S17	
J10 jumper positions	
Default position	RB5 RB4 RB3
RB5 connected to PGM	RB5 RB4 RB3
RB4 connected to PGM	RB5 RB4 RB3
RB3 connected to PGM	RB5 RB4 RB3
Socket selection	
Default	J8 J9
DIP40 DIP28 DIP18A DIP18B	J8 J9
DIP20 DIP14 DIP8	J8 J9
Oscillator settings	
J13	RA6, RA7 as OSC
DIP40 DIP28 DIP18A DIP18B	OSC I/O
DIP20 DIP14 DIP8	OSC I/O
J14	RA4, RA5 as OSC
DIP40 DIP28 DIP18A DIP18B	OSC I/O
DIP20 DIP14 DIP8	OSC I/O
Jumpers and selectors	
All pins are free	1 2 NO
Pins 1 and 2 are connected	1 2 Z
Pins 5, 6, 7 and 8 are ON	1 2 3 4
All pins are free	1 2 3
Pins 1 and 2 are connected while 2 and 3 are disconnected	1 2 3
DIP switches	
DIP-SW8	



Schematic continues overleaf