

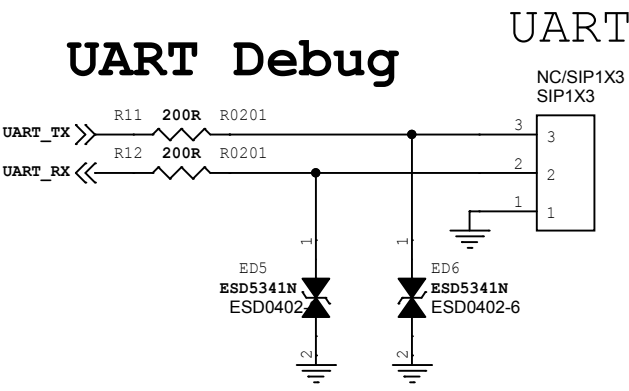
Shenzhen Xunlong Software Co., Ltd					
Project:	OPI_CM5_Base				
File:	02.RTC/IR/5V/3V3				
Date:	Wednesday, June 05, 2024		Rev:	V1.2	
Size:	A3	Sheet:	2	Of:	13

D

C

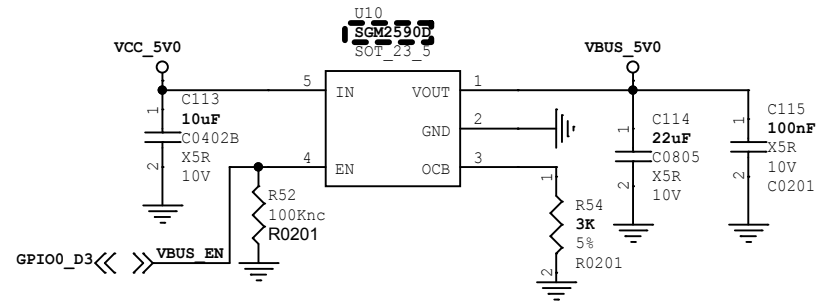
B

A

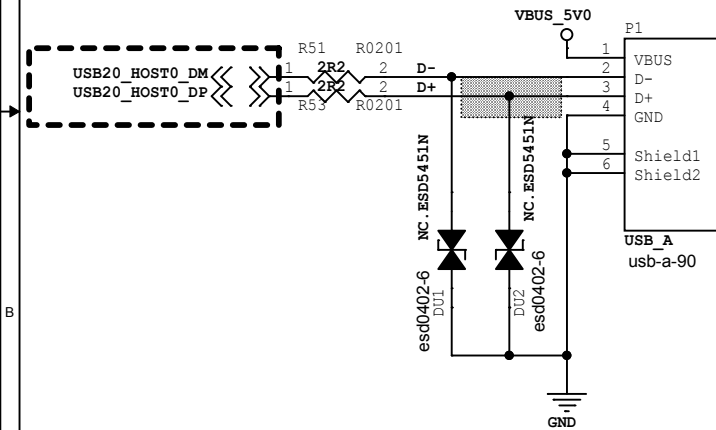


Shenzhen Xunlong Software Co., Ltd

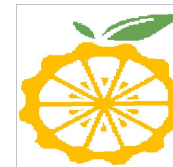
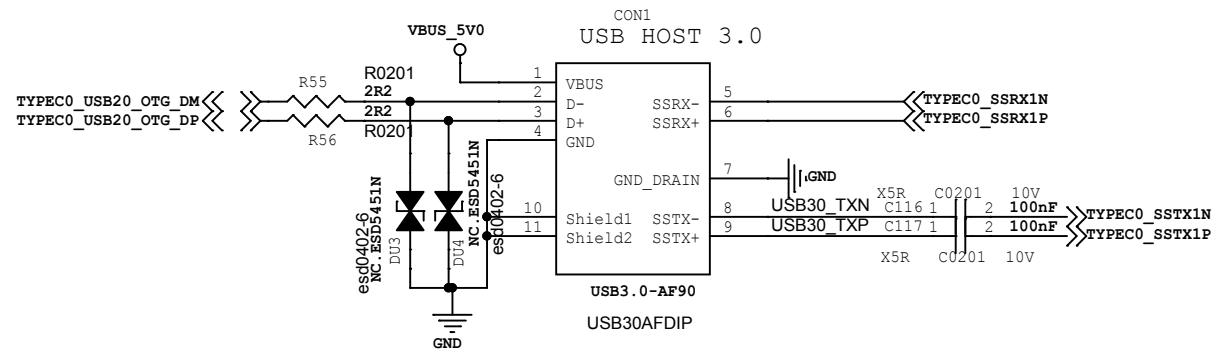
Project: OPI_CM5_Base					
File: 03. KEY/ Debug/LED/FAN					
Date: Wednesday, June 05, 2024			Rev: V1.2		
Size:	A3	Sheet:	3	Of:	13



## USB2.0 Type-A Port

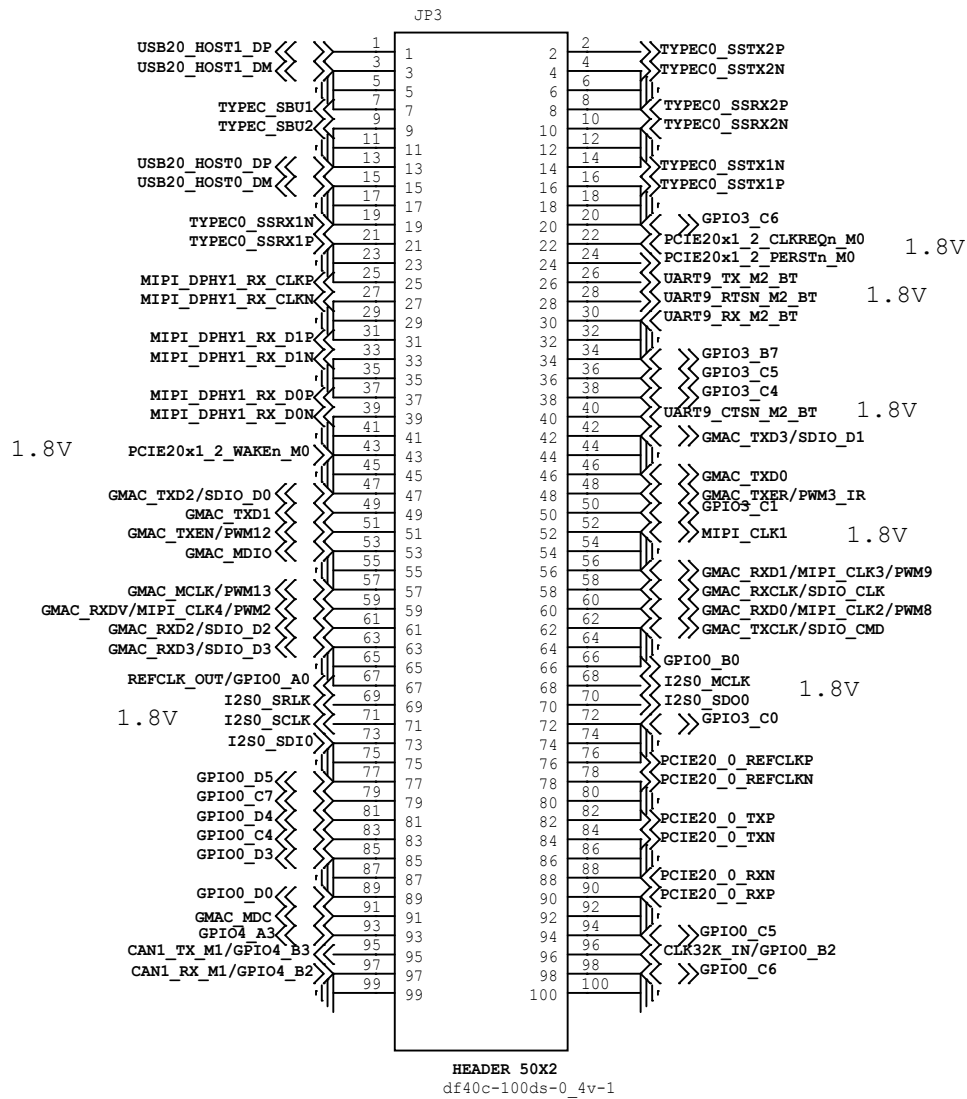


## USB3.0 HOST



**Shenzhen Xunlong Software Co., Ltd**

<b>Project:</b>	OPI_CM5_Base			
<b>File:</b>	05.USB20/USB30 HOST Port			
<b>Date:</b>	Wednesday, June 05, 2024	<b>Rev:</b>	V1.2	
<b>Size:</b>	A3	<b>Sheet:</b>	5	<b>Of:</b> 13



GPIO0 \*\*:1.8V  
GPIO1\_C0-C7、D0-D5:1.8V  
GPIO3 \*\*:1.8V  
GPIO4 \*\*:3.3V  
GMAC:1.8V  
I2S0:1.8V



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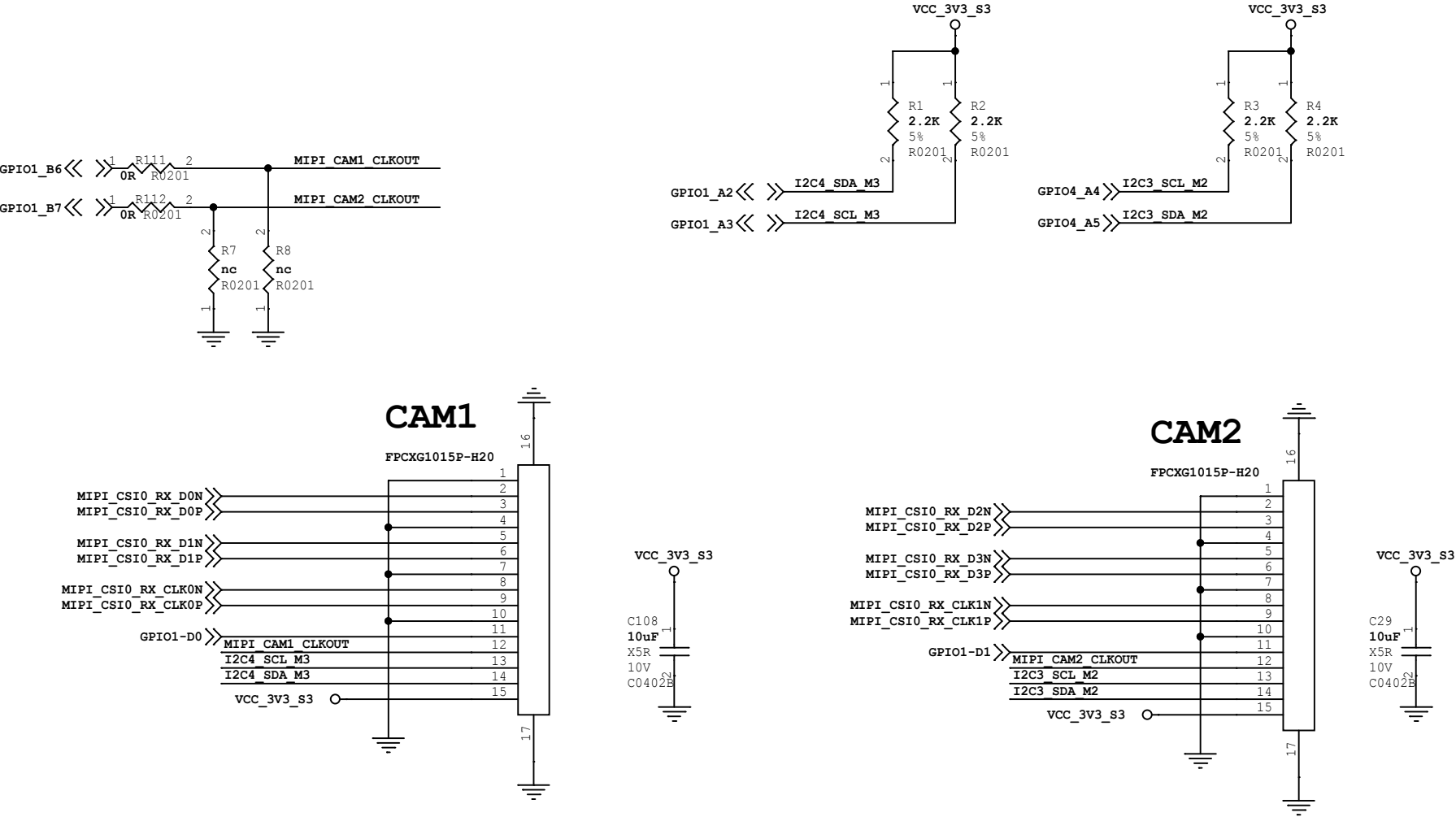
Project: OPI\_CM5\_Base

File: 10.CON\_JP3

Date: Wednesday, June 05, 2024 Rev: V1.2

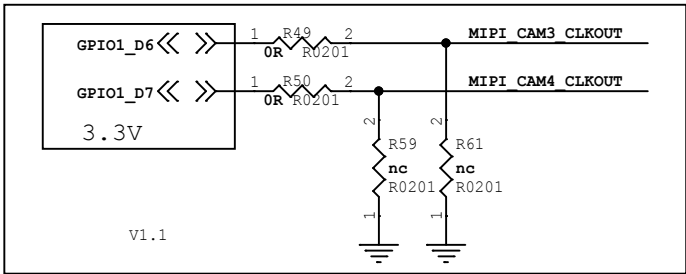
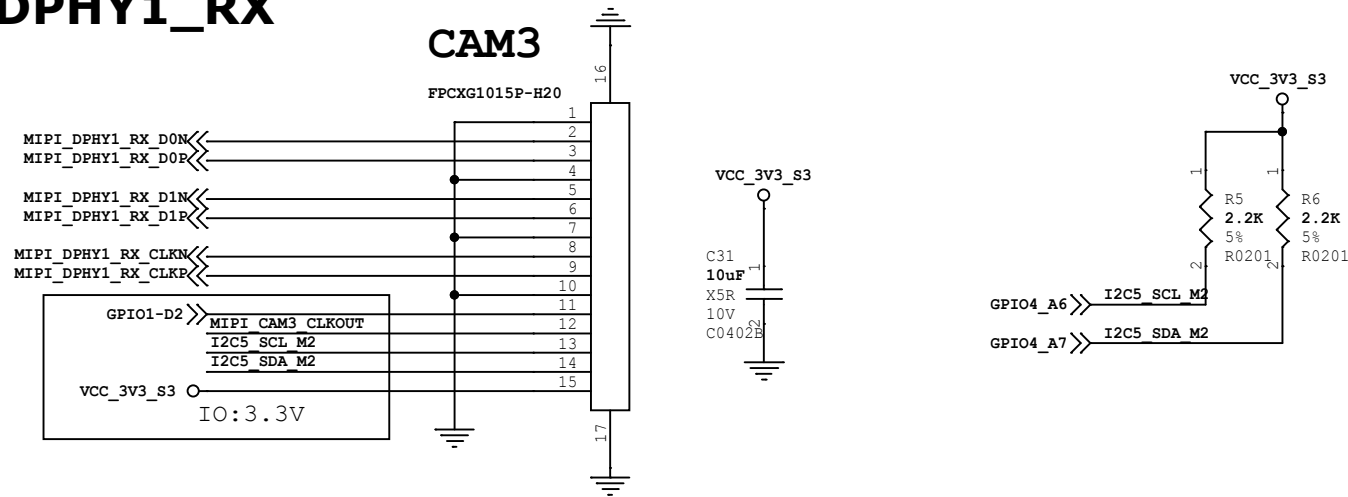
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MIPI-CSIO\_RX

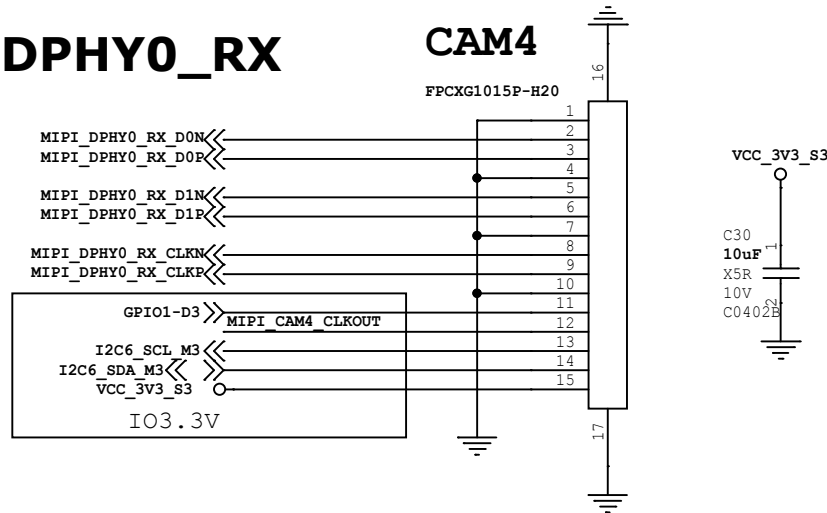


Shenzhen Xunlong Software Co., Ltd					
Project:	OPI_CM5_Base				
File:	11.VI-Cam MIPI CSI				
Date:	Wednesday, June 05, 2024	Rev:	V1.2		
Size:	A3	Sheet:	11	Of:	13

VI-Camera DPHY1\_RX



VI-Camera DPHY0\_RX



Shenzhen Xunlong Software Co., Ltd					
Project:	OPI_CM5_Base				
File:	12.VI-Cam MIPI-DPHY				
Date:	Wednesday, June 05, 2024	Rev:	V1.2		
Size:	A3	Sheet:	12	Of:	13