Description			OPi CM5 C	^OR	FI	P1 100PIN		
Commit (NY)	occription	din Namo						Description
Secretary Secr				_			Pin Name	
Second (97)								
Ground (BY)								
Packing								
Pasternig								
Ground (MY)								
Finding								
Fasting								
Reserved Person								
CROD Specially 2 337 CROSS C								
GRIOLAN SERVICE SERV			Reserved	19	20	Reserved		Floatimg
Ground (IV) GRO 19 23 M. 3 FORD 12 D. CHS 3 DAM MOLARET SEX MOSPAN MISS MUNICIPOL (2) C. MO TO Pipellar) a 3 J. M. 3 GRO 12 C. L. DELS SEMENTARY SEX MOSPAN MISS MUNICIPOL (2) C. MO TO Pipellar) a 3 J. M. 3 GRO 12 C. L. DELS SEMENTARY SEX MOSPAN MISS MUNICIPOL (2) C. MO TO Pipellar) a 3 J. M. 3 GRO 12 C. L. DELS SEX MOSPAN MISS MUNICIPOL (3) C. MOL PIPER SEX MOSPAN M	PIO Typically a 3 3V					a		Ground (0V)
GPO Typically a 33V, 33V or 18V PML 500L MINTER CSL MIGHTED LSB 0 GPOL 50 25 86 GPOL CL	<u></u> '							
GPO Typically a 33V 33V or 18V PomL SDE MILSDE MILS								GPIO Typically a 3.3V , 3.3V or 1.8V
GROL 32 1 7 2 8 GROL A. PRICK MAJERIOL R. D. D. A. D. CROWN MAJERIA C. S. D. PRICK MAJERIOL R. D. D. A. D. CROWN MAJERIA C. S. D. PRICK MAJERIA C. S. D	PIO Typically a 3.3V , 3.3V or 1.8V	DM1_SDI1_M1/SPI2_CS1_M0/GPIO1_B0_u	GPIO1_B0	25	26	GPIO1_C1	I2C3_SCL_M0/UART3_TX_M0/SPI4_MOSI_M0/GPIO1_C1_z	GPIO Typically a 3.3V , 3.3V or 1.8V
## CONTRICTOR STATE SMALL COLD MULLIART R.K. M.25 PRO MOST MULCHES MULLIART R.K. M.25 PRO MOST MULCHES MULLIART R.K. M.25 PRO MOST MULLIAR	PIO Typically a 3.3V , 3.3V or 1.8V	DM1_SDI2_M1/SPI0_MISO_M2/GPIO1_B1_d	GDIO1 R1	27	28	GDIO1 A2		GPIO Typically a 3.3V , 3.3V or 1.8V
GROUND (OF) GROUND	PIO Typically a 3.3V , 3.3V or 1.8V P	DM1_SDI3_M1/UART4_RX_M2/SPI0_MOSI_M2/GPIO1_B2_d					I2C4_SCL_M3/UART6_CTSN_M1/PWM1_M2/SPI4_CS0_M2/GPIO1	GPIO Typically a 3.3V , 3.3V or 1.8V
GROUT BY JOHN STATE AND A STREET STATE AND A STREAM	PO Tomically a 2 21/ 2 21/ and 01/		GF1O1_B2	23	30	GFIO1_A3		Current (MA)
GPIO Typically a 3.3V	PIO Typically a 3.3V , 3.3V or 1.8V	M2/GPIO1_B3_d						
SPAT SPERMY 8.3 N 14 MJGRIDL De LU CPRICE 6 70 Pict 18 70 Pict 18			GND	33	34	GPIO1_A4		GPIO Typically a 3.3V , 3.3V or 1.8V
GPIO Typically a 33V, 33V or 18V GPIOLATE TRANSPORCES MAZGROU B4 GPIOL B4 37 38 GPIOLAS GPIOLATE TRANSPORCES MAZGROU B4 GPIOLATE			GPIO1_D6	35	36	GPIO1_D7		GPIO Typically a 3.3V
GPIO Typically a 33.V , 33.V or 1.8V MIP, CAMERAZ, CLK, MOSPOPIC, TX, MOZCE, SCL, MAJANAN MIP, CAMERAZ, CLK, MOSPOPIC, TX, MOZCE, SCR, MAJANAN MIP, CAMERAZ, CLK, MOSPOPIC, TX, MOZCE, SCR, MAJANAN MIP, CAMERAZ, CLK, MAJ								GPIO Typically a 3.3V , 3.3V or 1.8V
GPIO Typically a 3.3V, 3.3V or 1.8V MIPL, CAMBREAL, CLIX, MIO/SPOIRE, TX, MO/JZCS, SCL, MIJALARTI CRIVING CRIV								GPIO Typically a 3.3V , 3.3V or 1.8V
Ground (00') 43	PIO Typically a 3 3V 3 3V or 1 8V	MIPI_CAMERA1_CLK_M0/SPDIF0_TX_M0/I2C5_SCL_M3/UART1_						
GRO 43 44 GPIOL AT GRO AS AS	T: T: Typically a 5.5V , 5.5V or 1.6V	X_M1/GPIO1_B6_u	GPIO1_B6	41	42	GND		Ground (04)
GPIO Typically a 3.3V 3.3V or 1.8V CPIO Typically a 3.3V 3.3V or 1.8V PMMC SILEPALGPIOD C 2 GPIO Typically a 3.3V 3.3V or 1.8V PMMC SILEPALGPIOD C 2 GPIO Typically a 3.3V 3.3V or 1.8V PMMC SILEPALGPIOD C 2 GPIO Typically a 3.3V GPIO Typically a 3.3V 3.3V or 1.8V PMMC SILEPALGPIOD C 2 GPIO Typically a 3.3V GPIO Typically a 3.3V 3.3V or 1.8V PMMC SILEPALGPIOD C 2 GPIO Typically a 3.3V GPIO Typic	round (0V)						PDM1_SDI0_M1/PCIE20X1_1_PERSTN_M2/PWM3_IR_M3/SPI2_CS	GPIO Typically a 3.3V , 3.3V or 1.8V
SPOL Typically a 3.3V 3.3V or 1.8V POMD_SDD_MO/SPIL_CSI_MZ/GPIO_E.d GPIO_E 49 50 GPIO_DD 250_SDD/JZCT_SCI_MD/JARTE_RX_MZ/GPIO_E.d GPIO_Typically a 3.3V GPIO_Typically a 3.3V A3.3V or 1.8V POMD_SDD_MO/SPIL_CSI_MZ/GPIO_LD_d GPIO_Typically a 3.3V GPIO_Typically a 3.3V A3.3V or 1.8V POMD_SDD_MO/SPIL_CSI_MZ/GPIO_LD_d GPIO_Typically a 3.3V GPIO_Typically a 3.3V A3.3V or 1.8V POMD_SDD_MO/SPIL_CSI_MZ/GPIO_LD_d GPIO_Typically a 3.3V GPIO_Typically a 3.3V GPIO_Typically a 3.3V GPIO_Typically a 3.3V A3.3V or 1.8V POMD_SDD_MO/SPIL_CSI_MZ/GPIO_LD_d GPIO_Typically a 3.3V GPIO_Typically a 3.3V GPIO_Typically a 3.3V GPIO_Typically a 3.3V A3.3V or 1.8V POMD_SDD_MO/TAG_TKK_MI/ZC3_SCI_MA/JAR GPIO_Typically a 3.3V GPIO_Typically a 3.3V A3.3V or 1.8V POMD_SDD_MO/TAG_TKK_MI/ZC3_SCI_MA/JAR GPIO_Typically a 3.3V A3.3V or 1.8V A3	odila (ov)		GND	43	44	GPIO1_A7		GPIO Typically a 5.5V , 5.5V OI 1.8V
GPIO Typically a 3.3V , 3.3V or 1.8V PMIC SIEEPR/SPIOC C2 47 48 GPIO 1.05 49 50 GPIO 1.05 MIC SIEEPR/SPIO C2 47 48 GPIO 1.05 MIC SIEEPR/SPIO MIC SIEEPR/SP	DIO Tunically a 2 21/ 2 21/ as 1 91/	/IPI_CAMERA2_CLK_M0/SPDIF1_TX_M0/SATA2_ACT_LED_M1/I					PDM0_CLK1_M0/I2C2_SDA_M3/PWM11_IR_M2/SPI4_CS1_M0/GPI	CDIO Turrically a 2 2V 2 2V as 1 9V
GPIO Typically a 3.3V 3.3V or 1.8V PDM0_SDID_MO/SPIL_CSI_M2/GPIO1_D5_d GPIO1_D5 49	20 Typically a 3.3V , 3.3V or 1.8V	C5 SDA M3/UART1 RX M1/PWM13 M2/GPIO1 B7 u	GPIO1_B7	45	46	GPIO1_C4	O1 C4 d	GPIO Typically a 3.3V , 3.3V or 1.8V
SPIO Typically a 3.3V , 3.3V or 1.8V POMO_SDID_MO/SPIL_CSL_MZ/GPIO1_DS_d	PIO Typically a 3.3V , 3.3V or 1.8V P	'MIC_SLEEP4/GPIO0_C2_d	GPIO0_C2	47	48	GPIO1_C6	PDM0_CLK0_M0/I2C4_SDA_M4/PWM15_IR_M2/GPIO1_C6_d	GPIO Typically a 3.3V , 3.3V or 1.8V
SPIC Typically a 3.3V 3.3V or 1.8V SDMMC_DIPPML_SDIZ_MOVINGS_TISS_MVI/ZC3_SDA_MA/UA NATZ_RX SDMMC_DIPPML_SDIZ_MOVINGS_TISS_MVI/ZC3_SDA_MA/UA NATZ_RX SDMMC_DIPPML_SDIZ_MOVINGS_TISS_MVI/ZC3_SDA_MA/UA NATZ_RX SDMMC_DIPPML_SDIZ_MOVINGS_TISS_MVI/ZC3_SCL_MA/UAR NATZ_RX SDMMC_DIPPML_SDIZ_MOVINGS_TISS_MVI/ZC3_SDA_MA/UA NATZ_RX SDMMC_DIPPML_SDIZ_MOVINGS_TISS_MVI/ZC3_SDA_MA/UA SDMMC_DIPPML_SDIZ_MOVINGS_TISS_MVI/ZC3_SDA_MA/UAR NATZ_RX SDMMC_DIPPML_SDIZ_MOVINGS_TISS_MVI/Z		DAMO CDIO MO (CDI1 CC1 MO (CDIO1 DE J					I2S0_SDO1/I2C7_SCL_M0/UART6_TX_M2/SPI1_MISO_M2/GPIO1_	
Cornor (0V) Corno (0V) Corno (0V) Co	** *		GPIO1_D5	49	50	GPIO1_D0		GPIO Typically a 3.3V , 3.3V or 1.8V
Ground (DV) S S S S S S S S S	PIC) Evnically a 3.3V 3.3V or 1.8V I		UART2_RX	51	52	GND		Ground (0V)
SpMMC_D0/PDM1_SDI3_M0//TAG_TCK_M1/JC2S_SCL_M4/JAR UART2_TX T2_TX_M1/PWM3_M1/GPIQ4_D0_u UART2_TX T2_TX_M1/PWM3_M1/GPIQ4_D0_u UART2_TX T2_TX_M1/PWM3_M1/GPIQ4_D0_u UART2_TX UART2_TX SpMMC_CLK/PDM1_CLK0_M0/FEST_CLKOUT_M0/MCU_JTAG_T SpMMC_CLK/PDM1_CLK0_M0/FEST_CLKOUT_M0/MCU_JTAG_T SpMMC_D1_PDM1_SDI3_M0/JC2C_SDA_M4/JART3_RX_M0/PDM1_CLK0_M0/FEST_CLKOUT_M0/MCU_JTAG_T SpMMC_D1_PDM1_SDI3_M0/JC2C_SDA_M4/JART3_RX_M0/PDM1_CLK_M0/FCQ_D3_d SpMMC_D3_PDM1_SDI3_M0/JTAG_TCK_M1/JC2S_SDA_M0/JAR SpMMC_D3_PDM1_SDI3_M0/JTAG_TCK_M1/JC2S_SCL_M4/JAR SpMMC_D3_FDM1_SDI3_M0/JTAG_TCK_M1/JC2S_SCL_M4/JAR SpMMC_D3_FDM1_SDI3_M0/JTAG_TCK_M1/JC2S_SCL_M4/JAR SpMMC_D3_FDM1_SDI3_M0/JTAG_TCK_M1/JC2S_SCL_M4/JAR SpMMC_D3_FDM1_SDI3_M0/JTAG_TCK_M1/JC2S_SCL_M4/JAR SpMMC_D3_FDM1_SDI3_M0/JTAG_TCK_M1/JC2S_SCL_M4/JAR SpMMC_D3_FDM1_SDI3_M0/JTAG_TCK_M1/JC2S_SDA_M4/JAR SpMMC_D3_FDM1_SDI3_M0/JTAG_TCK_M1/JC2S_SDA_M4/JAR SpMMC_D3_FDM1_SDI3_M0/JTAG_TCK_M1/JC2S_SDA_M4/JAR SpMMC_D3_FDM1_SDI3_M0/JTAG_TCK_M1/JC2S_SDA_M4/JAR SpMMC_D3_FDM1_SDI3_M0/JTAG_TCK_M1/JC2S_SDA_M4/JAR SpMMC_D3_FDM1_SDI3_M0/JTAG_TCK_M1/JC2S_SDA_M4/JAR SpMMC_D3_FDM1_SDI3_M0/JTAG_TCK_M1/JC2S_SDA_M4/JAR SpMMC_D3_FDM1_SDI3_M0/JTAG_TCK_M0/JC2S_SCL_M0/JAR SpMMC_D3_FDM1_SDI3_M0/JTAG_TCK_M0/JAC SpMMC_D3_FDM1_SDI3_M0/JAG_TCK_M0/JAC Sp		0						GPIO Typically a 3.3V , 3.3V or 1.8V
SPCARD CLK signal SDMMC_CLK/PDM1_CLKOW MO/TEST_CLKOUT_MO/MCU_TAG_T SDMMC_CLK/PDM1_CLK (MMO/TEST_CLKOUT_MO/MCU_TAG_T SDMMC_CDM_PDM1_SDM_MO/TAG_T SDMMC_DDM1_SDM_MO/TAG_T	S S		GND	33	34	G1101_D1		CT C T
SDCARD CLK signal SDMMC CLK/PDM1_CLKD_MO/TEST_CLKOUT_MO/MOU_JTAG_TT MS_MO/CAND_RX_MIALARTS_TX_MO/PDQ4_DS_d SDMMC_CLK S7 S8 GPIO1_D3 MI_/SPI1_CSO_MZ/GPIO1_D3_d GRUD			UART2 TX	55	56	GPIO1 D2		GPIO Typically a 3.3V , 3.3V or 1.8V
MS_MO/CANO_RX_MI/UARTS_TX_MO/GPI04_DS_d SDMMC_CLK S7 S8 GPI01_D3 MI/SPIL_CSO_MZ/GPI01_D3_d SPI0_I/IDICAL SPI0_	S		<u>-</u>					
GROUN SDMMC_D3/PDM1_SDI0_M0/JTAG_TMS_M0/J2C8_SDA_M0/JA SDMMC_D3 SDMMC_D3/PDM1_SDI0_M0/JTAG_TMS_M0/J2C8_SDA_M0/JA SDMMC_D3 SDMMC_D3/PDM1_SDI3_M0/JTAG_TCK_M1/J2C3_SCL_M4/JAR SDMMC_D3 SDMMC_D3/PDM1_SDI3_M0/JTAG_TCK_M1/J2C3_SCL_M4/JAR SDMMC_D3/PDM1_SDI3_M0/JTAG_TCK_M1/J2C3_SCL_M4/JAR SDMMC_D0 S3 64 Reserved SDMMC_D1/PDM1_CLK1_M0/MCU_JTAG_TCK_M0/CAND_TX.M SDCARD CMD signal SDMMC_D1/PDM1_SDI3_M0/JTAG_TCK_M1/J2C3_SDA_M4/JAR SDMMC_D1/PDM1_SDI3_M0/JTAG_TTS_M1/J2C3_SDA_M4/JA RT2_KX_M1/PWM9_M1/JGPI04_D1_U SDMMC_D1/PDM1_SDI2_M0/JTAG_TTS_M1/J2C3_SDA_M4/JA RT2_KX_M1/PWM9_M1/JGPI04_D1_U SDMMC_D1/PDM1_SDI2_M0/JTAG_TCK_M0/J2C8_SCL_M0/JAR TT2_KX_M1/PWM9_M1/JGPI04_D1_U SDMMC_D1/PDM1_SDI1_M0/JTAG_TCK_M0/J2C8_SCL_M0/JAR TT2_KX_M1/PWM9_M1/JGPI04_D1_U SDMMC_D1/PDM1_SDI1_M0/JTAG_TCK_M0/J2C8_SCL_M0/JAR TT2_KX_M1/PWM9_M1/JGPI04_D1_U SDMMC_D1/PDM1_SDI1_M0/JTAG_TCK_M0/J2C8_SCL_M0/JAR TT2_KX_M1/PWM9_M1/JGPI04_D1_U SDMMC_D1 SDMMC_D1 SDMMC_D1/PDM1_SDI1_M0/JTAG_TCK_M0/J2C8_SCL_M0/JAR TT2_KX_M1/PWM9_M1/JGPI04_D1_U SDMMC_D1 SDMM			SDMMC CLK	57	58	GPIO1 D3		GPIO Typically a 3.3V , 3.3V or 1.8V
SDCARD Data3 signal SDMMC_D3/PDMI_SDID_M0/JTAG_TCK_M0/JZC8_SDA_M0/JA_								Ground (0V)
SDCARD Data0 signal SDMMC_D0/PDM1_SDI3_M0//TAG_TCK_M1/2G3_SCL_M4/UAR SDMMC_D0/PDM1_SDI3_M0//TAG_TCK_M1/2G3_SCL_M4/UAR SDMMC_D0/PDM1_SDI3_M0//TAG_TCK_M1/2G3_SCL_M4/UAR SDMMC_D0/PDM1_SDI3_M0//TAG_TCK_M1/2G3_SCL_M4/UAR SDMMC_D0/PDM1_SDI3_M0//TAG_TCK_M1/2G3_SCL_M4/UAR SDMMC_D0/PDM1_SDI3_M0//TAG_TCK_M1/2G3_SDA_M4/UAR SDMMC_D1/PDM1_SDI2_M0//TAG_TMS_M1/2G3_SDA_M4/UAR SDMMC_D1/PDM1_SDI2_M0//TAG_TMS_M1/2G3_SDA_M4/UAR SDMMC_D1/PDM1_SDI2_M0//TAG_TMS_M1/2G3_SDA_M4/UAR SDMMC_D1/PDM1_SDI2_M0//TAG_TCK_M0/2C8_SCL_M0/UAR SDMMC_D1 SDMMC	SI		0.15	33	- 00	0.12	SDMMC CMD/PDM1 CLK1 M0/MCU ITAG TCK M0/CAN0 TX M	
SDMANC_DO/PDM1_SDI2_MO/JTAG_TCK_M1/J2C3_SCL_M4/UAR SDMMC_DO 63 64 Reserved SDMMC_DO 63 64 Reserved SDMMC_DO SDCARD Data1 signal SDMMC_DI/PDM1_SDI2_MO/JTAG_TMS_M1/J2C3_SDA_M4/UA SDCARD Data1 signal SDMMC_DI/PDM1_SDI2_MO/JTAG_TMS_M1/J2C3_SDA_M4/UA SDMMC_DI/PDM1_SDI2_MO/JTAG_TCK_M0/J2C8_SCL_M0/UAR SDMMC_D2 SDMMC_D2/PDM1_SDI2_MO/JTAG_TCK_M0/J2C8_SCL_M0/UAR SDMMC_D2 SDMMC_D2/PDM1_SDI2_MO/JTAG_TCK_M0/J2C8_SCL_M0/UAR SDMMC_D2 SDMMC_D2 SDMMC_D2 SDMMC_D2/PDM1_SDI2_MO/JTAG_TCK_M0/J2C8_SCL_M0/UAR SDMMC_D2 SDMM			SDWWC D3	61	62	SDMMC CMD		SDCARD CMD signal
SDMAC Data3 signal T2_TX_MI/PWM8_MI/GPIO4_D0_u SDMMC_D0 GROD GS 64 Meserved Ground (0V)			3DIVIIVIC_D3	01	02	3DIVIIVIC_CIVID	I/OARTS_RX_WU/PWWI/_IR_WII/GPIO4_D4_U	
Ground (0V) GND GND GS GND Ground (0V) SDCARD Data1 signal SDMMC_D1/PDM1_SDI2_M0/JTAG_TMS_M1/J2C3_SDA_M4/UA SDMMC_D1 GF G8 SARADC_IN1 SARADC_IN1 SARADC_IN1 SARADC_IN1 SARADC_IN1 SECOVERY signal , In up via 10K to1.8V			CDMMC DO	63	C A	Reserved		Do not Connect anything to this pin.
SDCARD Datal signal SDMMC_DI/PDMI_SDI2_M0/JTAG_TMS_M1/I2C3_SDA_M4/UA RT2_RX_MI/PWM9_M1/CPIO4_D1_u SDMMC_D1 SDMMC_D1 SDMMC_D2/PDMI_SDI1_M0/JTAG_TCK_M0/J2C8_SCL_M0/UAR SDMMC_D2 Ground (0V) SDMMC_D2 GROUND T5_CTSN_M0/GPIO4_D2_u SDMMC_D2 GROUND T5_CTSN_M0/GPIO4_D2_u SDMMC_DET GRION_A4 SDMMC_DET/GRION_A4_u						CND		
SDCARD Data1 signal RT2_RX_MI/PWM9_M1/GPIO4_D1_u SDMMC_D1 SDMMC_D2/PDM1_SDI1_M0/JTAG_TCK_M0/I2C8_SCL_M0/UAR SDMMC_D2 Ground (0V) Do not Connect anything to this pin. Output to Power switch for the SDCARD GPIO4_B4 For 3.3V GPIO. 4.75V-5.25V Main power input			מאט	05	00	עווט		
SDCARD Data2 signal SDMMC_D2/PDMI_SDI_M0/JTAG_TCK_M0/J2C8_SCL_M0/UAR T5_CTSN_M0/GPIO4_D2_u SDMMC_D2 69 70 Reserved Floating Do not Connect anything to this pin. Output to Power switch for the SDCARD GPIO4_B4 For 3.3V GPIO. A.75V-5.25V Main power input Main po	DCARD Data Esignal		CDMMC D1	67	co	CADADC TAIT	SARADC_IN1	RECOVERY signal , Internally pulled
SDMAC_D2 69 70 Reserved Ploating Ground (OV) Do not Connect anything to this pin. Output to Power switch for the SDCARD GPIO_A4 For 3.3V GPIO. A.75V-5.25V Main power input Main power input	- R		2DWWC_DJ	6/	68	SAKADC_IN1		up via 10K to1.8V
Ground (0V) GND TO NOT Connect anything to this pin. GROND GND TO TO Reserved GND TO TO RESERVED GROUND TO TO TO THE SDCARD GPIO0_AA_ U SDMMC_DET/GPIO0_AA_ U SDMMC_DET/GPIO			coc. n -			Reserved		Floatimg
Do not Connect anything to this pin. Output to Power switch for the SDCARD GPIO4_B4 For 3.3V GPIO. A.75V-5.25V Main power input Main po								-
Output to Power switch for the SDCARD GPIO_A4_U SDMMC_PWREN/PMU_DEBUG/GPIO_A5_d SDMMC_PWREN 75 76 GPIO_A4_U SDMMC_DET/GPIO_A4_U SDMMC_DET/GPIO_A4_U SDMMC_DET/GPIO_A4_U SDMMC_DET/GPIO_A4_For 1.8V GPIO_A4, For 1.8V GPIO_A4,								
SDMMC_PWREN/PMU_DEBUG/GPIOU_AS_d SDMMC_PWREN/PMU_DEBUG/GPIOU_AS_d SDMMC_PWREN 75 76 GPIOU_A4 GPIOU_A4_I GPI	o not Connect anything to this pin.		Reserved	73	74	GND		Ground (0V)
4.75V-5.25V Main power input							SDMMC_DET/GPIO0_A4_u	SDMMC0_DET GPIO0_A4,For 1.8V GPIO.
4.75V-5.25V Main power input			SDMMC_PWREN	75	76	GPIO0_A4		
4.75V-5.25V Main power input	75V-5.25V Main power input	Main power input	VBUS	77	78	GPIO_VREF	POWER In,3.3V for 3.3V GPIO,1.8V For 1.8V GPIO	Power for 3.3V GPIO or for 1.8V GPIO.
4.75V-5.25V Main power input	75V-5.25V Main power input	Aain power input					ACT_LED_M0/DDRPHY_CH2_DTB1/I2C6_SCL_M3/UART8_RX_M0/	IIC Clock pin
4.75V-5.25V Main power input	+			, 5	-00			
VBUS 81 82 IZC6_SDA_M3 2C6_SDA_M3/QAR18_1X_M0/SPI2_CS1_M1/GPI04_B0_d GPI04_B0, for 3.3V 4.75V-5.25V Main power input Main power input POWER VCC_3V Out 3.3V +/-2.5% Power input	75V-5 25V Main power input	Main nower input						
4.75V-5.25V Main power input Main power input Main power input Main power input POWER VCC_3V Out 3.3V +/-2.5% Power 300m A nor pin for a	750 5.250 Main power input	ium power input	VRUS	21	82	12C6 SDA M3	2C6_SDA_M3/UART8_TX_M0/SPI2_CS1_M1/GPIO4_B0_d	GPIO4_B0, for 3.3V
4.75V-5.25V Main power input Main power input POWER VCC_3V Out	+		*203	01	02	TECO_3DY_INI3		
	7EV E 2EV Main noveminant	Asin newer input					DOMER VCC 2V Out	3.3V +/-2.5% Power Output max
NRO2 83 84 ACC_3A3_23	/ov-o.25v Main power input	nain power input	VIDUE	0.2		VCC 2V2 C2	POWER VCC_3V OUT	300mA per pin for a total of 600mA
			ARO2	83	84	VCC_3V3_S3		1
3 3V +/-2 5% Power								3.3V +/-2.5% Power Output max
4.75v-5.25v Main power input Main power input Main power input Main power input 300mA per pin for a	75V-5.25V Main power input	Main power input					POWER VCC_3V Out	300mA per pin for a total of 600mA
VBUS 85 86 VCC_3V3_S3 300mA per pin for a			VBUS	85	86	VCC_3V3_S3		seems per pin for a total of oddillA.
19V / 259 Paules								1.8V +/-2.5% Power Output max
4.75V-5.25V Main power input Main power input Main power input POWER VCC_3V Out 200m A nor nin for a	75V-5.25V Main power input						POWER VCC_3V Out	300mA per pin for a total of 600mA.
VBUS 87 88 VCC_1V8_S3			VBUS	87	88	VCC_1V8_S3		Journa per pin for a total of 600mA.

Floatimg		Reserved	89	90	VCC_1V8_S3	POWER VCC_3V Out	1.8V +/-2.5% Power Output max 300mA per pin for a total of 600mA.
Floatimg		Reserved	91	92	PMIC_RESET_L		Bidirectional pin. Can be driven low to Reset the CM5 CPU.
A low on this pin forces booting , Internally pulled up via IO to1.8V	SARADC_IN0_BOOT	SARADC_IN0	93	94	SARADC_IN3	SARADC_IN3	12-bit ADC in signal,MAX 1.8V
	BT1120_D12/SATA0_ACT_LED_M0/DDRPHY_CH3_DTB2/I2C5_S CL_M1/PWM13_M1/SPI3_MOSI_M1/GPIO4_B6_d	GPIO4_B6	95	96	SARADC_IN2	SARADC_IN2	12-bit ADC in signal,MAX 1.8V
	CIF_D7/BT1120_D7/I2S1_SDI2_M0/DDRPHY_CH1_DTB3/I2C5_S DA_M2/SPI2_CS0_M1/GPIO4_A7_d	GPIO4_A7	97	98	GND		Ground (0V)
Input. Drive low to power off or power on ,CM5. Internally pulled up with a 45K to VBUS	POWER KEY(PWRON_L To GND).	PWRON_L	99	100		CIF_D5/BT1120_D5/I2S1_SDI0_M0/DDRPHY_CH1_DTB_1/I2C3_SD A_M2/UART3_TX_M2/SPI2_MOSI_M1/GPI04_A5_d	GPIO4_A5, for 3.3V

o VBUS		PWRON_L	99	100	GPIO4_A5		GPIO4_A5, for 3.3V
		OPi CM5 (COR	Œ,	IP2 100PIN		
escription		Signal	PIN	NO.	Signal		Description
-	TVDECO LICENO OTO ID					PCIE20X1_1_CLKREQN_M2/DP0_HPDIN_M2/I2C2_SDA_M4/UART	PCIE_CLKREQN
PEC0_USB20_OTG_ID	TYPECO_USB2O_OTG_ID	TYPEC0_USB20_OTG_ID	1	2	GPIO1_A0	6_RX_M1/SPI4_MISO_M2/GPIO1_A0_d	GPIO1_A0 for 3.3V
	TYPECO_USB2O_OTG_DM					PCIE20X1_1_WAKEN_M2/I2C2_SCL_M4/UART6_TX_M1/SPI4_MOS	PCIE_WAKEN
PEC0_USB20_OTG_DM	TTPECU_USB2U_UTG_DIM	TYPEC0_USB20_OTG_DM	3	4	GPIO1_A1	I_M2/GPIO1_A1_d	GPIO1_A1 for 3.3V
(DECO LICENSO OTC DD	TYPECO_USB2O_OTG_DP		-		GPIO4_A6	CIF_D6/BT1120_D6/I2S1_SDI1_M0/DDRPHY_CH1_DTB2/I2C5_SCL	GPIO4_A6 for 3.3V
/PEC0_USB20_OTG_DP	11211 121 12	TYPEC0_USB20_OTG_DP	7	6 8	GND	_M2/UART3_RX_M2/SPI2_CLK_M1/GPIO4_A6_d	
round (0V) CIE20_PERSTn_3V3_L	CUE DO OTTAGO DO COSA LIBERA NA DESCENDA A DESCENDA DA	GND	/	8	PCIE20_2_REFCL		Ground (0V)
	CIF_D2/BT1120_D2/I2S1_LRCK_M0/PCIE20X1_1_PERSTN_M1/D DRPHY_CH0_DTB2/SPI0_CLK_M1/GPIO4_A2_d	GPIO4_A2	9	10		PCIE20_2_REFCLKP	DCIE30 3 DEECLED
PIO4_A2 for 3.3V	DRFTTT_CTIO_DTB2/3F10_CERC_W11/GF104_A2_d		3	10	PCIE20_2_REFCL		PCIE20_2_REFCLKP,
patimg		Reserved	11	12		PCIE20_2_REFCLKN	DCIESO S DEECLAN
d (010		GND	13		GND		PCIE20_2_REFCLKN
ound (0V)		GND	13	14	PCIE20_2_RXP/S		Ground (0V) PCIE20_2_RXP
IPI_CSI0_RX_D0N	MIPI_CSI0_D0N	MIPI_CSI0_D0N	15	16		PCIE20_2_RXP/SATA30_2_RXP/USB30_2_SSRXP	SATA30_2_RXP
PI_C3IO_RX_DOIN		IVITET_C310_D0IN	13	10	PCIE20_2_RXN/S		PCIE20_2_RXN
IPI_CSI0_RX_D0P	MIPI_CSI0_D0P	MIPI_CSI0_D0P	17	18		PCIE20_2_RXN/SATA30_2_RXN/USB30_2_SSRXN	SATA30_2_RXN
round (0V)		GND	19	20			Ground (0V)
ouna (ov)		GND	19	20	PCIE20_2_TXP/S		PCIE20_2_TXP
IPI_CSI0_RX_D1N	MIPI_CSI0_D1N	MIPI_CSI0_D1N	21	22		PCIE20_2_TXP/SATA30_2_TXP/USB30_2_SSTXP	SATA30_2_TXP
IFI_C3IO_KX_D1IN		WIFT_C3IO_D1IV	21		PCIE20_2_TXN/S		PCIE20_2_TXN
IPI_CSI0_RX_D1P	MIPI_CSI0_D1P	MIPI_CSI0_D1P	23	24	ATA30 2 TXN	PCIE20_2_TXN/SATA30_2_TXN/USB30_2_SSTXN	SATA30_2_TXN
ound (0V)		GND	25	26			Ground (0V)
ouria (0V)		GIND	23	20	MIPI_DPHY0_RX		Glouria (ov)
IPI_CSI0_RX_CLK0N	MIPI_CSI0_CLK0N	MIPI_CSI0_CLK0N	27	28		MIPI_DPHY0_RX_D0N/MIPI_CPHY0_RX_TRIO0_A	MIPI_DPHY0_RX_D0N
IFI_C3IO_KX_CEROIN		MIPI_CSIO_CLKON	21	20	MIPI_DPHY0_RX		IVITET_DETITIO_IXX_DOIN
IPI_CSI0_RX_CLK0P	MIPI_CSI0_CLK0P	MIPI_CSI0_CLK0P	29	30		MIPI_DPHY0_RX_D0P/MIPI_CPHY0_RX_TRIO0_B	MIPI_DPHY0_RX_D0P
round (0V)		GND	31				Ground (0V)
Tourid (0V)		GND	31	32	MIPI_DPHY0_RX		Ground (OV)
IPI CSIO RX D2N	MIPI_CSI0_D2N	MIPI_CSI0_D2N	33	34		MIPI_DPHY0_RX_D1N/MIPI_CPHY0_RX_TRIO0_C	MIPI_DPHY0_RX_D1N
III 1_C3IO_IOX_D2IV		IVIII I_CSIO_DZIV	33	37	MIPI_DPHY0_RX		MILI_DITITO_IOC_DITI
IPI_CSI0_RX_D2N	MIPI_CSI0_D2N	MIPI_CSI0_D2N	35	36		MIPI_DPHY0_RX_D1P/MIPI_CPHY0_RX_TRIO1_A	MIPI_DPHY0_RX_D1P
round (0V)		GND	37		GND		Ground (0V)
odila (07)		GITE		- 50	MIPI_DPHY0_RX		Ground (0V)
IPI_CSI0_RX_D3N	MIPI_CSI0_D3N	MIPI_CSI0_D3N	39	40		MIPI_DPHY0_RX_CLKN/MIPI_CPHY0_RX_TRIO1_B	MIPI_DPHY0_RX_CLKN
11_0510_10(_D514		Will I_CSIO_DSIV	33	10	MIPI_DPHY0_RX		WILL I_DITTIO_TOT_CERT
IPI_CSI0_RX_D3P	MIPI_CSI0_D3P	MIPI CSIO D3P	41	42	CLKP	MIPI_DPHY0_RX_CLKP/MIPI_CPHY0_RX_TRIO1_C	MIPI_DPHY0_RX_CLKP
patimg		Reserved	43	44	GND		Ground (0V)
IPI_CSI0_RX_CLK1N	MIPI_CSIO_CLK1N	MIPI_CSI0_CLK1N	45	46			Floatimg
IPI CSIO RX CLK1P	MIPI_CSI0_CLK1P	MIPI CSIO CLK1P		48			Floatimg
DMI0_TX_ON_H	BT1120_D11/DDRPHY_CH3_DTB1/UART9_RX_M1/PWM12_M1/						
PIO4_B5 for 3.3V	SPI3_MISO_M1/GPIO4_B5_d	GPIO4_B5	49	50	GND		Ground (0V)
	BT1120_D15/SPDIF1_TX_M2/PCIE20X1_2_PERSTN_M1/HDMI_T						
DMI0_TX_CEC_M0	X0_CEC_M0/I2C8_SDA_M3/PWM6_M1/SPI3_CS1_M1/GPIO4_C1				Reserved		Floatimg
2IO4_C1 for 3.3V	_d	HDMI_TX0_CEC_M0	51	52			
DMI0_SBDN/HPD for HDMI							
2.1	HDMI_TX0_SBDN/EDP_TX0_AUXN	HDMI_TX0_SBDN			Reserved		Floatimg
OMIO_HPD for HDMI V2.0			53	54			
ound (0V)		GND	55		GND		Ground (0V)
IPI_DPHY0_TX_D0N	MIPI_DPHY0_TX_D0N/MIPI_CPHY0_TX_TRIO0_A	MIPI_DPHY0_TX_D0N	57	58			HDMI0_SBDP,for HDMI V2.1
IPI_DPHY0_TX_D0P	MIPI_DPHY0_TX_D0P/MIPI_CPHY0_TX_TRIO0_B	MIPI_DPHY0_TX_D0P	59	60	Reserved		Floatimg
round (0V)		GND	61	62			Ground (0V)
MIPI_DPHY0_TX_D1N	MIPI_DPHY0_TX_D1N/MIPI_CPHY0_TX_TRIO0_C	MIPI_DPHY0_TX_D1N	63	64	Reserved		Floatimg

MIPI_DPHY0_TX_D1P	MIPI_DPHY0_TX_D1P/MIPI_CPHY0_TX_TRIO1_A	MIPI_DPHY0_TX_D1P	65	66	Reserved		Floatimg
Ground (0V)		GND	67		GND		Ground (0V)
MIPI_DPHY0_TX_CLKN	MIPI_DPHY0_TX_CLKN/MIPI_CPHY0_TX_TRIO1_B	MIPI_DPHY0_TX_CLKN	69			HDMI_TX0_D2P/EDP_TX0_D2P	HDMI0_TX2P
MIPI_DPHY0_TX_CLKP	MIPI_DPHY0_TX_CLKP/MIPI_CPHY0_TX_TRIO1_C	MIPI_DPHY0_TX_CLKP	71	72		HDMI_TX0_D2N/EDP_TX0_D2N	HDMI0_TX2N
Ground (0V)		GND	73		GND	- 1- /- 1- 1-	Ground (0V)
MIPI_DPHY1_TX_D0N	MIPI_DPHY1_TX_D0N/MIPI_CPHY1_TX_TRIO0_A	MIPI_DPHY1_TX_D0N	75			HDMI_TX0_D1P/EDP_TX0_D1P	HDMI0_TX1P
MIPI_DPHY1_TX_D0P	MIPI_DPHY1_TX_D0P/MIPI_CPHY1_TX_TRIO0_B	MIPI_DPHY1_TX_D0P	77	78		HDMI_TX0_D1N/EDP_TX0_D1N	HDMI0 TX1N
Ground (0V)		GND	79	80			Ground (0V)
MIPI_DPHY1_TX_D1N	MIPI_DPHY1_TX_D1N/MIPI_CPHY1_TX_TRIO0_C	MIPI_DPHY1_TX_D1N	81	82		HDMI_TX0_D0P/EDP_TX0_D0P	HDMI0_TX0P
MIPI_DPHY1_TX_D1P	MIPI_DPHY1_TX_D1P/MIPI_CPHY1_TX_TRIO1_A	MIPI_DPHY1_TX_D1P	83	84		HDMI_TX0_D0N/EDP_TX0_D0N	HDMI0_TX0N
Ground (0V)		GND	85		GND		Ground (0V)
MIPI_DPHY1_TX_CLKN	MIPI_DPHY1_TX_CLKN/MIPI_CPHY1_TX_TRIO1_B	MIPI_DPHY1_TX_CLKN	87	88		HDMI_TX0_D3P/EDP_TX0_D3P	HDMI0_TX3P
MIPI_DPHY1_TX_CLKP	MIPI_DPHY1_TX_CLKP/MIPI_CPHY1_TX_TRIO1_C	MIPI_DPHY1_TX_CLKP	89	-		HDMI_TX0_D3N/EDP_TX0_D3N	HDMI0_TX3N
Ground (0V)		GND	91		GND	N-3-1-2-1-2-1-2-1-1-2-1-1-1-1-1-1-1-1-1-1	Ground (0V)
MIPI_DPHY1_TX_D2N	MIPI_DPHY1_TX_D2N/MIPI_CPHY1_TX_TRIO2_A	MIPI_DPHY1_TX_D2N	93	94	MIPI_DPHY1_TX _D3N	MIPI_DPHY1_TX_D3N/MIPI_CPHY1_TX_TRIO2_C	MIPI_DPHY1_TX_D3N
MIDI DDINA TV DOD	MIPI_DPHY1_TX_D2P/MIPI_CPHY1_TX_TRIO2_B		0.5	0.0	MIPI_DPHY1_TX	MIPI_DPHY1_TX_D3P/NO_USE	MIDI DDI MI TV DOD
MIPI_DPHY1_TX_D2P		MIPI_DPHY1_TX_D2P	95	96			MIPI_DPHY1_TX_D3P
Ground (0V)		GND	97	98			Ground (0V)
HDMI_TX0_SDA_M0	BT1120_D14/PCIE20X1_2_WAKEN_M1/HDMI_TX0_SDA_M0/I2C					BT1120_D13/PCIE20X1_2_CLKREQN_M1/HDMI_TX0_SCL_M0/DD	HDMI_TX0_SCL_M0
GPIO4_C0, for 3.3V	8_SCL_M3/SPI3_CS0_M1/GPIO4_C0_u	HDMI_TX0_SDA_M0	99	100	0 M0	RPHY_CH3_DTB3/I2C5_SDA_M1/SPI3_CLK_M1/GPIO4_B7_u	GPIO4_B7, for 3.3V
		00' 011- 4		_	ID2 400B***		
		OPi_CM5_C	OR	Ε.	JP3 100PIN		
Description		Signal	PIN	NO.	. Signal		Description
USB20_HOST1_DP	USB20_HOST1_DP	USB20_HOST1_DP	1	2	TYPEC0_SSTX2P	TYPEC0_SSTX2P/DP0_TX3P	TYPEC0_SSTX2P
USB20_HOST1_DM	USB20_HOST1_DM	USB20_HOST1_DM	3	4	TYPEC0_SSTX2N	TYPEC0_SSTX2N/DP0_TX3N	TYPEC0_SSTX2N
Ground (0V)		GND	5	6	GND		Ground (0V)
TYPEC_SBU1	TYPEC0_SBU1/DP0_AUXP	TYPEC0_SBU1	7	8	TYPEC0_SSRX2P	TYPEC0_SSRX2P/DP0_TX2P	TYPEC0_SSRX2P
TYPEC_SBU2	TYPEC0_SBU2/DP0_AUXN	TYPEC0_SBU2	9	10	TYPEC0_SSRX2N	TYPEC0_SSRX2N/DP0_TX2N	TYPEC0_SSRX2N
Ground (0V)		GND	11	12	GND		Ground (0V)
USB20_HOST0_DP	USB20_HOST0_DP	USB20_HOST0_DP	13	14	•	TYPECO_SSTX1N/DPO_TX1N	TYPEC0_SSTX1N
USB20_HOST0_DM	USB20_HOST0_DM	USB20_HOST0_DM	15	16		TYPEC0_SSTX1P/DP0_TX1P	TYPEC0_SSTX1P
Ground (0V)		GND	17	18			Ground (0V)
TYPEC0_SSRX1P	TYPEC0_SSRX1P/DP0_TX0P	TYPEC0_SSRX1P	19	20	GPIO3_C6	CIF_D10/SPI3_MISO_M3/GPIO3_C6_u	GPIO3_C6 for1.8V
TYPEC0_SSRX1P	TYPEC0_SSRX1P/DP0_TX0P	TYPEC0_SSRX1P	21	22	GPIO3_C7	CIF_D11/PCIE20X1_2_CLKREQN_M0/HDMI_TX0_SCL_M2/I2C5_SC L_M0/SPI3_MOSI_M3/GPIO3_C7_u	PCIE20x1_2_CLKREQn_M0 GPIO3_C7, for 1.8V
Ground (0V)		GND	23	24	GPIO3_D1	CIF_D13/PCIE20X1_2_PERSTN_M0/UART4_TX_M1/PWM9_M2/SPI 0_MISO_M3/GPIO3_D1_d	GPIO3_D1, for 1.8V
MIPI_DPHY1_RX_CLKP	MIPI_DPHY1_RX_CLKP/MIPI_CPHY1_RX_TRIO1_C	MIPI_DPHY1_RX_CLKP	25	26	GPIO3_D5	MCU_JTAG_TMS_M1/UART9_TX_M2/PWM11_IR_M3/SPI0_CS1_M 3/GPIO3_D5_d	UART9_TX_M2_BT GPIO3_D5, for 1.8V
MIPI_DPHY1_RX_CLKN	MIPI_DPHY1_RX_CLKN/MIPI_CPHY1_RX_TRIO1_B	MIPI_DPHY1_RX_CLKN	27	28	GPIO3_D2	CIF_D14/I2C7_SCL_M2/UART9_RTSN_M2/SPI0_MOSI_M3/GPIO3_ D2_d	UART9_RTSN_M2_BT GPIO3_D2, for 1.8V
Ground (0V)		GND	29	30	GPIO3_D4	HDMI_TX0_HPD_M1/MCU_JTAG_TCK_M1/UART9_RX_M2/SPI0_CS	
MIPI_DPHY1_RX_D1P	MIPI_DPHY1_RX_D1P/MIPI_CPHY1_RX_TRIO1_A	MIPI_DPHY1_RX_D1P	31	_	GND	0_M3/GPIO3_D4_d	GPIO3_D4, for 1.8V Ground (0V)
MIPI_DPHY1_RX_D1N	MIPI_DPHY1_RX_D1N/MIPI_CPHY1_RX_TRIO0_C	MIPI_DPHY1_RX_D1P	33	34	CDIO2 D7	GMAC1_PTP_REF_CLK/I2C3_SCL_M1/SPI1_MOSI_M1/GPIO3_B7_d	GPIO3_B7,for 1.8V
Ground (0V)		GND	35	36	CDIO2 CE	CIF_D9/FSPI_CS1N_M2/CAN2_TX_M0/UART5_RX_M1/SPI3_CS1_M 3/GPIO3_C5_u	GPIO3_C5,for 1.8V
MIPI_DPHY1_RX_D0P	MIPI_DPHY1_RX_D0P/MIPI_CPHY1_RX_TRIO0_B	MIPI_DPHY1_RX_D0P	37	38	CDIO2 C4	CIF_D8/FSPI_CS0N_M2/CAN2_RX_M0/UART5_TX_M1/SPI3_CS0_M 3/GPIO3_C4_u	GPIO3_C4,for 1.8V
MIPI_DPHY1_RX_D0N	MIPI_DPHY1_RX_D0N/MIPI_CPHY1_RX_TRIO0_A	MIPI_DPHY1_RX_D0N	39	40	GPIO3_D3	CIF_D15/I2C7_SDA_M2/UART9_CTSN_M2/PWM10_M2/SPI0_CLK_ M3/GPIO3_D3_d	UART9_CTSN_M2_BT GPIO3_D3, for 1.8V
Ground (0V)		GND	41	42	GPIO3_A1	GMAC1_TXD3/SDIO_D1_M1/I2S3_SCLK/AUDDSM_LN/FSPI_D2_M 2/I2C6_SCL_M4/PWM11_IR_M0/SPI4_MOSI_M1/GPIO3_A1_u	GMAC_TXD3/SDIO_D1 GPIO3_A1, for 1.8V
PCIE20x1_2_WAKEn_M0 GPIO3_D0, for 1.8V	CIF_D12/PCIE20X1_2_WAKEN_M0/HDMI_TX0_SDA_M2/I2C5_SD A_M0/UART4_RX_M1/PWM8_M2/SPI3_CLK_M3/GPI03_D0_u	GPIO3_D0	43	44	GND		Ground (0V)
Ground (0V)		GND	45	46	GPIO3_B3	GMAC1_TXD0/I2S2_SDO_M1/UART2_RTSN/GPIO3_B3_u	GMAC_TXD0 GPIO3_B3, for 1.8V
GMAC_TXD2/SDIO_D0 GPIO3_A0, for 1.8V	GMAC1_TXD2/SDIO_D0_M1/I2S3_MCLK/FSPI_D0_M2/I2C6_SDA _M4/PWM10_M0/SPI4_MISO_M1/GPIO3_A0_u	GPIO3_A0	47	48	GPIO3_B2	GMAC1_TXER/I2S2_SDI_M1/UART2_RX_M2/PWM3_IR_M1/GPIO3 _B2_d	GMAC_TXER/PWM3_IR GPIO3_B2, for 1.8V
GMAC_TXD1 GPIO3_B4, for 1.8V	GMAC1_TXD1/I2S2_MCLK_M1/UART2_CTSN/GPIO3_B4_u	GPIO3_B4	49	50	GPIO3_C1	GMAC1_PPSCLK/UART7_RX_M1/SPI1_CLK_M1/GPIO3_C1_d	GPIO3_C1,for 1.8V
GMAC_TXEN/PWM12 GPIO3_B5, for 1.8V	GMAC1_TXEN/I2S2_SCLK_M1/CAN1_RX_M0/UART3_TX_M1/PW M12_M0/GPI03_B5_u	GPIO3_B5	51	52	GPIO3_A6	ETH1_REFCLKO_25M/MIPI_CAMERA1_CLK_M1/I2C4_SCL_M0/GPI O3_A6_d	MIPI_CLK1 GPIO3_A6, for 1.8V
GMAC_MDIO GPIO3_C3, for 1.8V	GMAC1_MDIO/MIPI_TE1/I2C8_SDA_M4/UART7_CTSN_M1/PW M15_IR_M0/SPI1_CS1_M1/GPIO3_C3_d	GPIO3_C3	53	54	GND		Ground (0V)

		1			ı	T	GMAC RXD1/MIPI CLK3/PWM9
Ground (0V)		GND	55	56	GPIO3_B0	GMAC1_RXD1/MIPI_CAMERA3_CLK_M1/PWM9_M0/GPIO3_B0_u	GPIO3_B0, for 1.8V
GMAC_MCLK/PWM13	GMAC1_MCLKINOUT/I2S2_LRCK_M1/CAN1_TX_M0/UART3_RX_	GPIO3 B6			GPIO3_A5	GMAC1_RXCLK/SDIO_CLK_M1/MIPI_CAMERA0_CLK_M1/FSPI_CLK	GMAC_RXCLK/SDIO_CLK
GPIO3_B6, for 1.8V	M1/PWM13_M0/GPIO3_B6_d	GP1O3_B0	57	58	GPIO3_A3	_M2/I2C4_SDA_M0/UART8_CTSN_M1/GPIO3_A5_d	GPIO3_A5, for 1.8V
GMAC_RXDV/MIPI_CLK4/PWM2	GMAC1_RXDV_CRS/MIPI_CAMERA4_CLK_M1/UART2_TX_M2/P	CDIO2 D1			CDIO2 A7	CMAC1 DVD0/MIDI CAMEDAR CLIV M1/DM/MQ M0/CDIOR A7	GMAC_RXD0/MIPI_CLK2/PWM8
GPIO3_B1, for 1.8V	WM2_M1/GPIO3_B1_d	GPIO3_B1	59	60	GPIO3_A7	GMAC1_RXD0/MIPI_CAMERA2_CLK_M1/PWM8_M0/GPIO3_A7_u	GPIO3_A7, for 1.8V
GMAC_RXD2/SDIO_D2	GMAC1_RXD2/SDIO_D2_M1/I2S3_LRCK/AUDDSM_LP/FSPI_D2_	CDYOR AR			CRYON A.A	GMAC1_TXCLK/SDIO_CMD_M1/I2S3_SDI/AUDDSM_RP/UART8_R	GMAC_TXCLK/SDIO_CMD
GPIO3 A2, for 1.8V	M2/UART8_TX_M1/SPI4_CLK_M1/GPIO3_A2_u	GPIO3_A2	61	62	GPIO3_A4	TSN M1/SPI4 CS1 M1/GPIO3 A4 d	GPIO3 A4, for 1.8V
GMAC RXD3/SDIO D3	GMAC1_RXD3/SDIO_D3_M1/I2S3_SDO/AUDDSM_RN/FSPI_D3_	CDY 0.3 4.3					
GPIO3_A3, for 1.8V	M2/UART8_RX_M1/SPI4_CS0_M1/GPIO3_A3_u	GPIO3_A3	63	64	GND		Ground (0V)
Ground (0V)	7. 12 2 7. 2012 7. 112 121	GND	65	66	GPIO0_B0	SPI2_CS1_M2/I2C1_SCL_M1/UART0_RX_M1/GPIO0_B0_z	GPIO0_B0, for 1.8V
REFCLK_OUT/GPIO0_A0 , for 1.8V	REFCLK_OUT/GPIO0_A0_d	GPIO0_A0	67	68	I2S0_MCLK	I2S0_MCLK/I2C6_SDA_M1/UART3_RTSN/PWM3_IR_M2/SPI4_CLK_ M0/GPI01_C2_d	I2S0_MCLK GPIO1_C2, for 1.8V
I2S0_SRLK GPIO1 C2, for 1.8V	I2SO_MCLK/I2C6_SDA_M1/UART3_RTSN/PWM3_IR_M2/SPI4_CL K_M0/GPIO1_C2_d	GPIO1_C2	69	70	I2S0_SDO0	I2S0_SDO0/I2C4_SCL_M4/UART4_CTSN/GPIO1_C7_d	I2S0_SDO0 GPIO1 C7, for 1.8V
I2SO SCLK						GMAC1 PPSTRIG/I2C3 SDA M1/UART7 TX M1/SPI1 MISO M1/	
GPIO1 C5, for 1.8V	I2S0_LRCK/I2C2_SCL_M3/UART4_RTSN/GPIO1_C5_d	GPIO1_C5	71	72	GPIO3_C0	GPIO3_C0_d	GPIO3_C0, for 1.8V
I2SO SDIO							
GPIO1_D4, for 1.8V	I2S0_SDI0/GPIO1_D4_d	GPIO1_D4	73	74	GND		Ground (0V)
Ground (0V)		GND	75	76	PCIE20_0_REFCLKP	PCIE20_0_REFCLKP	PCIE20_0_REFCLKP
	I2S1_SDO3_M1/CPU_BIG1_AVS/I2C1_SDA_M2/CAN2_TX_M1/H						
GPIO0_D5, for 1.8V	DMI_TX0_SCL_M1/SPI3_CS1_M2/SATA_MP_SWITCH/GPIO0_D5_	GPIO0_D5	77	78	PCIE20_0_REFCLKN	PCIE20_0_REFCLKN	PCIE20_0_REFCLKN
	I2S1_SDI2_M1/PDM0_SDI0_M1/I2C6_SDA_M0/UART1_RTSN_M						
GPIO0_C7, for 1.8V	2/PWM6 M0/SPI0 MISO M0/GPIO0 C7 d	GPIO0_C7	79	80	GND		Ground (0V)
	I2S1 SDO2 M1/PDM0 SDI2 M1/PWM3 IR M0/I2C1 SCL M2/C						
GPIO0 D4, for 1.8V	AN2_RX_M1/HDMI_TX0_SDA_M1/SPI3_CS0_M2/SATA_CPDET/G	GPIO0 D4			PCIE20 0 TXP	PCIE20 0 TXP/SATA30 0 TXP	PCIE20_0_TXP
	PIO0 D4 u			1 82			
	DDAMO CLICA MATERIANA MONTANTO DV MONTOCA CDA MATERIA						
GPIO0_C4, for 1.8V	HPDIN M1/GPIO0 C4 d	GPIO0_C4	83	84	PCIE20_0_TXN	PCIE20_0_TXN/SATA30_0_TXN	PCIE20_0_TXN
GPIO0 D3, for 1.8V	LITCPU AVS/SPI3 CLK M2/GPIO0 D3 u	GPIO0 D3	85	86	GND		Ground (0V)
Ground (0V)		GND	87	88	PCIE20_0_RXN	PCIE20_0_RXN/SATA30_0_RXN	PCIE20_0_RXN
GPIO0_D0, for 1.8V	I2S1_SDI3_M1/PDM0_SDI1_M1/I2C6_SCL_M0/UART1_CTSN_M2 /PWM7_IR_M0/SPI3_MISO_M2/GPI00_D0_d	GPIO0_D0	89	90	PCIE20_0_RXP	PCIE20_0_RXP/SATA30_0_RXP	PCIE20_0_RXP
GMAC MDC	CAAACA AADCAAADI TEO GOCO CCL AAAALADTZ DTCALAAA (DABAA	CRY 0.3 C3			CNID		c 1,000
GPIO3 C2, for 1.8V	4 M0/SPI1 CS0 M1/GPIO3 C2 d	GPIO3_C2	91	92	GND		Ground (0V)
	CIF_D3/BT1120_D3/I2S1_SCLK_M0/DDRPHY_CH0_DTB_3/UART	CDY 0 4 4 3			caron cr	I2S1_SDI0_M1/GPU_AVS/UART0_TX_M0/I2C4_SCL_M2/PWM4_M	CDYON OF 6 A OV
GPIO4_A3, for 3.3V	0 TX M2/GPIO4 A3 d	GPIO4_A3	93	94	GPIO0_C5	0/GPIO0 C5 u	GPIO0_C5, for 1.8V
	CIF_VSYNC/BT1120_D9/I2S1_SDO2_M0/PCIE20X1_2_BUTTON_R					·	İ
CAN1 TX M1/GPIO4 B3, for 3.3V	STN/DDRPHY CH2 DTB3/I2C7 SDA M3/UART8 CTSN M0/PW				GPIO0 B2	CLK32K_IN/CLK32K_OUT0/GPIO0_B2_u	CLK32K IN/GPIO0 B2, for 1.8V
	M15 IR M1/CAN1 TX M1/GPIO4 B3 u	_	95	96	_		1
	CIF_HREF/BT1120_D8/I2S1_SDO1_M0/PCIE20X1_1_BUTTON_RS	İ	33			I2S1_SDI1_M1/NPU_AVS/UART0_RTSN/PWM5_M1/SPI0_CLK_M0/	
					GPIO0 C6		GPIO0_C6, for 1.8V
CAN1 RX M1/GPIO4 B2, for 3.3V	TN/DDRPHY CH2 DTB2/I2C7 SCL M3/UART8 RTSN M0/PWM	IGPIO4 B2					
CAN1_RX_M1/GPIO4_B2, for 3.3V	TN/DDRPHY_CH2_DTB2/I2C7_SCL_M3/UART8_RTSN_M0/PWM 14 M1/SPI0 CS0 M1/CAN1 RX M1/GPIO4 B2 u	GPIO4_B2	97	98	GP100_C6	SATA_CP_POD/GPIO0_C6_u	GF100_C6, 101 1.8V