

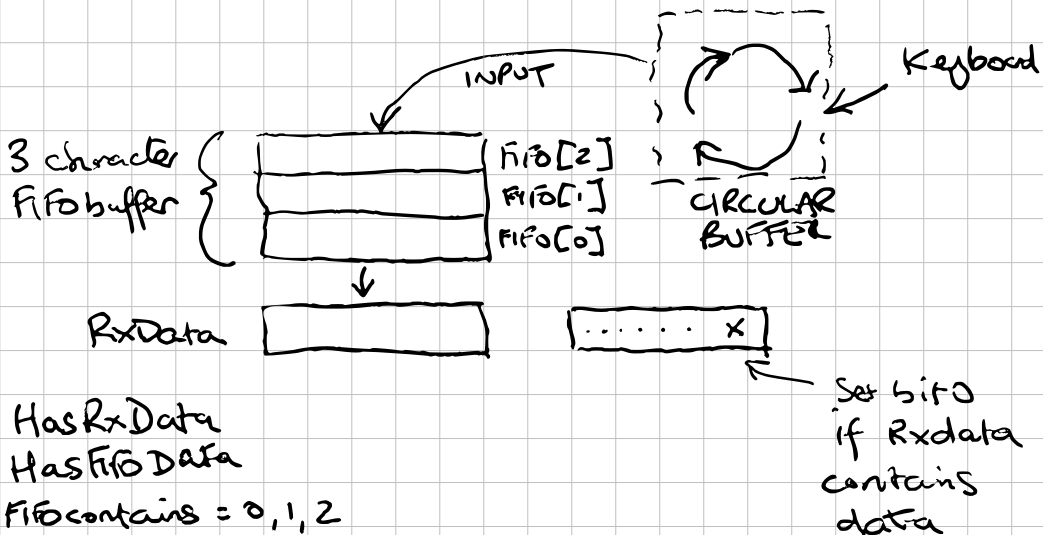
SIO INPUT

- * Comes from keyboard \rightarrow SIO \rightarrow CPU
- * Triggers interrupt.
- * 3 character FIFO input
- * Can set or clear RTS

MY TWEAKS

- * Add a circular buffer.
 - o Improves pipelining and performance
 - o Provides thread synchronisation/isolation

STRUCTURE



Don't pull from circular buffer if RTS is high

When we have RxData, see if we can trigger a interrupt. If not, keep looping until we can. Maybe need a timeout on this to sleep for 1ms if 1000 attempts failed, for example.

SIO OUTPUT

- * Comes from CPU \rightarrow SIO \rightarrow Screen/Log
- * No interrupts or wait situations, just keeps outputting characters.
- * Doesn't use DTR etc.

MY TWEAKS

- * Uses circular buffer for terminal output
 - o Improves pipelining and performance
 - o Provides thread synchronisation/isolation

STRUCTURE

