SIO INPUT Comes from Keyboard -> 510 -> CPU
Trigges interrupt. 3 character Fire imput * Can set or clear RTS MY TWEAKS * Add a circular buffer. o Improves pipelining and porformance o Provides thread synchronisation (isolation STRUCTURE INPUT Keyboad 3 chracter (Fifobuffer } [FIB[2] FIRE T GREWAR fifo[o] BUFFER Pixoata Se 5170 HaskxData if Rxdata Has Fifo Dara contains FIFOCONTains = 0,1,2 data Don't pull from circular buffer if RTS is high When we have RaData, see if we can trigge a interrupt. If not, keep looping until we can. Maybe need a timeout on this to sleep for Ins if 1000 attempts foiled, for oxample.

SIO OUTPUT

* Comes from CPU -> SIO -> Screen/Log

* No interripts or wait situations, just keeps
outputting characters.

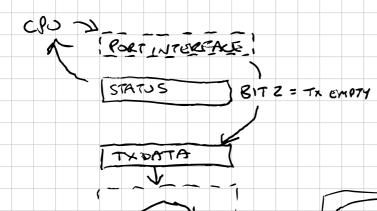
* Doesn't use DTR etc.

MY TWEAKS

* Uses circular buffer for terminal output

0 Improves pipelining and performance
0 Provides thread synchronisation (isolation

STRUCTURE



CIRCULAR BUFFER