

# COS10004: Computer Systems

## Lab 5

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### Planning your Assignment

2.1) Main components consumed for first stage:

- JK Flip Flop (for Play/Pause state appropriately)
- Buttons (Play/Pause buttons)
- Pin (On/Off)
- AND gate

2.2) Resources I found relevant to these components:

- Module 2.2 COS10004 (link: [https://swinburne.instructure.com/courses/46548/pages/2-dot-2-adders-and-programmable-gates?module\\_item\\_id=2928319](https://swinburne.instructure.com/courses/46548/pages/2-dot-2-adders-and-programmable-gates?module_item_id=2928319))
- Module 2.4 COS10004 (link: [https://swinburne.instructure.com/courses/46548/pages/2-dot-4-storage-and-rs-flip-flop?module\\_item\\_id=2928321](https://swinburne.instructure.com/courses/46548/pages/2-dot-4-storage-and-rs-flip-flop?module_item_id=2928321))
- Utilization from Lab 2 (link: <https://swinburne.instructure.com/courses/46548/assignments/475233>)

3) Outlining first stage of the comprehensive circuit:

