Z80 Home Brew #3
Monitor ROM
December 12, 2022

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Overview

The machine consists of a Z80-CPU, 2K EPROM (expandable to 4K), 4K of RAM, two Z80-PIO, one Z80CTC, one Z80-SIO/0, and an HDSP-2111 8-character 5x7 LED dot matrix display. All four PIO channels are wired to a 50-pin FCC connector (P3). Both SIO channels are wired to a DIP14 socket/connector (P4). General-purpose I/O expansion is provided on a pair of DIP14 sockets/connectors (P1, P2). The HDSP-2111 display is wired as an I/O expansion.

The original EPROM for this machine was damaged, and all odd pages (256-byte chunks) are missing (A8 shorted to GND). Half the monitor code is missing, but some things can be reconstructed or restored based how the existing code interacts.

General Operation

The monitor may be entered from a running program by pressing the NMI button or by executing a RST 1 instruction (0xCF) in the program. In the case of RST 1, the saved PC will point to the RST 1 instruction (not the next instruction). In both cases, the saved PC is printed after a '>' character before entering the monitor loop.

Pressing NMI while running the monitor is similar to RESET.

User Commands

In the following, *addr* and *byte* are entered in hexadecimal. **[ ]** means the parameter is optional. CR means the carriage return key. LF means the line feed key.

Implemented Commands

The following commands either have code that defines the operation, or can be reasonably determined based on space available and logical deduction.

[*addr*]CR
print the byte stored at *addr*, advance *addr* by 1.

[*addr*]**/**

print the word stored at *addr*, advance *addr* by 2. Also used after **T** or **R** commands to view subsequent values/

[*addr*]**I**

Input bytes starting at *addr*. Prints address and current contents and waits for input. Commands are:

[*byte*]CR

store *byte* (if entered) in *addr*, increment *addr* by 1.

-

decrement *addr* by 1.

.

return to monitor

[*addr*]**G**

Go (start execution) at *addr* or saved PC

P

start execution at PC+1 (for continuing after RST 1 traps).

T

print word at saved SP (top of stack), increment *addr* by 2. The top of stack will not include the PC that was pushed as part of the NMI or RST 1. The rest of the stack may be viewed by using **/** commands.

R*reg***/**

print contents of saved register pair. *reg* is one of: A,B,D,H,A',B',D',H',X,Y,S,P. Increment *addr* by 2, effectively selecting the next register pair.

H

Host control mode? Appears to accept commands from SIO channel B, as if connected to a remote computer. Remote commands are echoed to console (channel A), unclear what other interaction there is.

TBD

Terminal mode? Unclear how to enter this mode. The code passes characters both directions between SIO channels A and B. In addition, there is a mode where the stream coming from channel B may contain Intel HEX format data, which is presumably loaded into memory. The console (channel A) user may press Ctrl-] to cause a BREAK condition to be sent to channel B, presumably to disconnect from the host. At least one other key command seems to exist, possibly to exit this mode.

Unknown Commands

The following commands do not have code or else interact with hardware who’s details are not known.

LF

(unknown)

^
: “up”?

V
: “down”?

Host Control Commands

Host control commands may be prefixed with an octal number. Unclear how data is sent back to host (in octal?).

.
: (unknown)

,
: (unknown)

\
: (unknown)

LF
: (unknown)

CR
: (unknown)

S
: (unknown)

R
: (unknown)

X
: Exit Host mode, return to monitor

M
: (unknown interaction with PIO-connected device)

/
: (unknown)

^
: (unknown)

<
: (unknown)

>
: (unknown)

G
: Get 2K bytes from PIO-connected device. Read pairs of 4-bits from PIO2A.

P
: Put 2K bytes to PIO-connected device. Writes pairs of 4-bits to PIO2B.

TBD
: (unknown) space for 2 more commands exits.

TBD
: (unknown) space for 2 more commands exits.