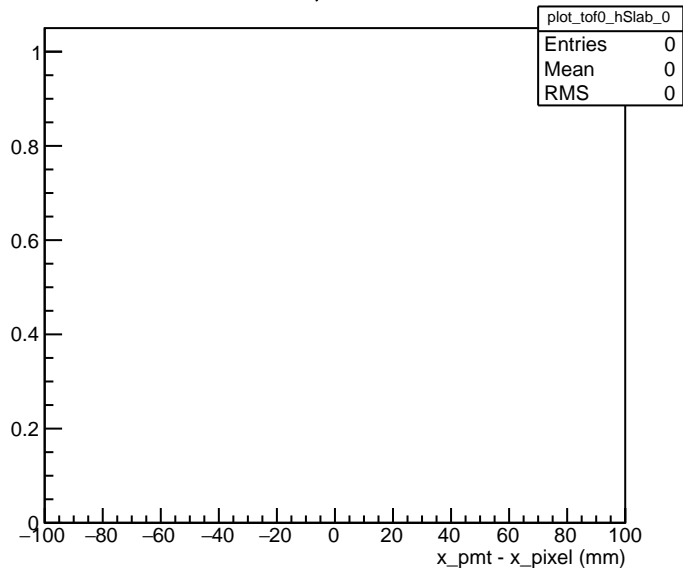
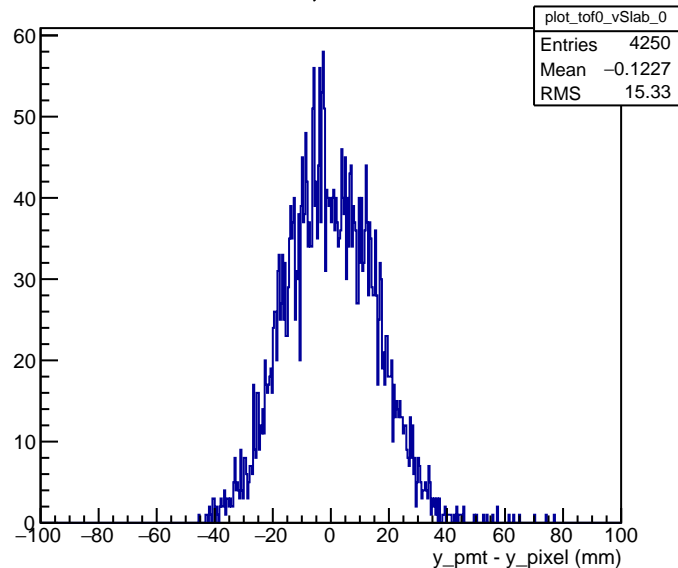


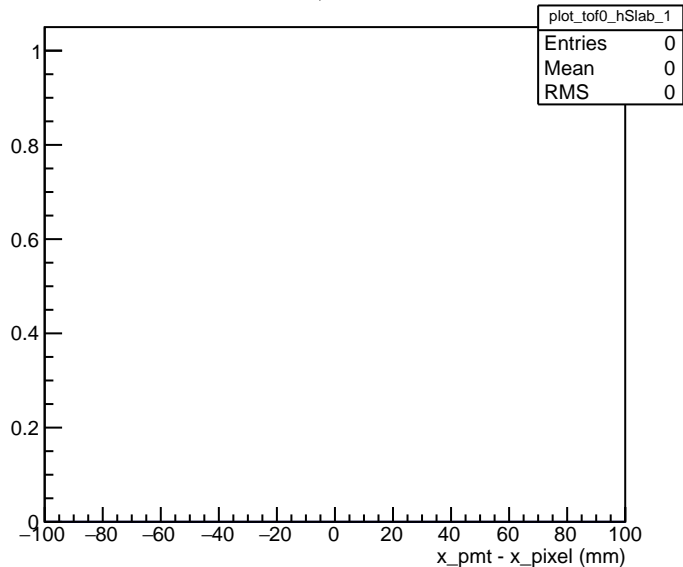
TOF0, Hslab 0



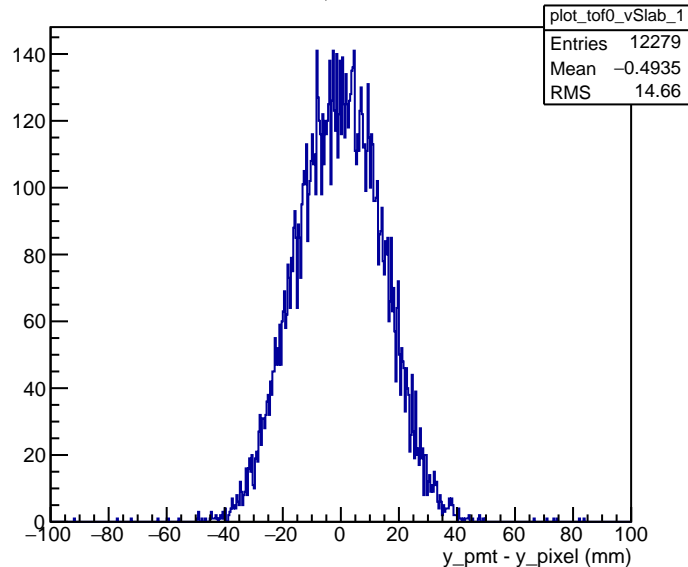
TOF0, Vslab 0



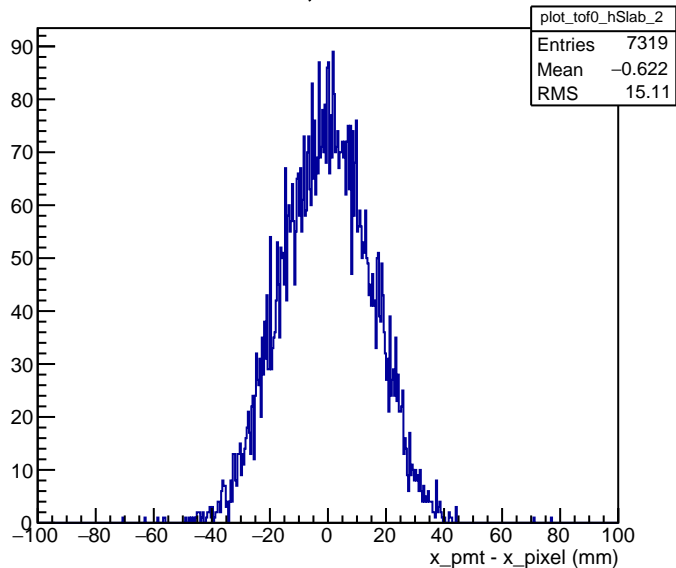
TOF0, Hslab 1



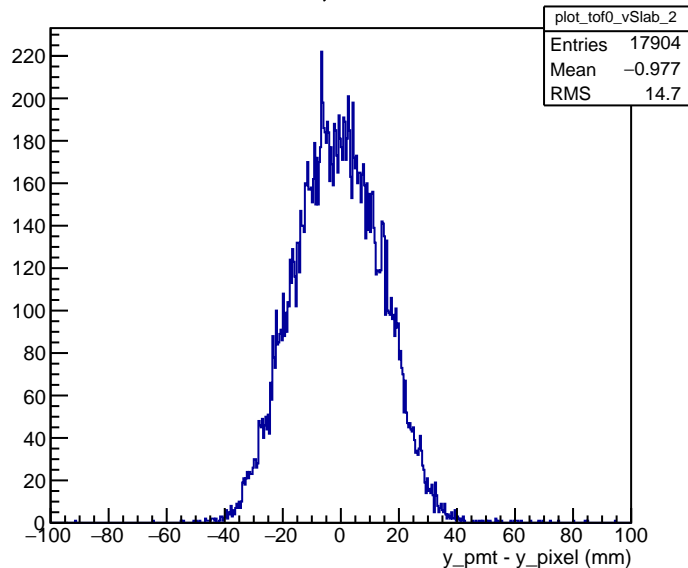
TOF0, Vslab 1



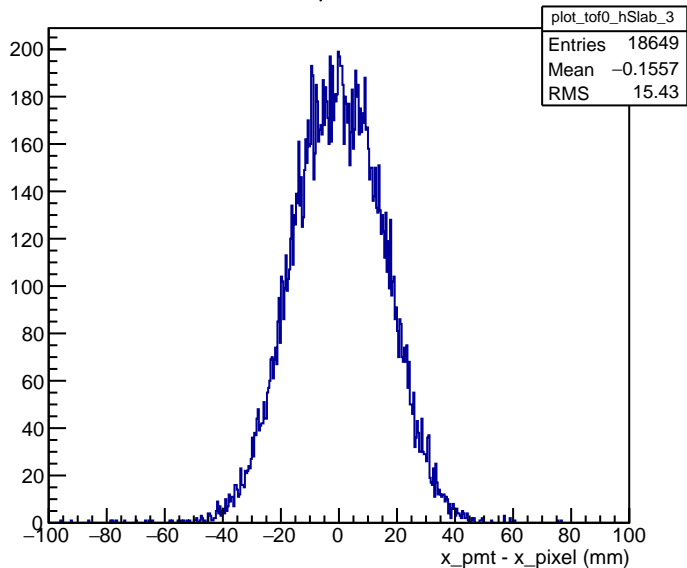
TOF0, Hslab 2



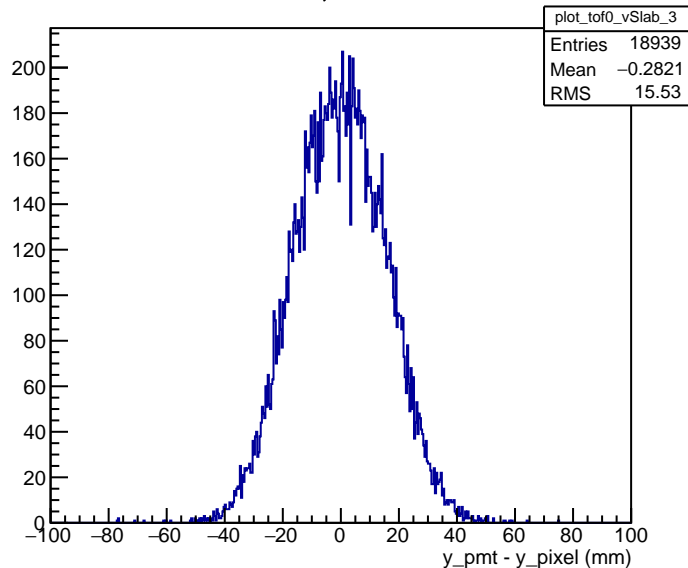
TOF0, Vslab 2



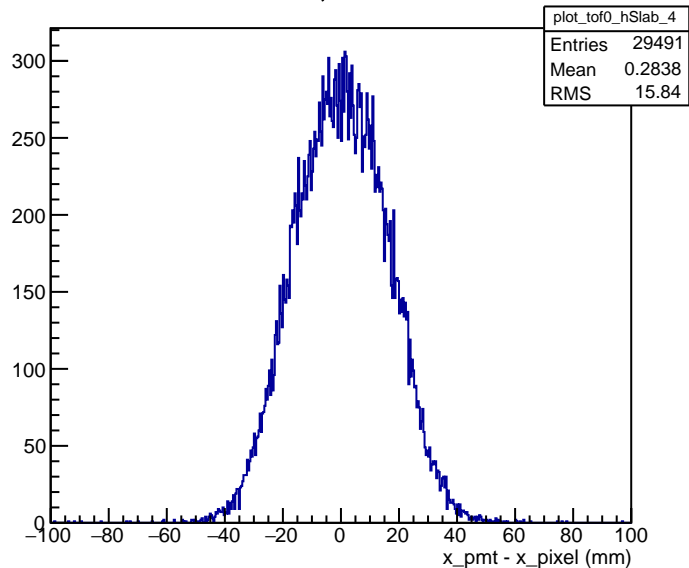
TOF0, Hslab 3



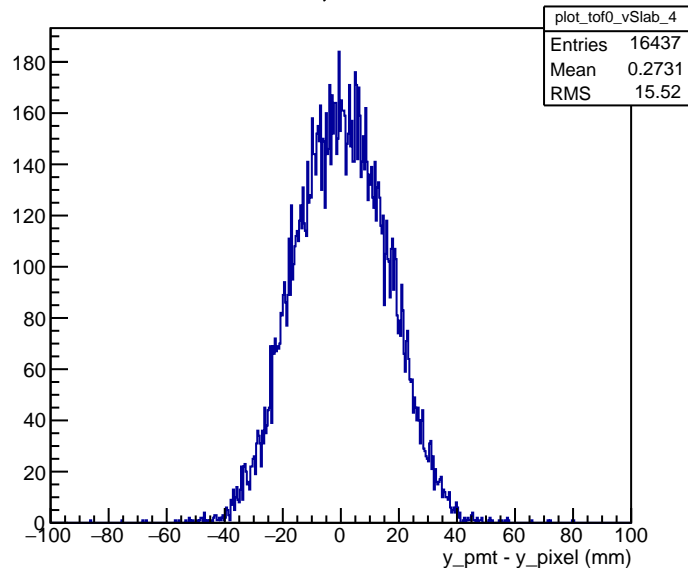
TOF0, Vslab 3



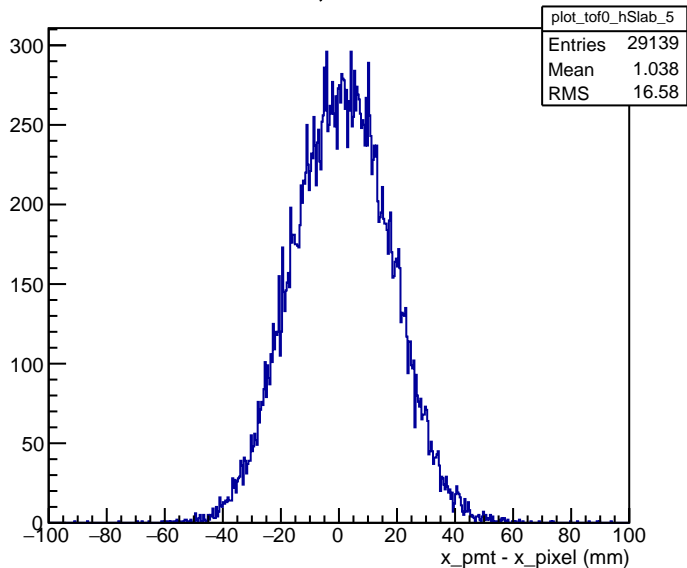
TOF0, Hslab 4



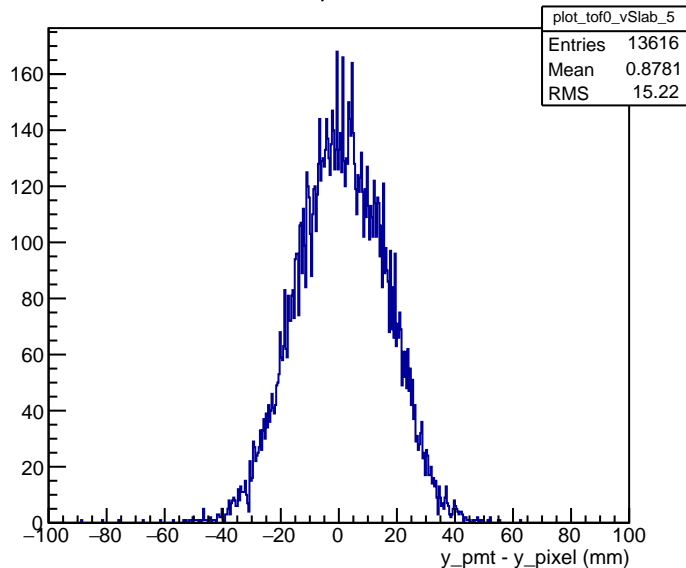
TOF0, Vslab 4



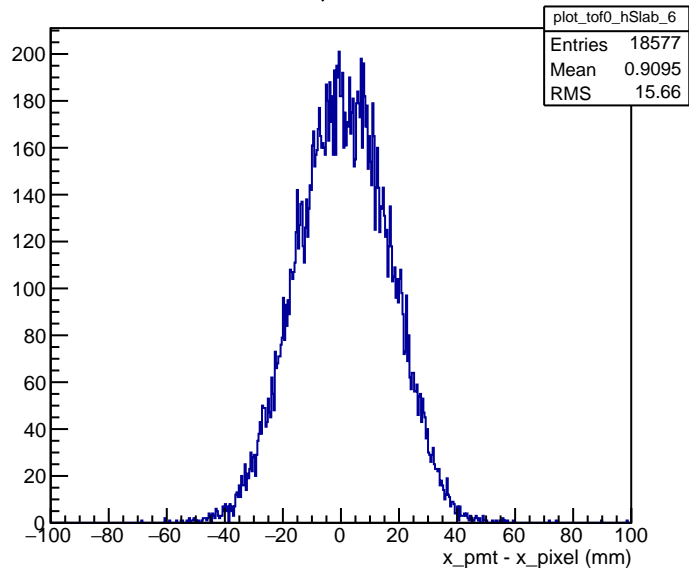
TOF0, Hslab 5



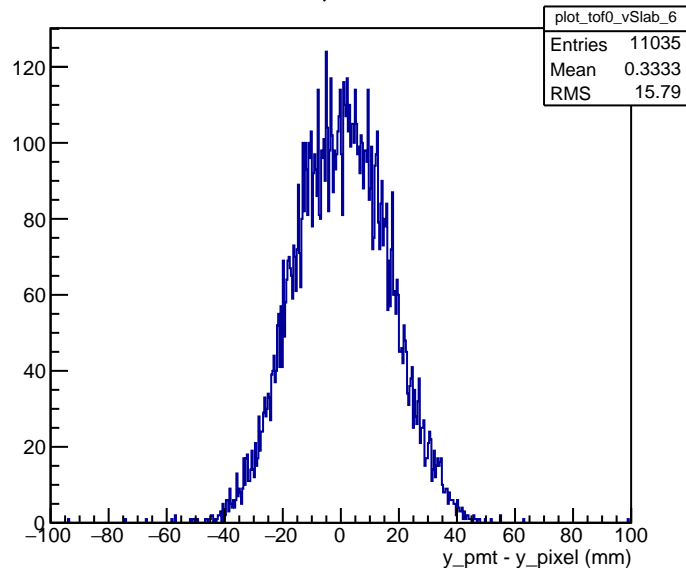
TOF0, Vslab 5



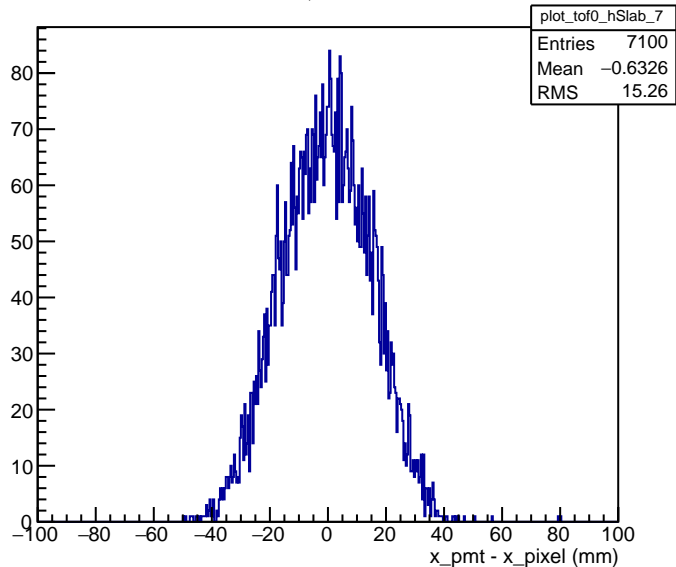
TOF0, Hslab 6



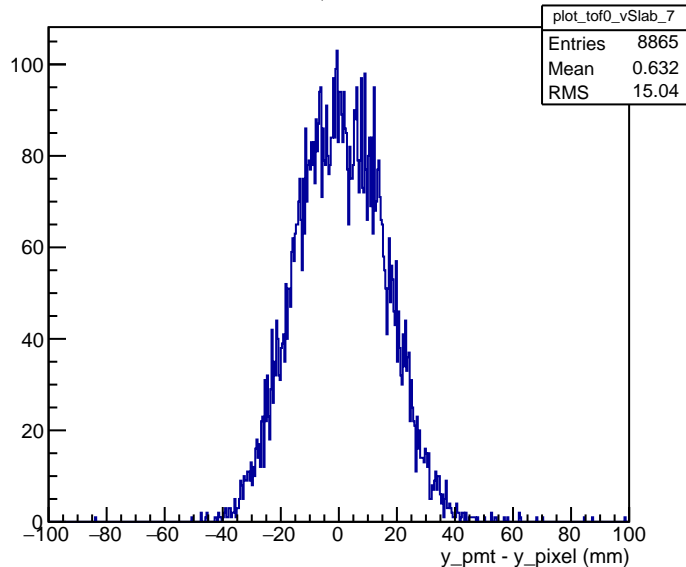
TOF0, Vslab 6



TOF0, Hslab 7

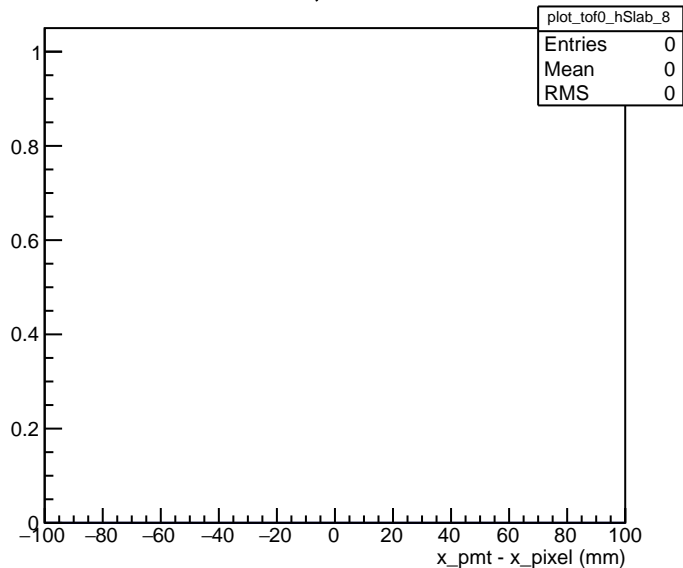


TOF0, Vslab 7

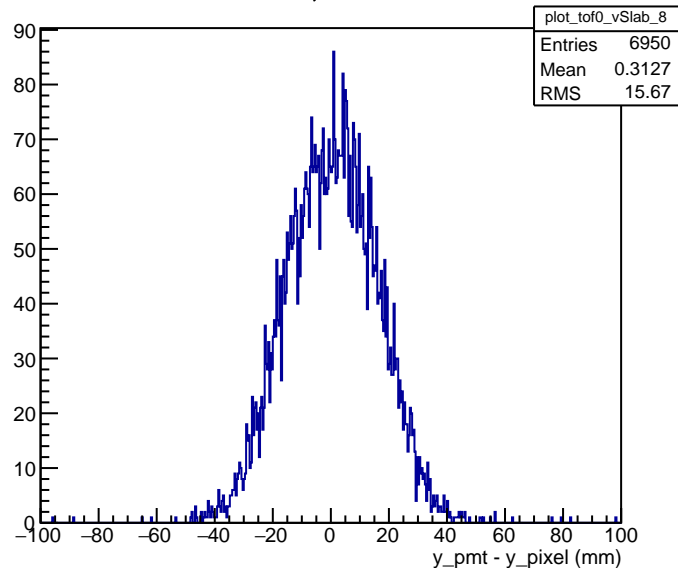




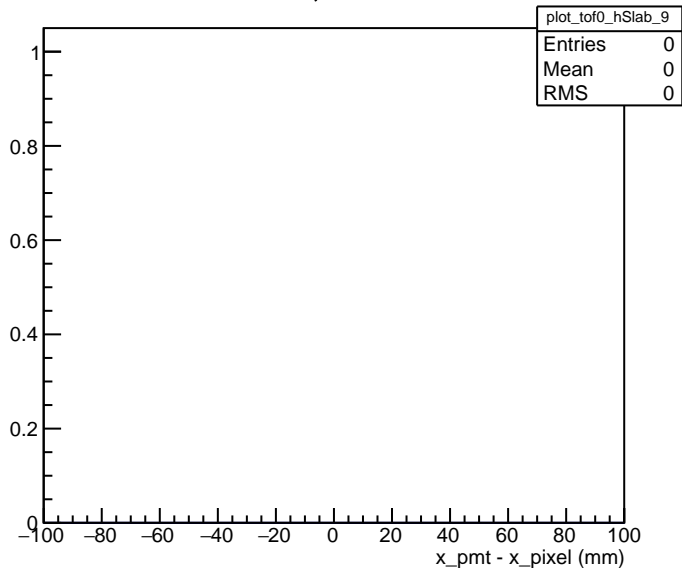
TOF0, Hslab 8



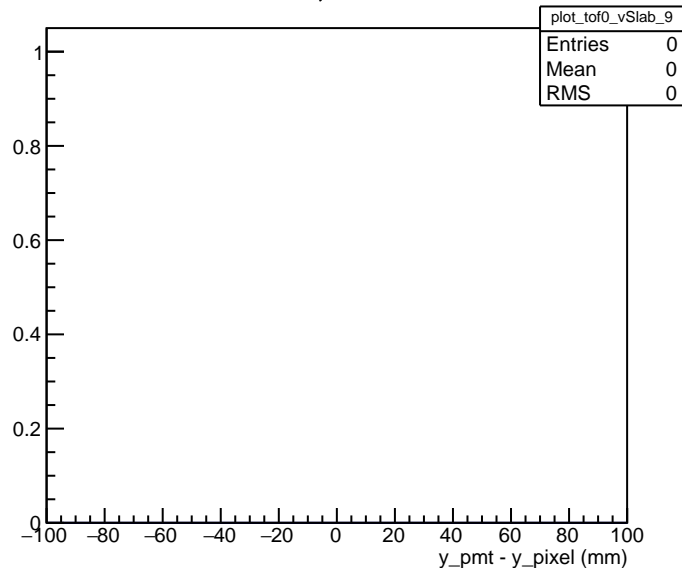
TOF0, Vslab 8



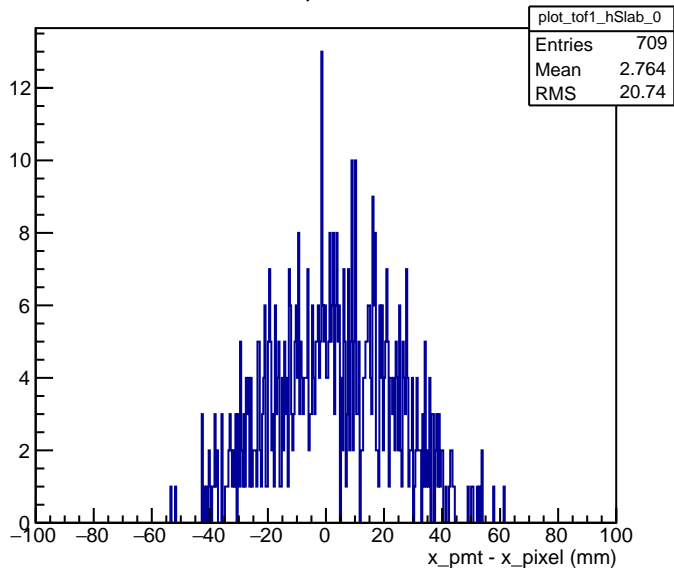
TOF0, Hslab 9



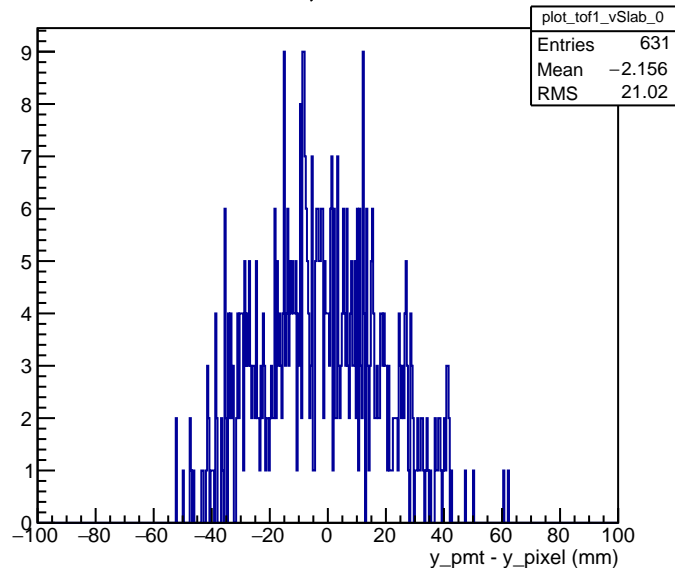
TOF0, Vslab 9



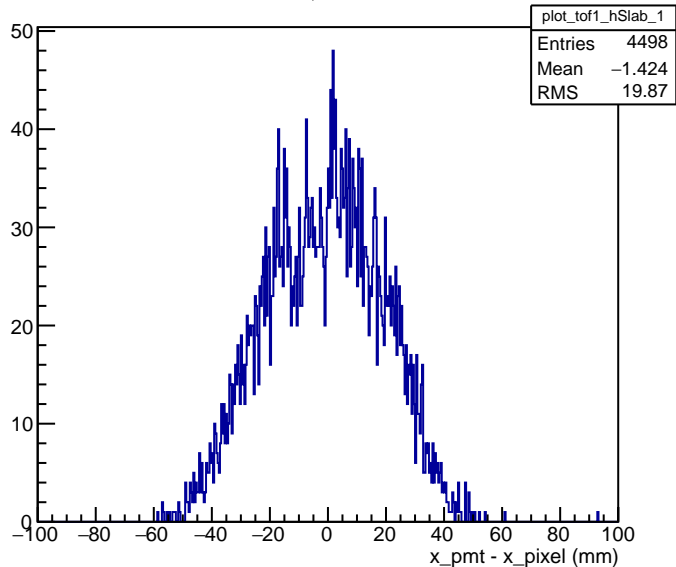
TOF1, Hslab 0



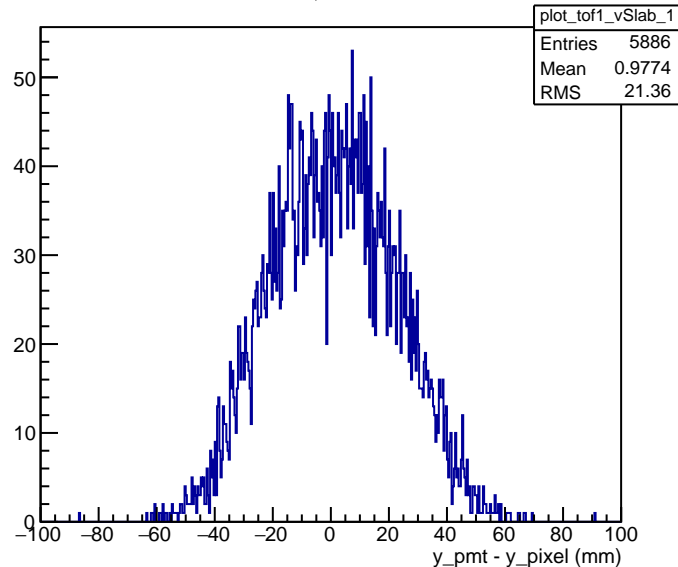
TOF1, Vslab 0



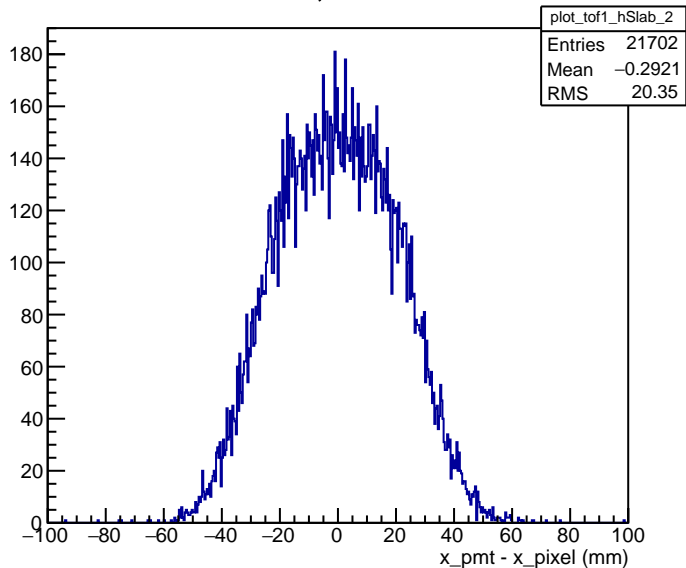
TOF1, Hslab 1



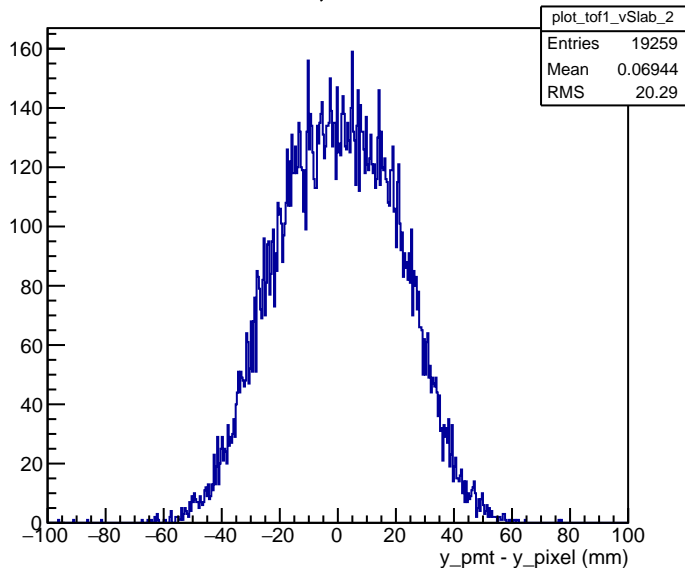
TOF1, Vslab 1



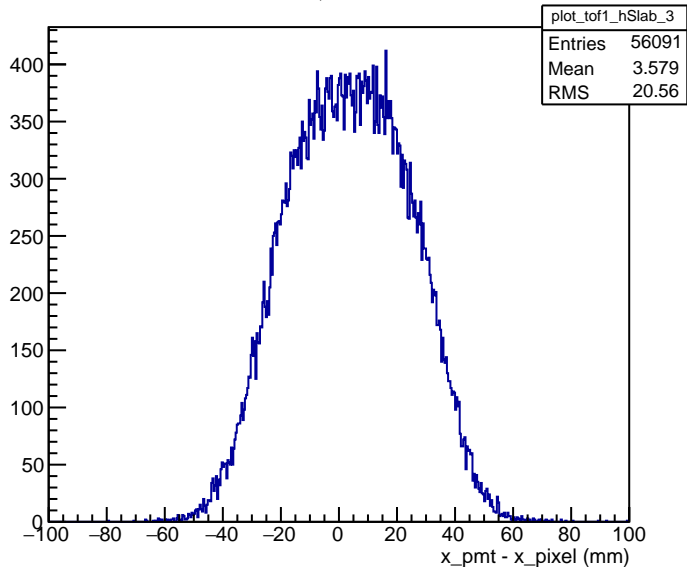
TOF1, Hslab 2



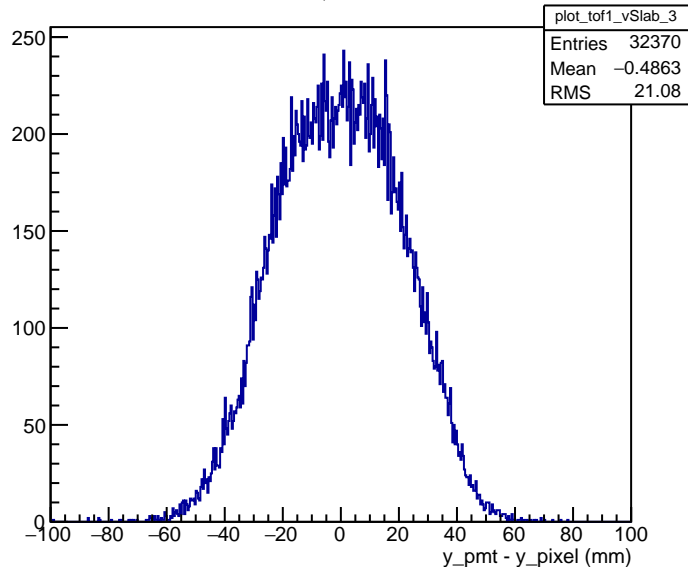
TOF1, Vslab 2



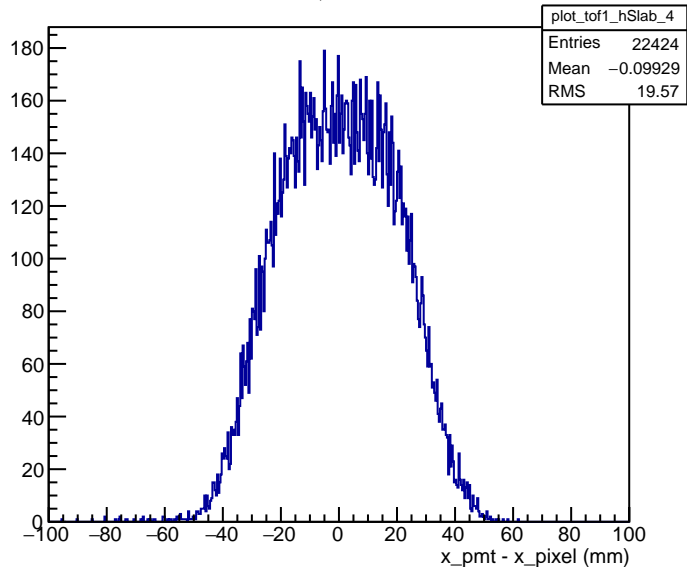
TOF1, Hslab 3



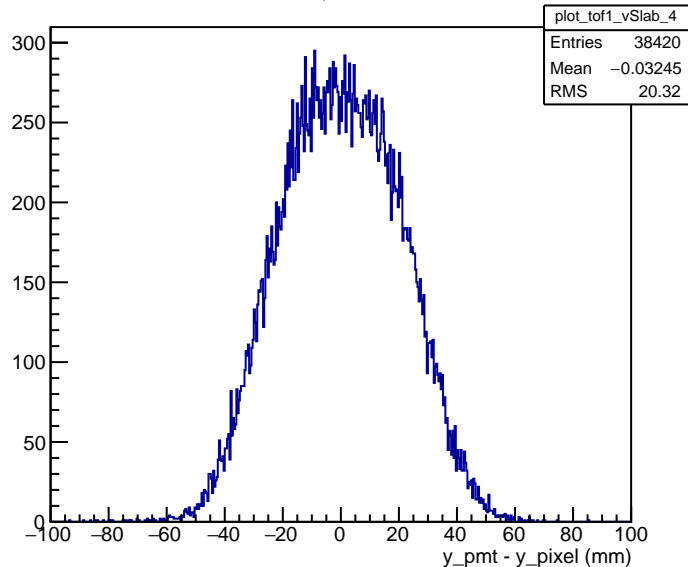
TOF1, Vslab 3



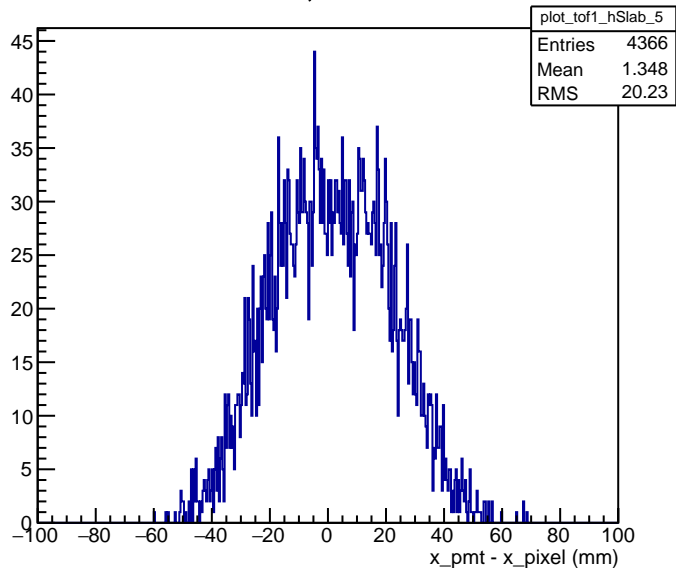
TOF1, Hslab 4



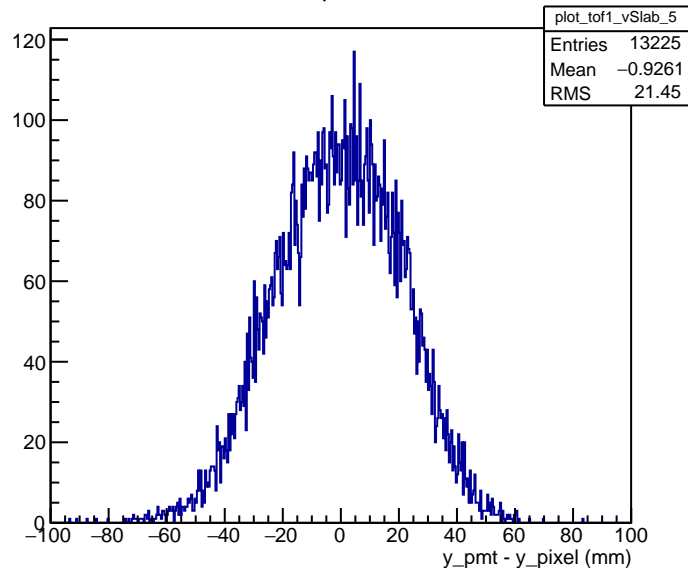
TOF1, Vslab 4



TOF1, Hslab 5

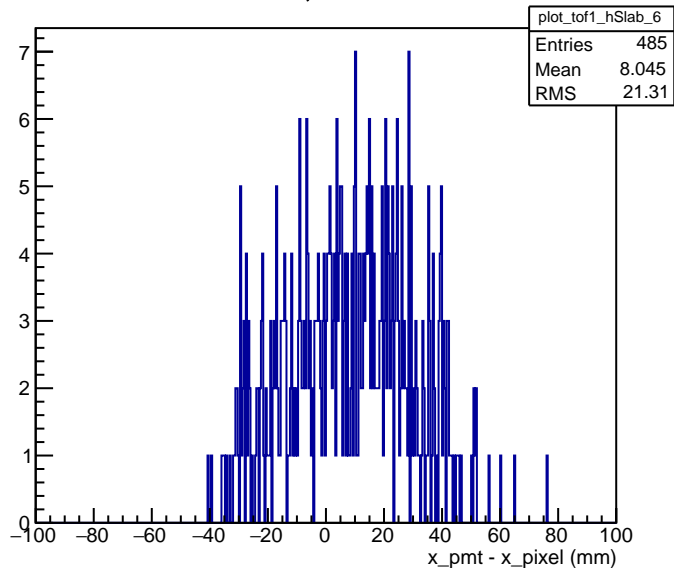


TOF1, Vslab 5





TOF1, Hslab 6



TOF1, Vslab 6

