Lab Session 2

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Introduction to ucos-II based application

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PRM : 2019BTEEN00035

Sub. : RTOS

- 2.4.1. 17 list the folders in the unision project of usos based application
 - => ARM, MCOS-II, MD2148, main
 - 2) Explain the use of each folder.
 -) (i) ARM: Includes device dependent files as we use arm processor. These files include processor & implementation specific constant
 - cii) wood-II: It convict of various of files with respect to real time operating system

ciii) mp2148: It is a board specific folder which

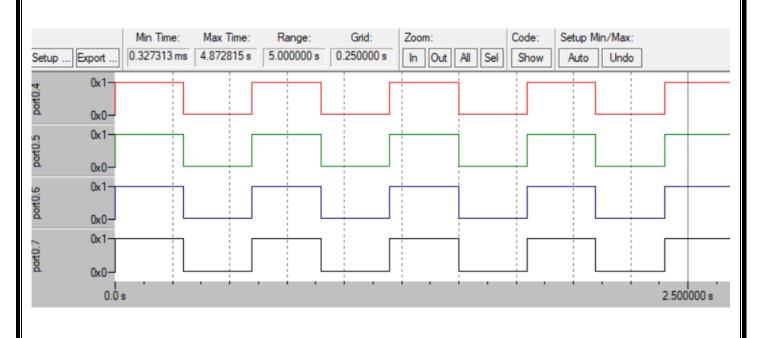
contains functions related to interfacing

- This folder is for application programmer.

 This folder contains all application programs
 i.e. main c code of program
- 37 Advantages of such Folder structure.
- main advantage is that programmers work with only folder which is associated with their work. Few other advantages are:
 - ci) Easy to understand
 - cit) Easy to debug of myn
 - chi) facy to modify

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	neithings and it was at printing but
2.4.2.	1) How many tasks are there in the application ?
	27 Do you find every task body has infinite loop? conite code of tasko.
	=> Yes, every task body has an intinite loop
	tarko code:
	Vaid Tours County and the 2
	Void Tosko (void *pdata)
	pdata = pdata;
100	while (1)
	(LED-ON(O);
	Ostime Dly (4);
- 613	Track of bottoms to desire and their
No. of the	(en-offco);
-5400	OSTime DIY (4);
amos por	2
	The same of the sa
	37 What does tasko do 1 of Tasko infinitely turns on g off the LED
	i.e. blinking of LED.
	The Division of

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2.4.3.	17 How to buit the project? The create project open main a file save it Built (F7) by clicking on built butto	n at top	
	How to open the logic analyzer wind add signals ? > Buit the project - Debug the project New window will open click on analysis window will be opened to be analysed window will be opened click on setup icon at top left corner name of signal -> press enter -> clo	ed.	
	37 Did you do any mistakes while add to logic analyser window ? >> No mistakes were done.	ing signal	'v
2.4.4.	Paste screenshot of observed waveform	n .	



- 27 write your observations about the waveform

 Though all tacks are having infinite loop
 they are executing simultaneously i.e. parallely.

 All the tacks are executed parallely i.e. it
 supports multiprogramming.
- 87 concusion you draw from observations.
- executes multiples processes simultaneously i.e.

 it supports parallelism.

2.45. Time of tasks before editing tasks

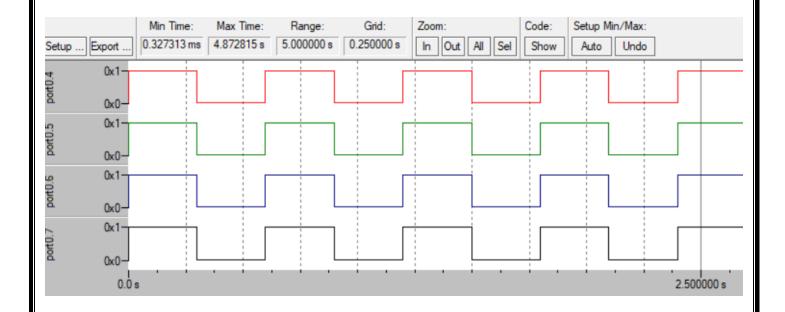
<u> </u>

Time of tasks after editing tasko. (delay = 6 for tasko)

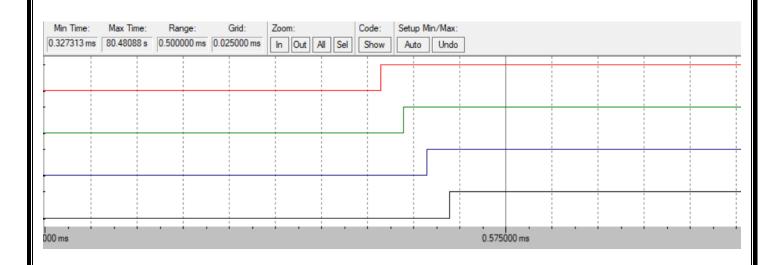
on period	off period
tasko : 1.20 3	tasko : 1.80 s
task1 : 0.801	task1 : 1.20 s
task2 : 0.80 s	task2 : 1.20s
tasks : 0.80s	task3 1.20s

O Conclusion: Change in one task will not affect the other tasks. So we can conclude that all tasks are executing independently.

Before Zooming



After Zooming



```
2.4.6. Screenshots of before zooning & after zooming
      are shown above.
      · contact conclusion can you draw from observations ?
      =) Even though the tasks appear to be
        minning simultaneously they are not executing at
         same time. There is some delay between them.
         Delay is due to context switching.
        write the reason of this delay. It you made
        any measurements, write them.
            There is only one processor & there are
         4 tasks that myn at same time. main reason
         for delay is the content switching.
         Measurements: tarko: 1,600355 5
                       task1 : 1.600369 5
                       task 2 : 1.600383 s
                       tasks : 1.600397 c
      · What do there measurements indicate &
      > The measurements indicate that there is
        a very little time difference beth tasks, so
         all tarks are not executing at the same time.
         nelay is so little that it seems like all
         processes are running parallely i.e. at same time
```

```
ied1.c ii Os_cpu_c.c x
 092
           void SWI Exception (int SWI Num, int *Regs)
093 - {
        OS TCB *ptcb;
 094
 095
        unsigned int temp; //ôôç°µÄkeil for 2.5 »ò ADS1.2Ĩ£¬Õâ
 096
                                //Ö±15ÓÓÃRO4 '¿É£¬£¬
 097
       switch (SWI Num)
098
 099
                                            /* EÎÎÑÇĐ»»° ÊÝOS TASK :
 100
             //case 0x00:
 101
            // break;
                                            /* Æô¶ ÈÎÎñ° ÊýOSStartH:
 102
             //case 0x01:
 103
            // break;
                                            /* 'ØÖжͰ ÊÝOS_ENTER_CI
 104
             case 0x02:
```

	Programa: States
	17 Run the project of stop it. Did it open some other file of showed the code execution point in that file & Paste the screenshot. >> Yes, when project was stopped, so other file was opened of showed execution point in that file.
	2) What conclusion can you draw from the observations =) Every time we stop the execution of project, some new file is opened up & execution point is highlighted. The meason for this is source code of operating system is part of project we are sunning of these source code files are also compiled with application program
2.4.8	17 open any os file (from ucos folder) a create
	a small syntax error compile the project what did you observe. The compiler throws an error \$ the target is not created
	27 What conclusion can you draw from observations? 3) operating system source code files are also compiled along with application code & hex file is created.
2.4.9	them
5	

2.4.10 Write your opinion about whether final hex file contains the os code as well as application code Justify your opinion. > It is true that her file contains the code of os as well as application. Since we have seen that by creating error in or folder File it will create a error of hex target is not created. 2.4.11 Write your opinion about whether the RTDS muns the procession where the her file will be downloaded (along with application code). Justify your opinion yes, RTDS mus on processor where the her file will be downloaded. When we stop the execution, we see that execution point is shown in the processor file of not in the application code or in as tile so when we apload the hex file on board, it will run on processor 2.4.12 Type any task without copy parte. Built & my the project write your experience below. A first I commented out the tarks & then tried to type it by myself without seeing the code. Initially I struggled at syntax as most of the functions are predefined so it is difficult to memorize, so I took help from tarks code then compiled the code & saw the waveform later I changed delay 2-3 times & noticed the change in the waveform duty cycle

2.4.13	modify the vert contten task for creating
	different waveforms pattern. Build & run the
	project.
	O write your experience
	=> I generated different - different waveforms
	having different duty cycles by changing delays
	in the code.
	@ Paute screenshot.

