| AVIOL | Lab Assignment - 4 Date: VOUVA |
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| | Proving that ucos-II uses a preemptive ternel |
| - CP1 | Name: Durverh Namesh Patil PRN: 2019BTEEN00035 |
| 4.8.1 | Programative of sometime programative |
| | Preemptive & non-preemptive kernel |
| 17 | From os? |
| ho mo | the os. context switching is provided by the kernel It is also a system program. It is |
| (0-31) | part of os which convert user commands into machine language. |
| epide i | trates a server relation to the contract |
| 4.4 | of the os |
| \$ V | user & hardware application & hardware |
| 27 | write brief information of Preemptive & non-preemptive kerner. |
| =) | preemptive ternel: Preemptive ternel schedules the task as per the |
| | priority of the tasks. When higher priority task armives it preempts the lower priority task g schedules the higher priority task. |
| | schedules, the figure, pro- |

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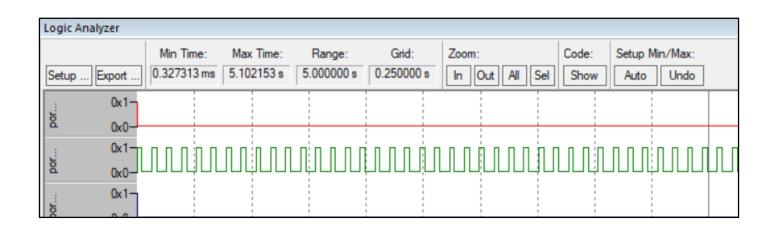
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|) = | Yes, we can use the same program. But |
| W. H. LL | to prove that was-I is preemptive as |
| 1-61 | to cer do come modifications. |
| | He need to write user defined function |
| - | so that we need to comite user defined CPU |
| | will get engaged in executing that & observe whether lower priority task preempts or not. |
| | collettier 1860 phoning to 1 |
| | returns the city of the control of t |
| 27 | Write steps in creating the modified program |
| 7 | 17 Declare & define the my Delay () Function |
| | above mainer Function which is user |
| | defined function |
| | 27 (all the my Delay () Function in the lower priority tauk |
| 1.004 | 3> observe the waveforms in the logic analyzer |
| | window. |
| | can be para with the second of the |
| 2.5 | a commentation and account the first |
| • | Winte program with proper indenting, syntar 4 |
| - | also taking come of car case sensitivity of |
| - | c language |
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```
#include "config.h"
#include "stdlib.h"
#include <stdio.h>
#define
            TaskStkLengh
                               64
                                                         //Define the Task0 stack length
OS STK
            TaskStk0 [TaskStkLengh];
                                                  //Define the Task stack
                                                  //Define the Task stack
OS_STK
            TaskStk1 [TaskStkLengh];
void
      Task0(void *pdata);
      Task1(void *pdata);
void
void myDelay()
{
      unsigned int i;
      for(i=0;i<65000;i++);
}
char buffer[25];
int main (void)
{
      LED init();
      TargetInit();
      OSInit ();
      OSTaskCreate (Task0,(void *)0, &TaskStk0[TaskStkLengh - 1], 6);
      OSTaskCreate (Task1,(void *)0, &TaskStk1[TaskStkLengh - 1], 7);
      OSStart();
      return 0;
}
void Task0 (void *pdata)
                                                              /* Dummy data */
      pdata = pdata;
      while(1)
      {
            LED on(0); // All LEDs on
            OSTimeDly(3);
            LED off(0); // All LEDs off
            OSTimeDly(3);
      }
}
```

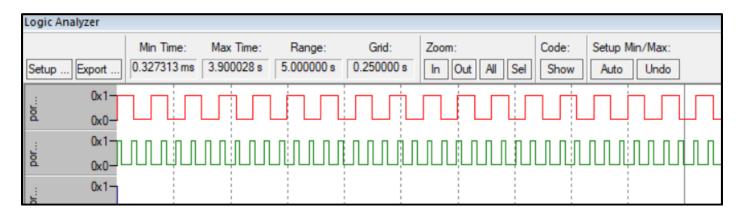
4.3 Observations:

Take screenshots of the simulated output for myDelay function used in HP task



Only higher priority task is running. Lower priority task will never get the CPU.

Take screenshots of the simulated output for myDelay function used in LP task



Both high and low priority tasks are executing.

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| Conclysion: | |
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| Describe the observations | Linto the coestion |
| conclusion with at manis | te reavoning for each of |
| the observation | re regulating for each of |
| | |
| | 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 |
| | in the lower priority tank |
| then both tasks i.e. | tasko & taski will min |
| parallely. When task! | armives it preempts the task |
| & when outimeply () | called in higher priority tack |
| then lower priority to | uk gets cpu |
| (ii) When my Delay () 450 | d in the higher priority tack |
| then only this tack | will run. Lower priority tack |
| will never get the | CPU. |
| (iii) wcos-II uses the | preemptive kernel. |
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