

# **Design of Band- gap Reference**

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### Band-Gap Reference Circuit

- A bandgap reference is a circuit widely used in analog and mixed-signal integrated circuits to generate a stable voltage that is largely independent of temperature, power supply variations, and manufacturing process changes.
- The concept is based on combining two voltages with opposite temperature coefficients.
- It is a vital analog building block used in many applications like Low Dropout voltage regulators, Analog to digital converter, Digital to analog converter, Buck converters etc. Compared to a voltage regulator, reference circuit lack current driving capability.
- As per industry standards, range of variation in temperature is considered as from  $-40^{\circ}\text{C}$  to  $125^{\circ}\text{C}$ . Range of supply variation depends on applications, typically 10% to 20% from the typical value of voltage supply.
- Basically 2 important variations are discussed in this report
  - i) Supply variations
  - ii) Temperature variations

### Effect of Temperature

- All electronic devices are sensitive to temperature variations.
- If voltage across a device increases with the increase in temperature, then such devices are called PTAT [Proportional to Absolute Temperature].
- If voltage decreases with the increase in temperature, then such devices are called CTAT [Complementary to Absolute Temperature]

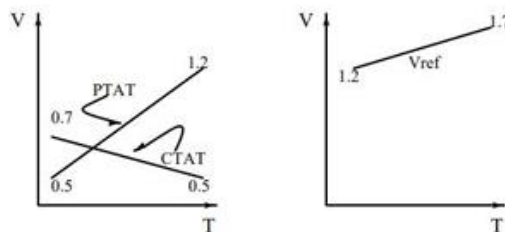


Fig. 1: (a) PTAT and CTAT responses (b) Response of voltage reference

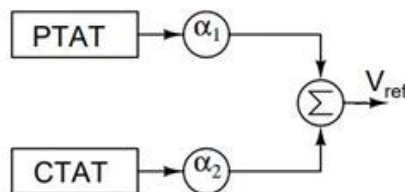


Fig. 2: Generation of  $V_{ref}$  voltage

## CTAT and PTAT Design

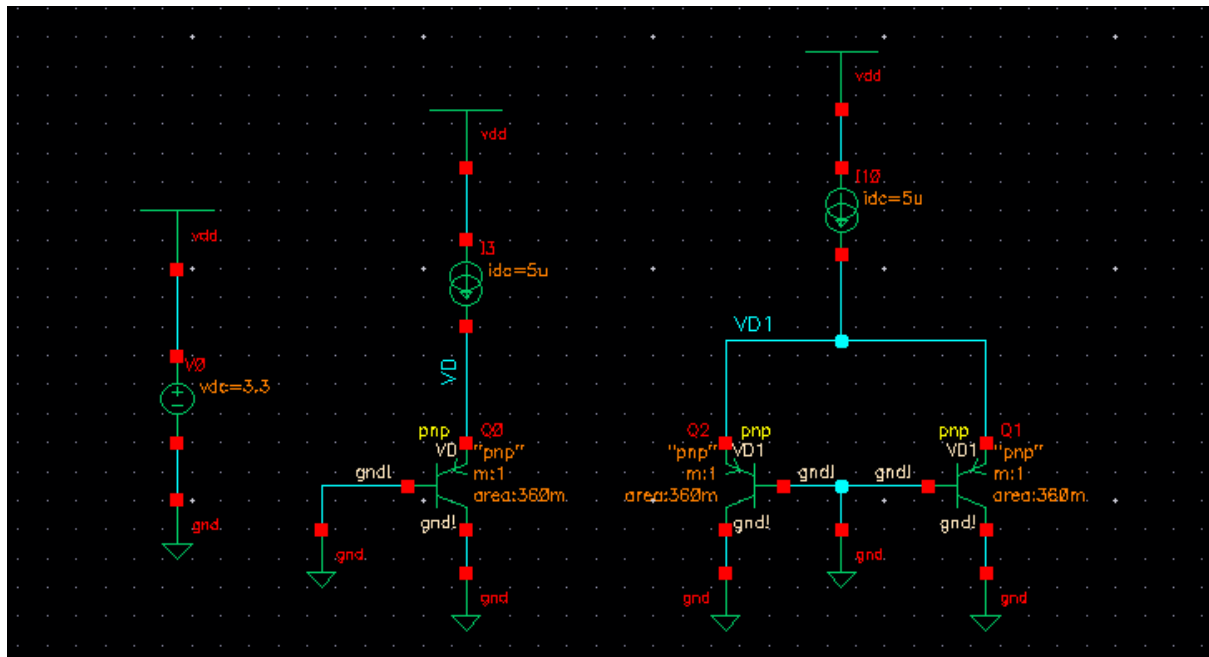


Fig3: Schematic of CTAT and PTAT design

### Final Circuit

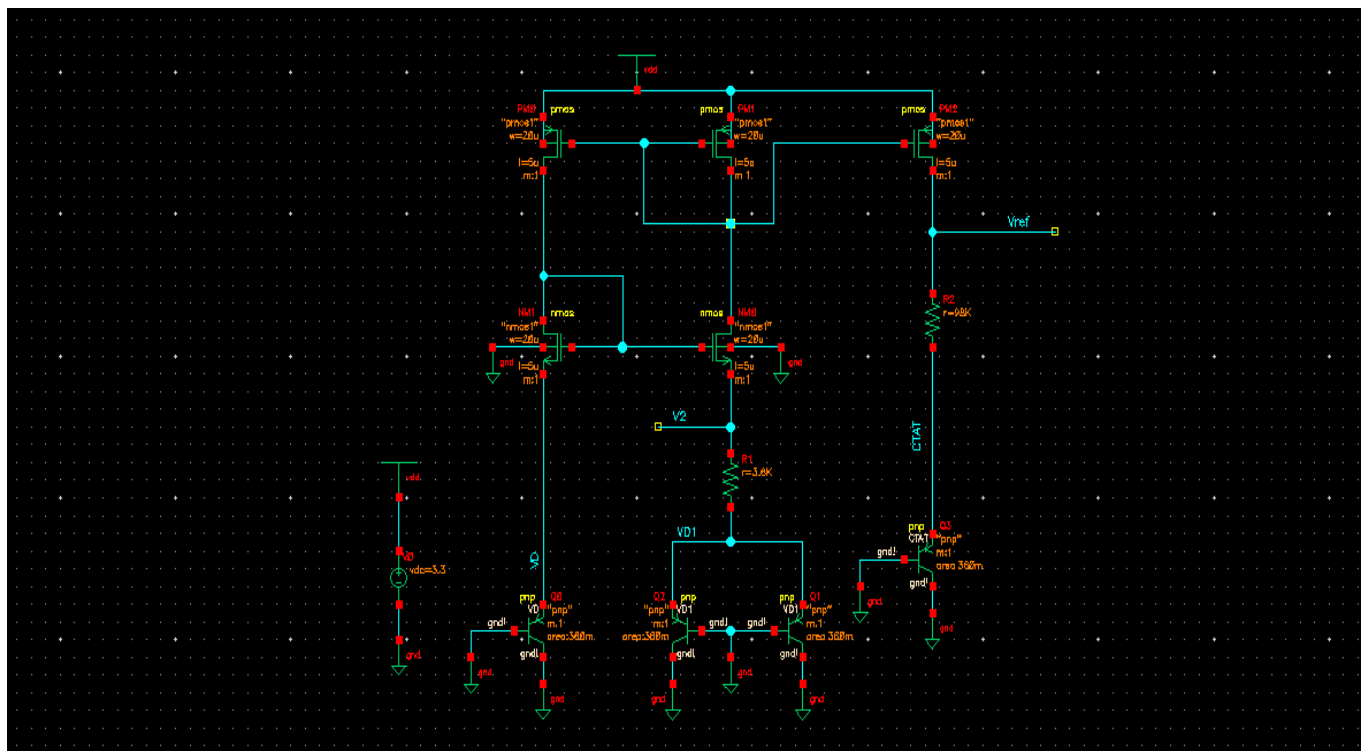


Fig 4: Schematic of the final circuit implementation

## Results:

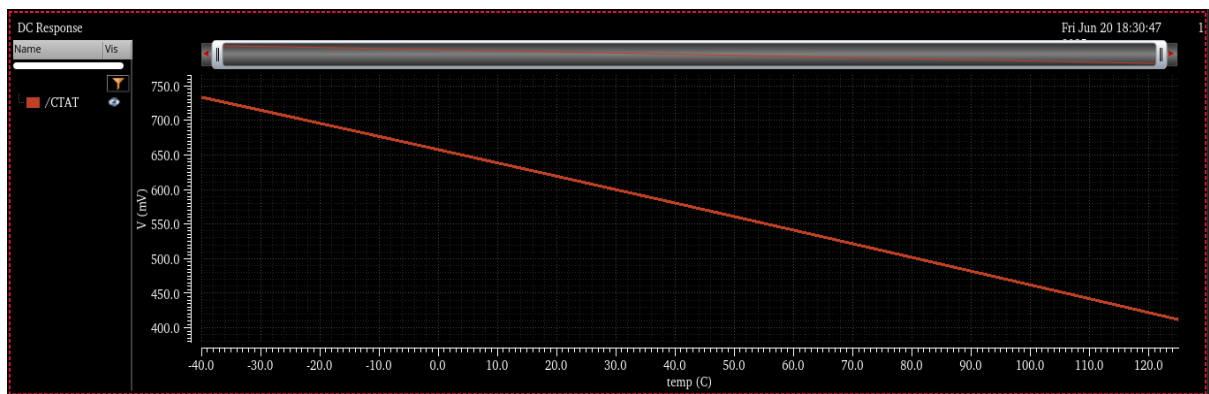


Fig5: CTAT graph

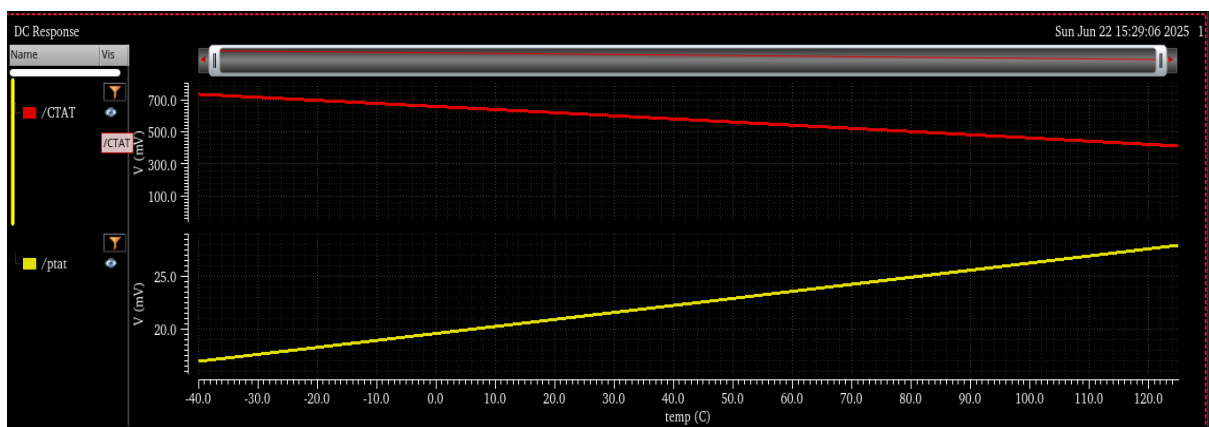


Fig5: CTAT and PTAT graph

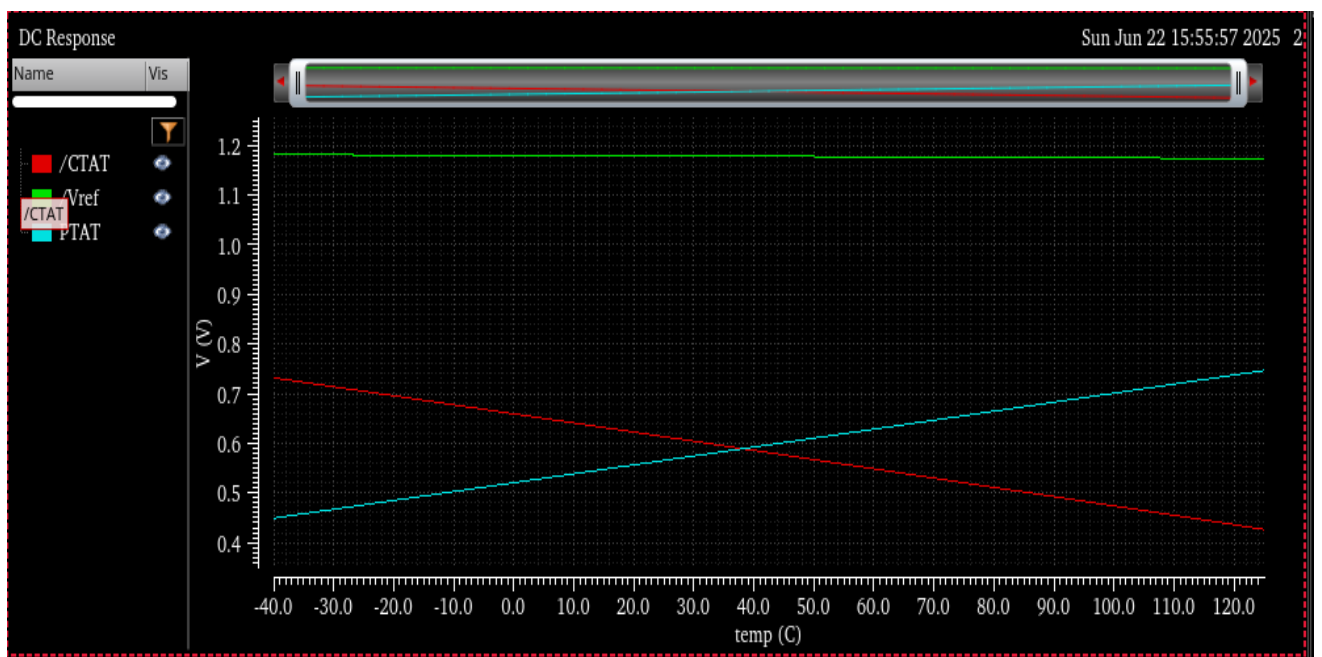


Fig 6: Final V reference voltage variation with Temperature

## Variation with supply voltage:

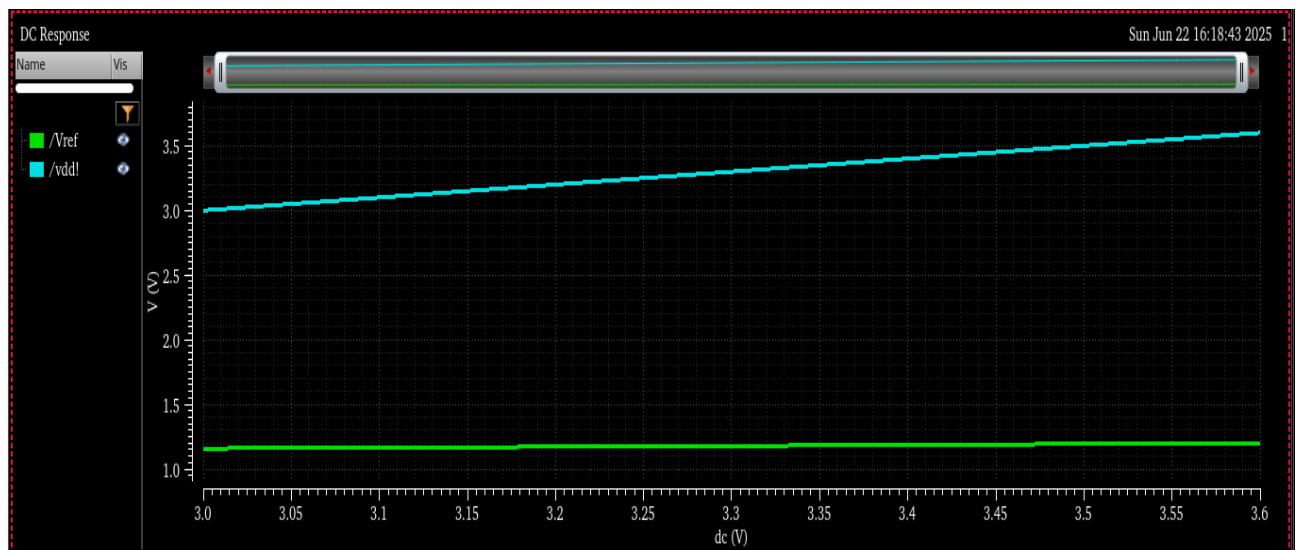
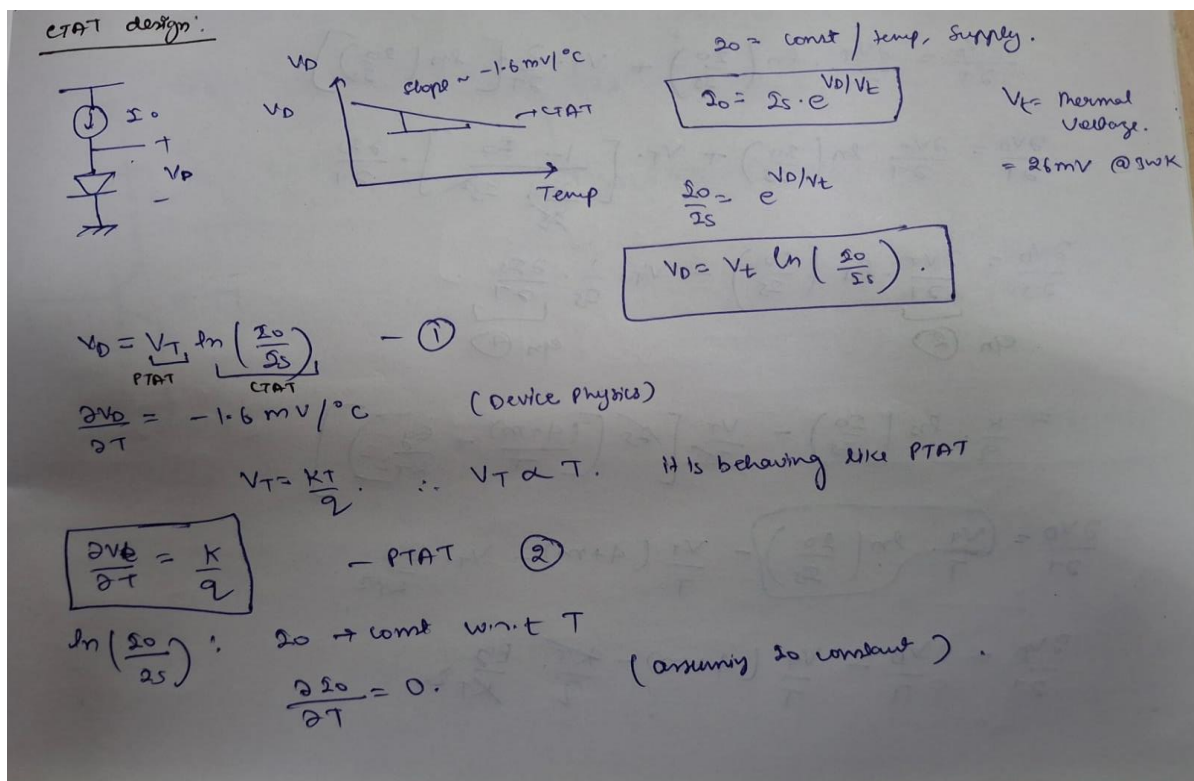


Fig 7: Variation with supply voltage

## Design methodology and discussion:

### CTAT Design



$$n_s = \frac{1}{4} K T \cdot n_i^2 \quad , \quad n_i^2 \propto T^3 \exp\left[-\frac{E_g}{K T}\right]$$

$\downarrow$   
 $n \propto H_0 T^m \quad m \approx -3/2$

$n_s$  is depending on Temp.

$$n_s \propto b T \cdot T^m \cdot T^3 \exp\left[-\frac{E_g}{K T}\right] = n_s \propto b T^{4+m} \exp\left[-\frac{E_g}{K T}\right] \quad (3)$$

$$\therefore n_s = b T^{(4+m)} \cdot \exp\left[-\frac{E_g}{K T}\right] \quad (3)$$

$$\frac{\partial n_s}{\partial T} = b \left[ T^{(4+m)} \cdot \exp\left[-\frac{E_g}{K T}\right] \cdot \frac{-E_g}{K} \cdot \frac{-1}{T^2} + \exp\left[-\frac{E_g}{K T}\right] \cdot (4+m) T^{(3+m)} \right]$$

$$= b \cdot \exp\left[-\frac{E_g}{K T}\right] \left[ T^{(4+m)} \cdot \frac{E_g}{K T^2} + (4+m) \cdot T^{(3+m)} \right]$$

$$\frac{\partial n_s}{\partial T} = n_s \cdot \left[ \frac{4+m}{T} + \frac{E_g}{K T^2} \right] \quad (4)$$

we know,  $V_D \approx V_T \ln\left(\frac{I_0}{I_s}\right)$

$$\frac{\partial V_D}{\partial T} \Rightarrow V_T, I_s, \frac{\partial V_T}{\partial T}, \frac{\partial I_s}{\partial T}$$

$$\therefore \frac{\partial V_D}{\partial T} = \frac{\partial V_T}{\partial T} \cdot \ln\left(\frac{I_0}{I_s}\right) + V_T \cdot \frac{\partial}{\partial T} \left[ \ln\left(\frac{I_0}{I_s}\right) \right]$$

$$\frac{\partial V_D}{\partial T} = \frac{\partial V_T}{\partial T} \ln\left(\frac{I_0}{I_s}\right) + V_T \cdot \left[ \frac{1 - \frac{I_0}{I_s}}{\frac{I_0}{I_s}} \right] \cdot \frac{\partial I_s}{\partial T}$$

$$\frac{\partial V_D}{\partial T} = \frac{\partial V_T}{\partial T} \cdot \ln\left(\frac{I_0}{I_s}\right) - V_T \cdot \frac{1}{I_s} \cdot \frac{\partial I_s}{\partial T}$$

eqn (2) eqn (4)

$$= \frac{K}{2} \cdot \ln\left(\frac{I_0}{I_s}\right) - \frac{V_T}{I_s} \left[ I_s \left[ \frac{(4+m)}{T} + \frac{E_g}{K T^2} \right] \right]$$

$$\frac{\partial V_D}{\partial T} = \frac{V_T}{T} \ln\left(\frac{I_0}{I_s}\right) - \frac{V_T}{T} (4+m) - V_T \cdot \frac{E_g}{K T^2}$$

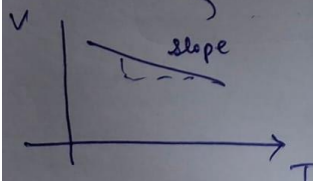
$$\frac{\partial V_D}{\partial T} = \frac{V_D}{T} - \frac{V_T}{T} (4+m) - \frac{K T}{2} \cdot \frac{E_g}{K T^2}$$

$$\therefore \frac{\partial V_D}{\partial T} = \frac{V_D}{T} - \frac{V_T}{T} (4+m) - \frac{E_g}{2T}$$

$$\therefore \frac{\partial V_D}{\partial T} = \frac{V_D - (4+m)V_T - E_g/2}{T}$$

slope of CTAT  
 $\therefore$  rate of change of  
 diode voltage with  
 Temp<sup>r</sup>.

$$\frac{\partial V_D}{\partial T} = \frac{0.7V - (4 - \frac{3}{2}) \cdot 26mV - 1.2}{300K}$$



$$= -1.88 mV$$

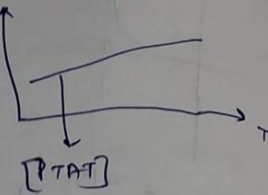
[ In beginning we say  
 slope =  $-1.6 mV / ^\circ K$ .



# PTAT DESIGN:

Any quantity, that increases with temperature linearly is called as Proportional to absolute Temp<sup>r</sup> [PTAT].

Our aim is to try to generate some quantity, which has direct proportionality to temp<sup>r</sup>.



$$\frac{KT}{2} \ln \left( \frac{I_1}{I_2} \times \frac{I_2}{I_1} \right)$$

make sure it does not vary with temp<sup>r</sup>.

basically we want to get rid of  $I_2$ , from we get direct proportionality to temp<sup>r</sup>.

$$= \frac{KT}{2} \ln \left( \frac{I_1}{I_2} \right) + \frac{KT}{2} \ln \left( \frac{I_2}{I_1} \right)$$

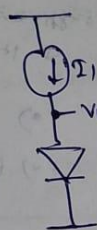
To get this voltage we passed the current  $I_1$  through diode.

To get this voltage, I should pass the current  $I_2$  to diode. (identical).

$$= \frac{KT}{2} \ln(n)$$

constant

now we have nice linear dependency on temp<sup>r</sup>.



$I_2 = I_1/n$  → it's easy to make copies of current source, so I will take n copies of  $I_2$ , so that  $I_2 = \frac{I_1}{n}$ .

If I have single diode voltage, that has CTAT behaviour →  $V_{EB1}$ .

But  $V_{EB2}$  should not change, I should put n diodes in parallel to.

$$\text{Then } \Delta V_{EB} = V_{EB1} - V_{EB2} \rightarrow \text{PTAT behaviour} \approx \frac{KT}{2} \ln(n).$$

To get a constant voltage, I had to add these 2.

$$V_{out} = V_{EB1} + K_p \times (V_{EB1} - V_{EB2}) = V_{EB1} + K_p \times \frac{KT}{2} \ln(n).$$

scaling factor