



VILNIAUS GEDIMINO TECHNIKOS UNIVERSITETAS

ELEKTRONIKOS FAKULTETAS

ELEKTRONINIŲ SISTEMŲ KATEDRA

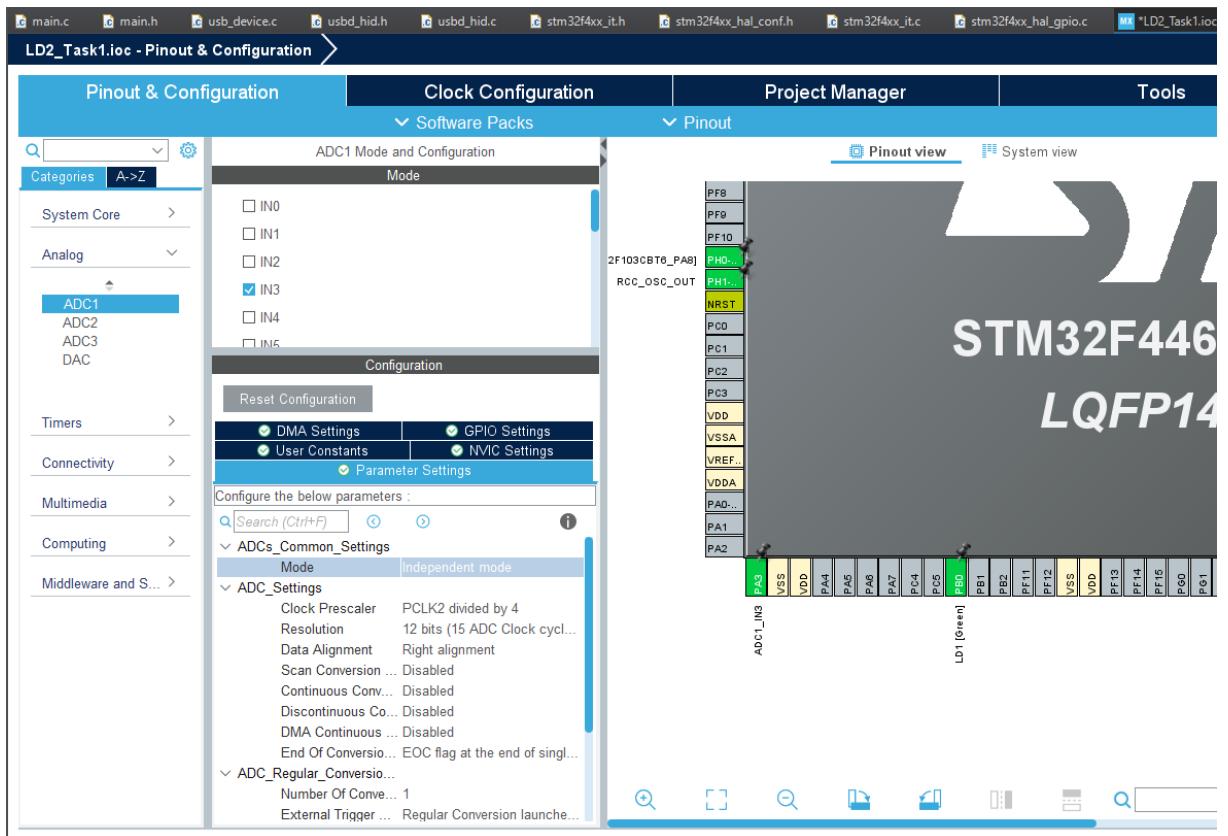
## **LABORATORINIS DARBAS 2**

Įterptinių sistemų inžinerija

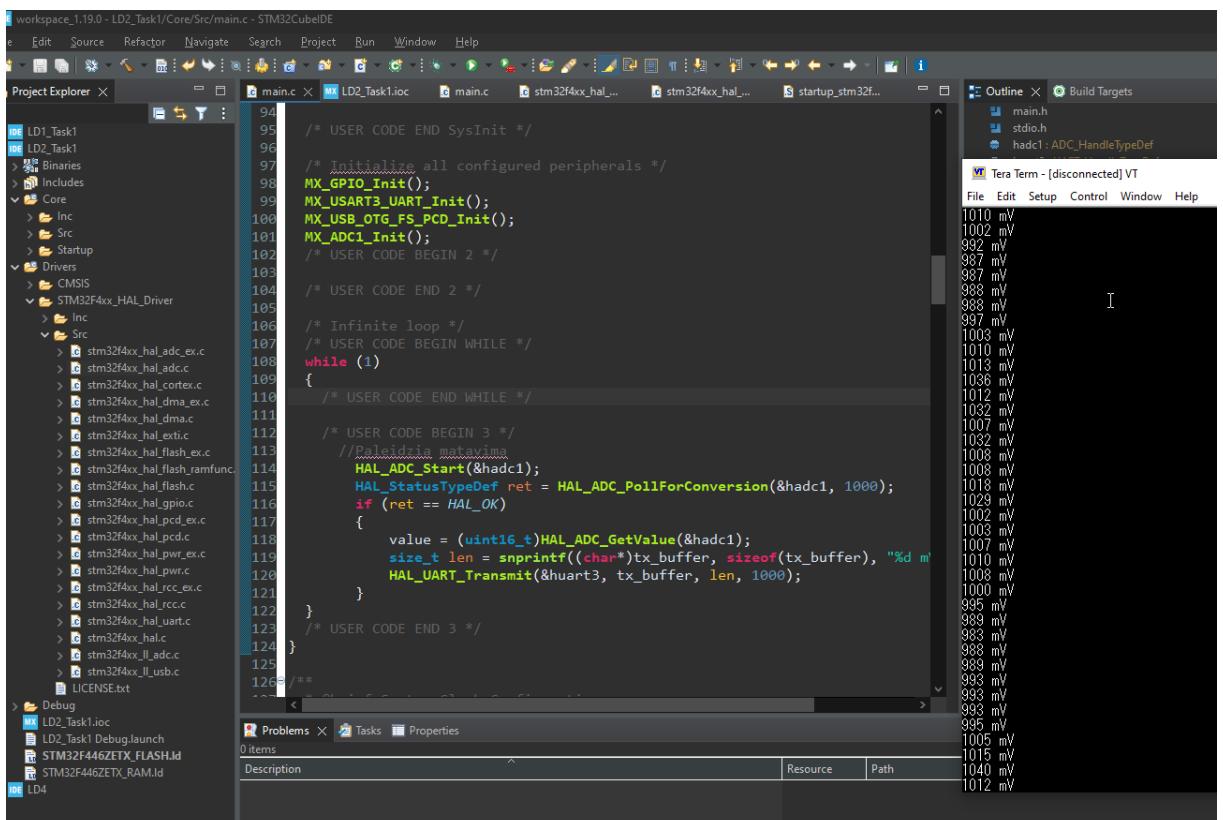
Atliko: EKSfm-24 gr. Ignas Malinauskas

Tikrino: dr. Eldar Šabanovič

# 1. LD\_Tas1 projekto konfigūracija



## 2. ADC (A0) patikrinimas be potenciometro per Tera Term



### **3. ADC nuskaitytos vertės iš potenciometro parodomos per UART ir multimetra**

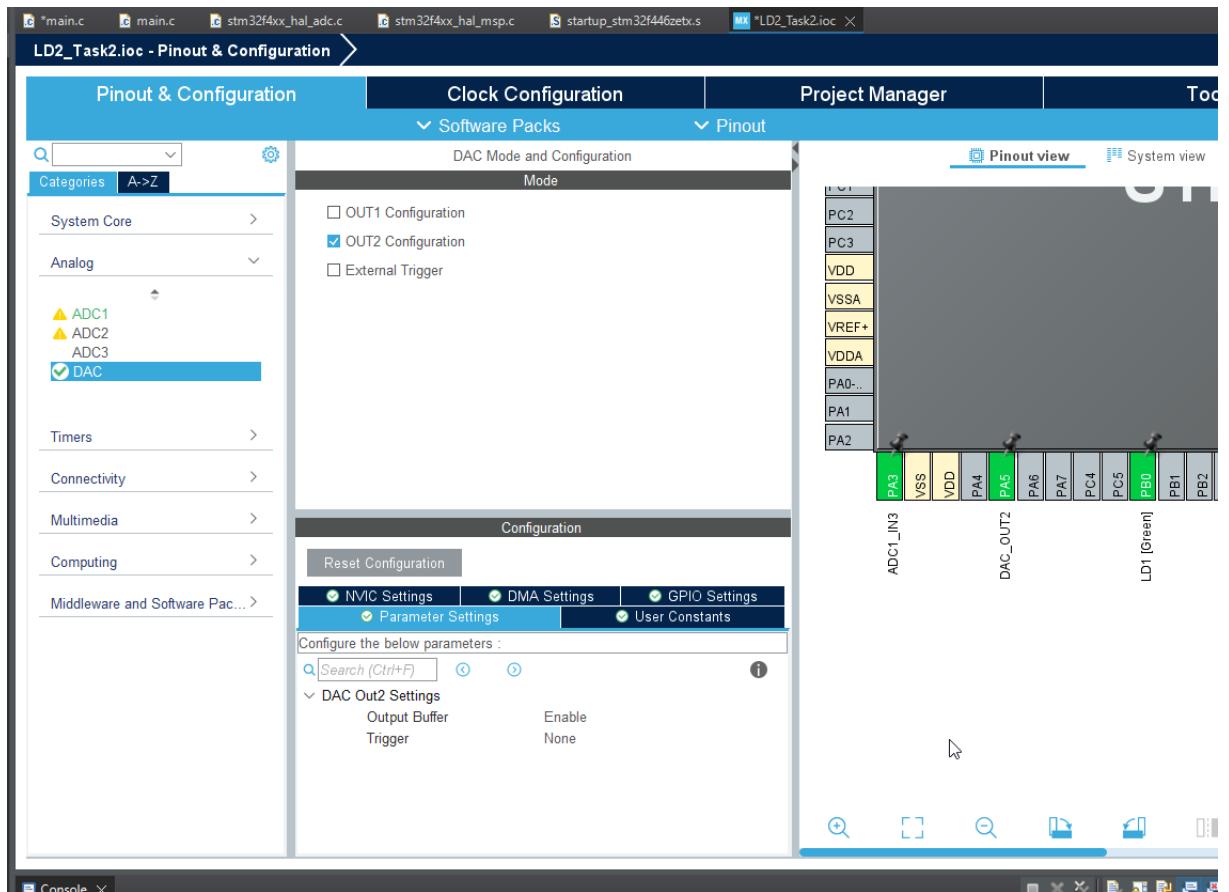
```
97  /* Initialize all configured peripherals */
98  MX_GPIO_Init();
99  MX_USART3_UART_Init();
100 MX_USB_OTG_FS_PCD_Init();
101 MX_ADC1_Init();
102 /* USER CODE BEGIN 2 */
103
104 /* USER CODE END 2 */
105
106 /* Infinite loop */
107 /* USER CODE BEGIN WHILE */
108 while (1)
109 {
110     /* USER CODE END WHILE */
111
112     /* USER CODE BEGIN 3 */
113     /* Paleidzia matavima */
114     HAL_ADC_Start(&hadc1);
115     HAL_StatusTypeDef ret = HAL_ADC_PollForConversion(&hadc1, 1000);
116     if (ret == HAL_OK)
117     {
118         value = (uint16_t)HAL_ADC_GetValue(&hadc1);
119         size_t len = snprintf((char*)tx_buffer, sizeof(tx_buffer), "%d mV\r\n", value * 3300 / 4095);
120         HAL_UART_Transmit(&huart3, tx_buffer, len, 1000);
121     }
122     HAL_Delay(100);
123 }
124 /* USER CODE END 3 */
125 }
126
127 /**
128  * @brief System Clock Configuration
<terminated> LD2_Task1 Debug [STM32 C/C++ Application] ST-LINK (ST-LINK GDB server) (Terminated Dec 18, 2025, 1:12:21 AM) [pid: 234]
Time elapsed during verifying operation: 00:00:00.252

Download verified successfully

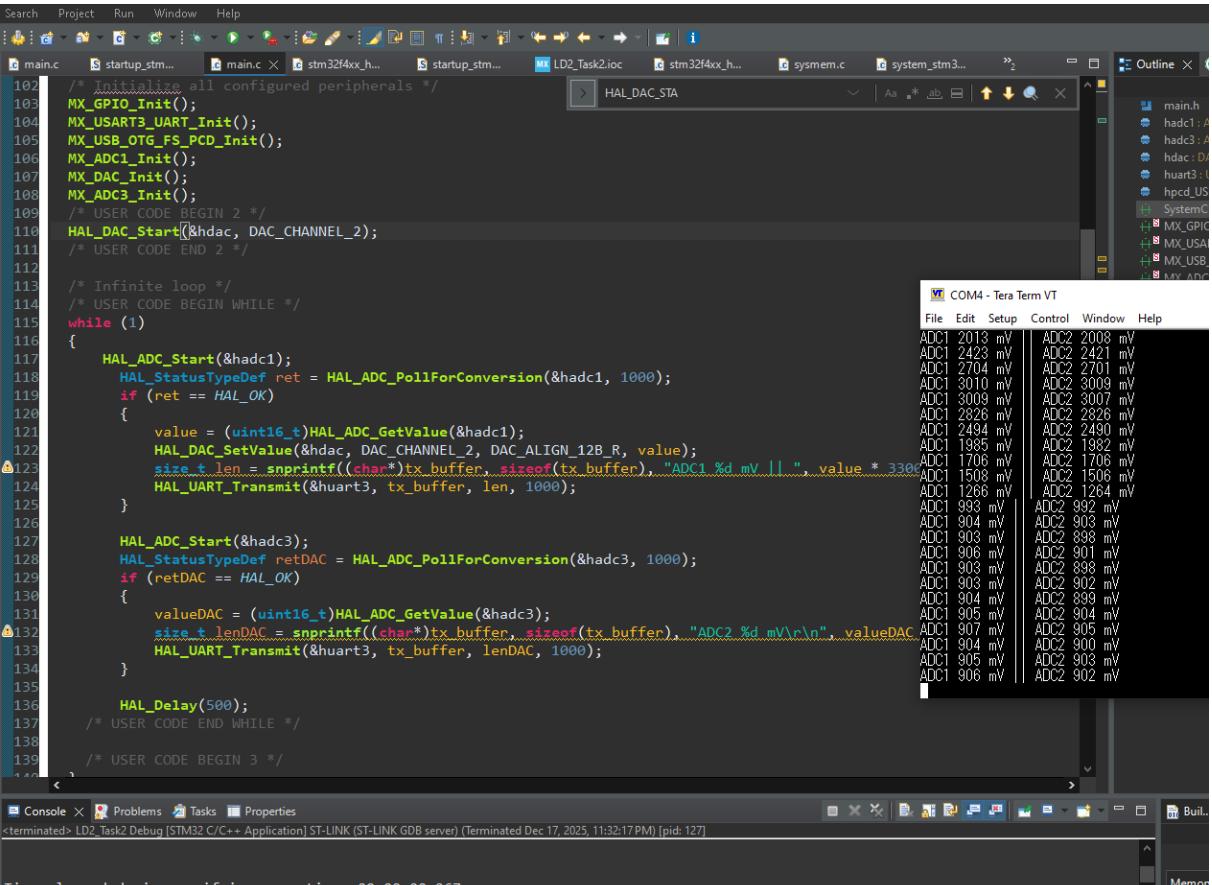
Shutting down...
Exit.
```

išmatuot su multimetru

## 4. LD2\_Task2 projekto konfigūracija



## 5. DAC (D13 (PA\_5)) išvesties kodas ir patikrinimas su ADC (A3(PF3))

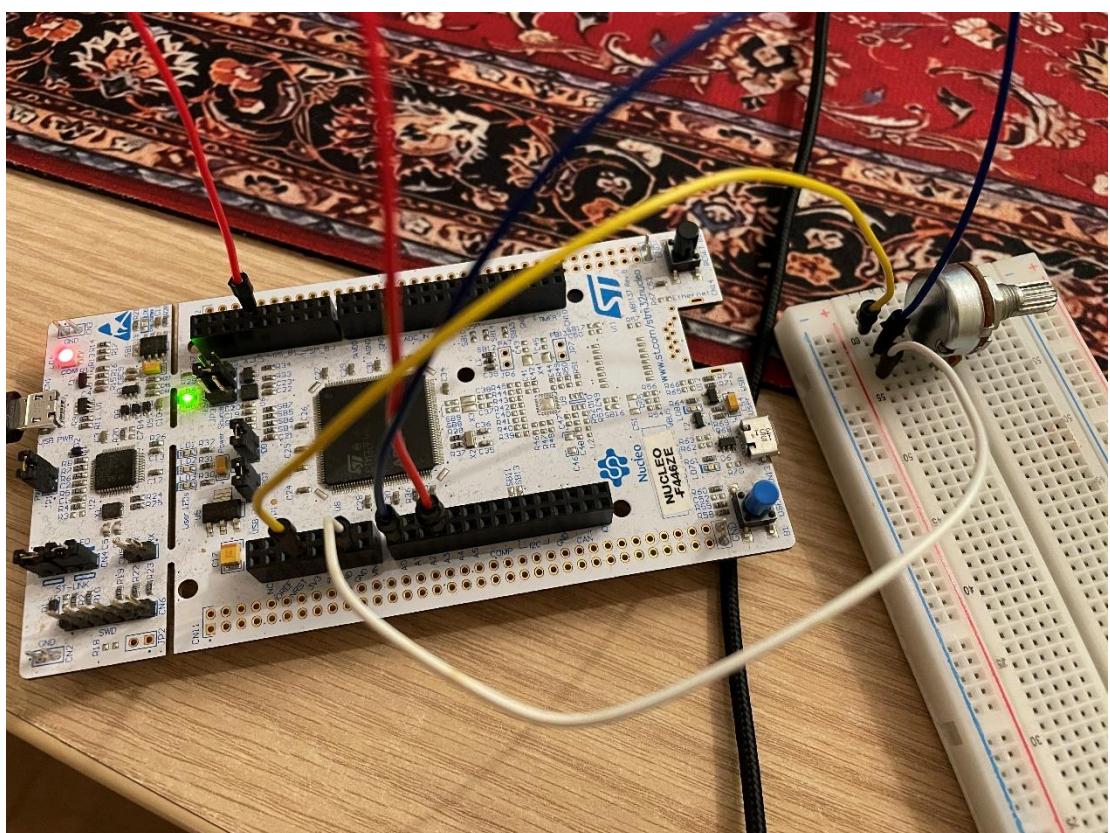


```

102  /* Initialize all configured peripherals */
103  MX_GPIO_Init();
104  MX_USART3_UART_Init();
105  MX_USB_OTG_FS_PCD_Init();
106  MX_ADC1_Init();
107  MX_DAC_Init();
108  MX_ADC3_Init();
109  /* USER CODE BEGIN 2 */
110  HAL_DAC_Start(&hdac, DAC_CHANNEL_2);
111  /* USER CODE END 2 */
112
113  /* Infinite loop */
114  /* USER CODE BEGIN WHILE */
115  {
116      HAL_ADC_Start(&hadc1);
117      HAL_StatusTypeDef ret = HAL_ADC_PollForConversion(&hadc1, 1000);
118      if (ret == HAL_OK)
119      {
120          value = (uint16_t)HAL_ADC_GetValue(&hadc1);
121          HAL_DAC_SetValue(&hdac, DAC_CHANNEL_2, DAC_ALIGN_12B_R, value);
122          size_t len = sprintf((char*)tx_buffer, sizeof(tx_buffer), "ADC1 %d mV || ", value * 330);
123          HAL_UART_Transmit(&huart3, tx_buffer, len, 1000);
124      }
125
126      HAL_ADC_Start(&hadc3);
127      HAL_StatusTypeDef retDAC = HAL_ADC_PollForConversion(&hadc3, 1000);
128      if (retDAC == HAL_OK)
129      {
130          valueDAC = (uint16_t)HAL_ADC_GetValue(&hadc3);
131          size_t lenDAC = sprintf((char*)tx_buffer, sizeof(tx_buffer), "ADC2 %d mV\r\n", valueDAC);
132          HAL_UART_Transmit(&huart3, tx_buffer, lenDAC, 1000);
133      }
134
135      HAL_Delay(500);
136  /* USER CODE END WHILE */
137
138  /* USER CODE BEGIN 3 */
139

```

The screenshot shows the Keil MDK-ARM IDE interface. The top window displays the C source code for the main application. To the right, there is a terminal window titled 'COM4 - Tera Term VT' showing a table of ADC conversion results. Below the main window is a smaller terminal window showing build logs. At the bottom, there is a status bar indicating the time elapsed during verification.



## **6. ) ADC DMA su pertrauktim kodas ir matavimai**

## 7. Skaičiavimai nekeičiant potenciometro pozicijos

	A	B	C	D	E	F	G	H
1	ADC matavaimai			ADC DMA suvidurkinti				
2				matavaimai				
3	2267 mV	2267		2251 mV	2251			
4	2267 mV	2267		2251 mV	2251	Vidurkis ADC	2267,019	
5	2269 mV	2269		2252 mV	2252	Vidurkis DMA	2251,630	
6	2266 mV	2266		2251 mV	2251			
7	2266 mV	2266		2252 mV	2252	Variacija ADC	2,471349	
8	2267 mV	2267		2252 mV	2252	Variacija DMA	0,237596	
9	2267 mV	2267		2252 mV	2252			
10	2266 mV	2266		2252 mV	2252			
11	2267 mV	2267		2252 mV	2252			
12	2266 mV	2266		2252 mV	2252			
13	2264 mV	2264		2252 mV	2252			
14	2268 mV	2268		2251 mV	2251			
15	2267 mV	2267		2252 mV	2252			
16	2270 mV	2270		2252 mV	2252			
17	2267 mV	2267		2251 mV	2251			
18	2270 mV	2270		2251 mV	2251			
19	2269 mV	2269		2252 mV	2252			
20	2266 mV	2266		2252 mV	2252			
21	2270 mV	2270		2252 mV	2252			
22	2270 mV	2270		2252 mV	2252			
23	2267 mV	2267		2251 mV	2251			
24	2266 mV	2266		2251 mV	2251			
25	2266 mV	2266		2252 mV	2252			
26	2266 mV	2266		2252 mV	2252			
27	2265 mV	2265		2251 mV	2251			
28	2267 mV	2267		2251 mV	2251			
29	2265 mV	2265		2252 mV	2252			
30	2266 mV	2266		2252 mV	2252			
31	2266 mV	2266		2252 mV	2252			
32	2266 mV	2266		2252 mV	2252			

Su DMA apskaičiuotos reikšmės tikslėnės, nes DMA neapkrauna procesoriaus ir atlieka nuskaitymus vienodo laiko intervale. Reikšmės nekeliauja per magistrale, tad dar papildomai sumažėja triukšmo bei paprasčiausiai apskaičiuojamas vidurkis sumažina atsitiktinį triukšmą.