



VILNIAUS GEDIMINO TECHNIKOS UNIVERSITETAS

ELEKTRONIKOS FAKULTETAS

ELEKTRONINIŲ SISTEMŲ KATEDRA

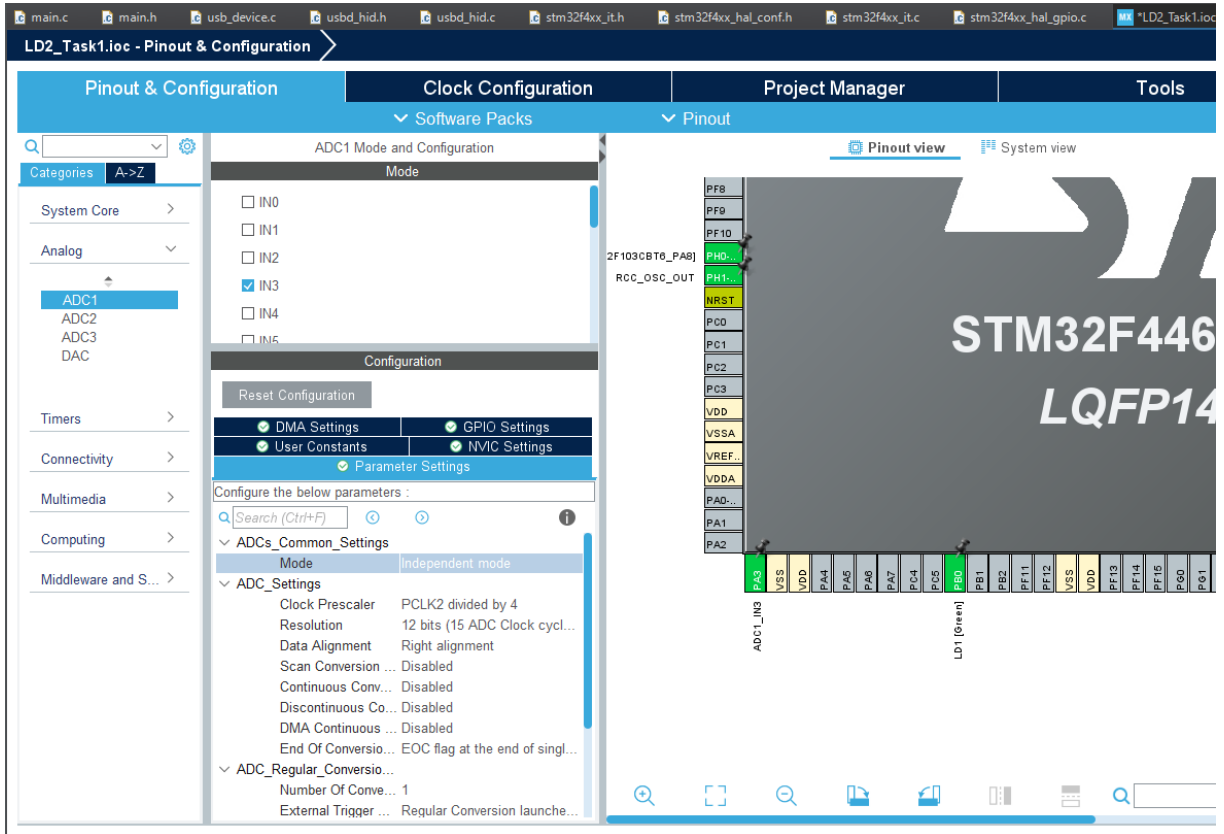
## **LABORATORINIS DARBAS 2**

Įterptinių sistemų inžinerija

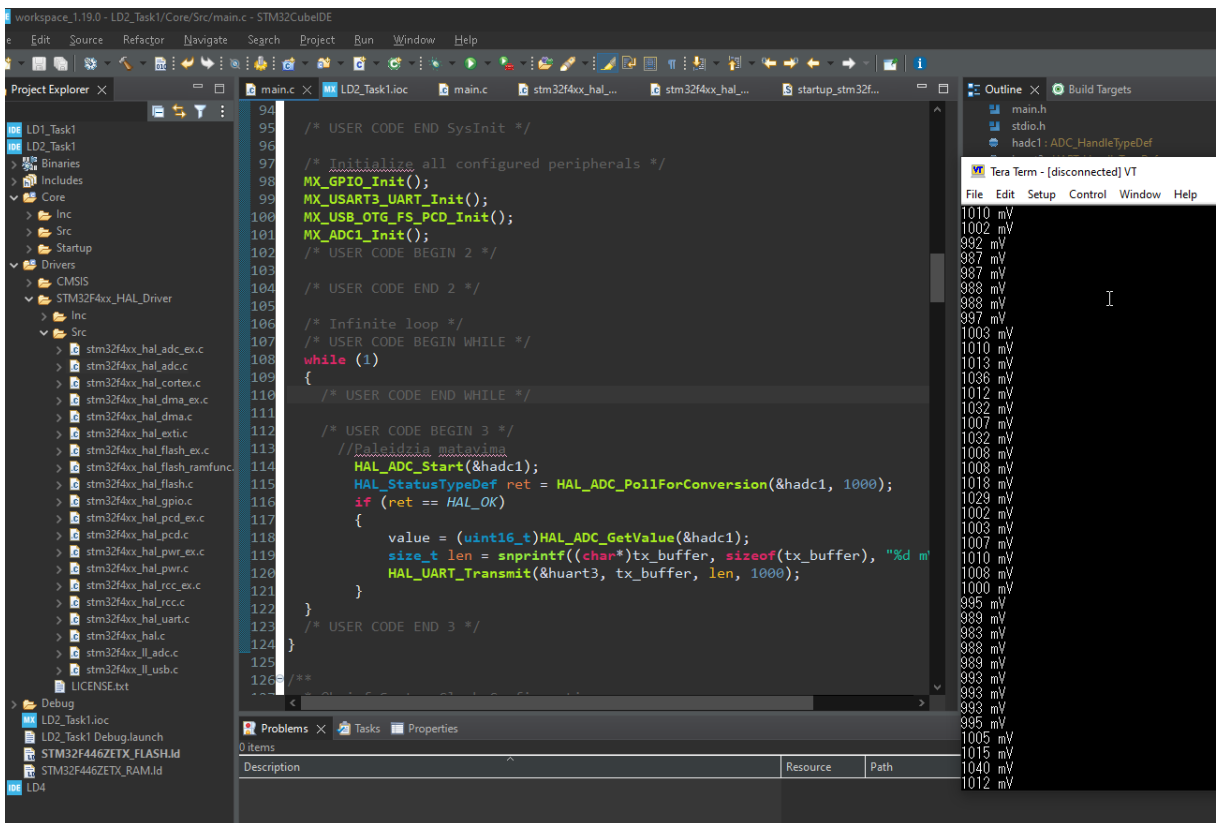
Atliko: EKSfm-24 gr. Ignas Malinauskas

Tikrino: dr. Eldar Šabanovič

## 1. LD\_Tas1 projekto konfigurācija



## 2. ADC (A0) patikrinimas be potenciometro per Tera Term

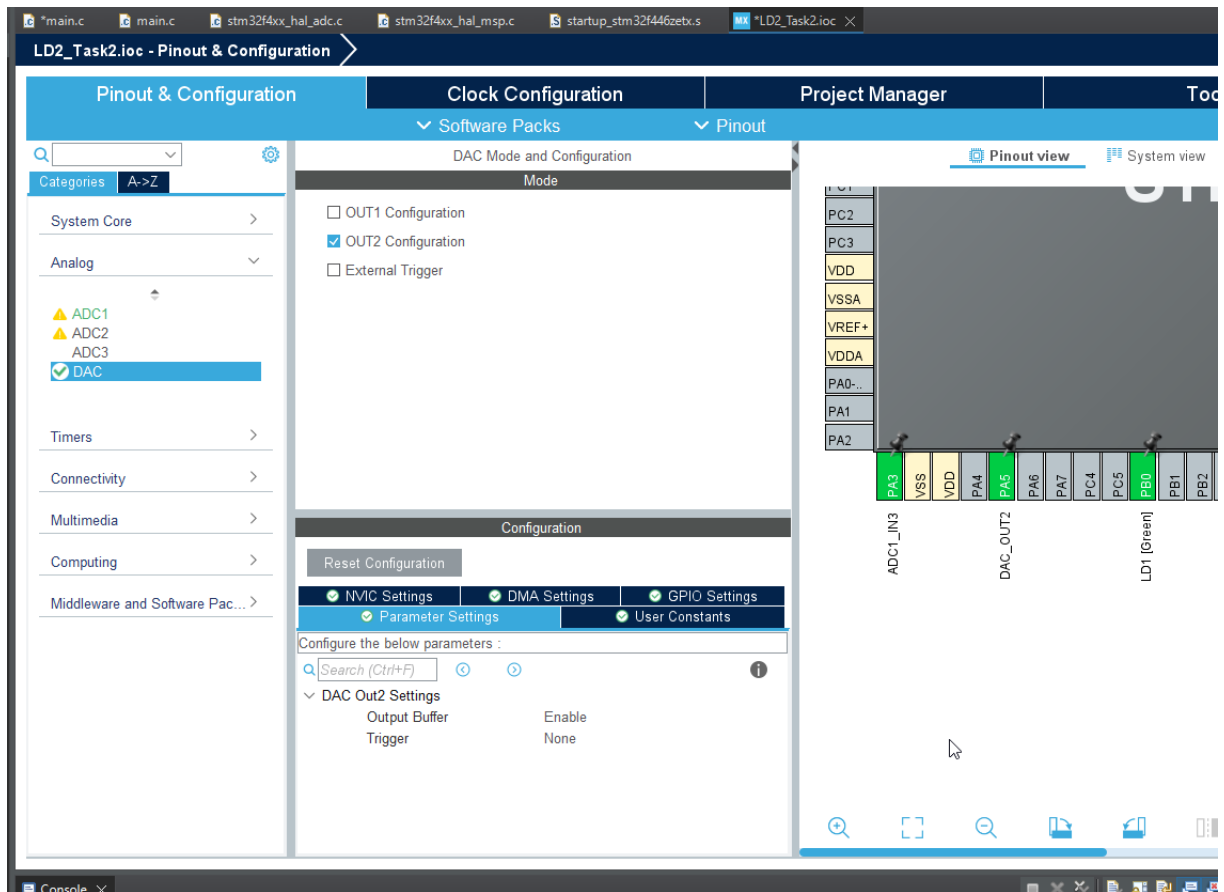


### 3. ADC nuskaitytos vertės iš potencimetro parodomos per UART ir multimetra

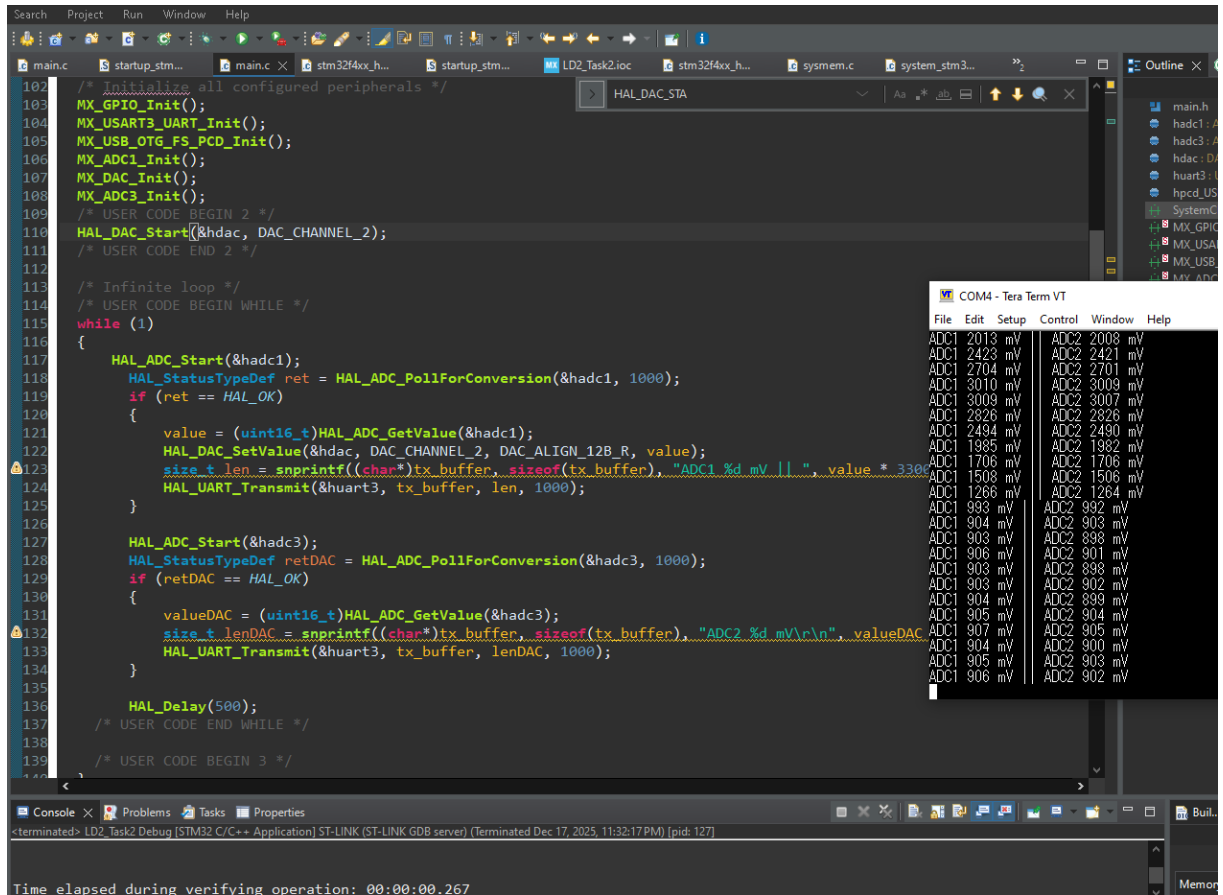
The image shows a screenshot of an IDE with a C code editor, a console window, and a status bar. The code editor displays a C program for STM32, including initialization of peripherals, a while loop for ADC conversion, and UART transmission. The console window shows the output of the program, including the time elapsed during the verifying operation and the download status. The status bar at the bottom indicates the program is terminated and provides the time elapsed during the verifying operation.

išmatuot su multimetru

## 4. LD2\_Task2 projekto konfigurācija



## 5. DAC (D13 (PA\_5)) išvesties kodas ir patikrinimas su ADC (A3(PF3))

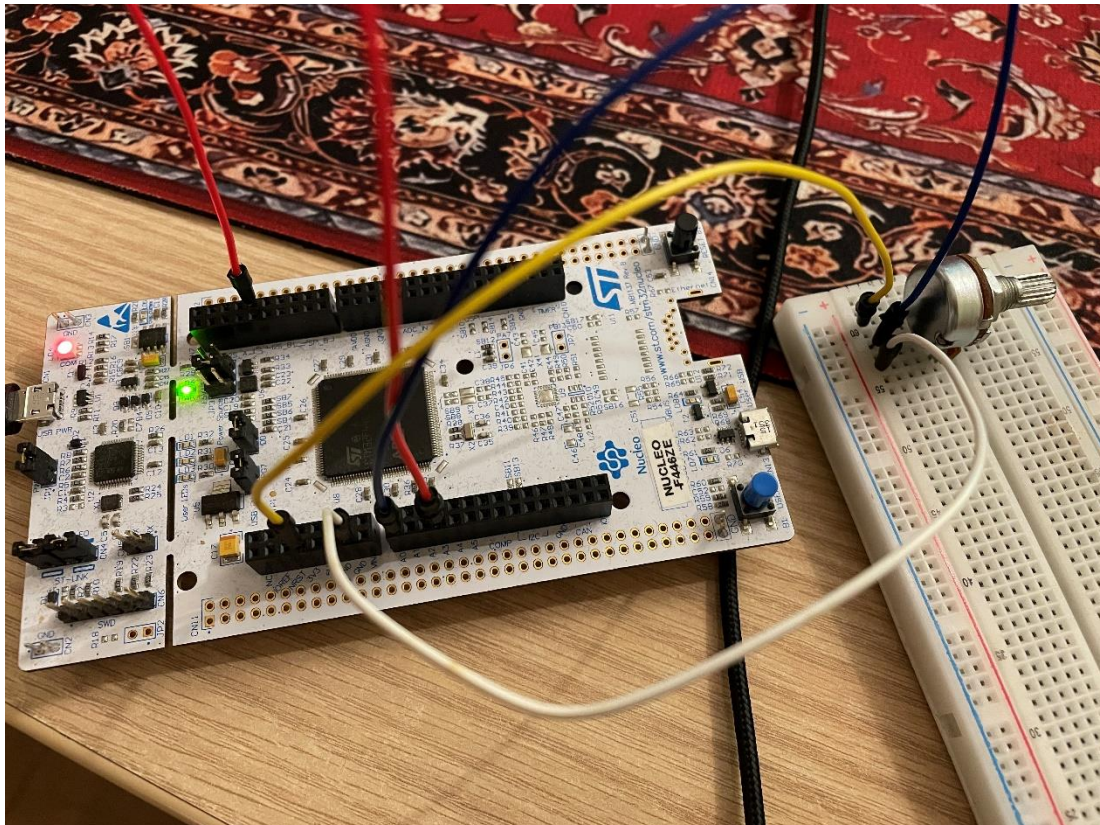


```
102 /* Initialize all configured peripherals */
103 MX_GPIO_Init();
104 MX_USART3_UART_Init();
105 MX_USB_OTG_FS_PCD_Init();
106 MX_ADC1_Init();
107 MX_DAC_Init();
108 MX_ADC3_Init();
109 /* USER CODE BEGIN 2 */
110 HAL_DAC_Start(&hdac, DAC_CHANNEL_2);
111 /* USER CODE END 2 */
112
113 /* Infinite loop */
114 /* USER CODE BEGIN WHILE */
115 while (1)
116 {
117     HAL_ADC_Start(&hadc1);
118     HAL_StatusTypeDef ret = HAL_ADC_PollForConversion(&hadc1, 1000);
119     if (ret == HAL_OK)
120     {
121         value = (uint16_t)HAL_ADC_GetValue(&hadc1);
122         HAL_DAC_SetValue(&hdac, DAC_CHANNEL_2, DAC_ALIGN_12B_R, value);
123         size_t len = snprintf((char*)tx_buffer, sizeof(tx_buffer), "ADC1 %d mV || ", value * 330);
124         HAL_UART_Transmit(&huart3, tx_buffer, len, 1000);
125     }
126
127     HAL_ADC_Start(&hadc3);
128     HAL_StatusTypeDef retDAC = HAL_ADC_PollForConversion(&hadc3, 1000);
129     if (retDAC == HAL_OK)
130     {
131         valueDAC = (uint16_t)HAL_ADC_GetValue(&hadc3);
132         size_t lenDAC = snprintf((char*)tx_buffer, sizeof(tx_buffer), "ADC2 %d mV\r\n", valueDAC);
133         HAL_UART_Transmit(&huart3, tx_buffer, lenDAC, 1000);
134     }
135
136     HAL_Delay(500);
137     /* USER CODE END WHILE */
138
139     /* USER CODE BEGIN 3 */
140
```

COM4 - Tera Term VT

ADC1	ADC2
2013 mV	2008 mV
2423 mV	2421 mV
2704 mV	2701 mV
3010 mV	3009 mV
3009 mV	3007 mV
2826 mV	2826 mV
2484 mV	2480 mV
1985 mV	1982 mV
1706 mV	1706 mV
1508 mV	1506 mV
1266 mV	1264 mV
993 mV	992 mV
904 mV	903 mV
903 mV	898 mV
906 mV	901 mV
903 mV	892 mV
903 mV	890 mV
904 mV	899 mV
905 mV	904 mV
907 mV	905 mV
904 mV	900 mV
905 mV	903 mV
906 mV	902 mV

Time elapsed during verifying operation: 00:00:00.267



## 6. ) ADC DMA su pertrauktim kodas ir matavimai

The screenshot displays the STM32CubeIDE environment. The main window shows the C source code for `main.c`. The code is structured as follows:

```

/* USER CODE BEGIN 2 */
HAL_DAC_Start(&hdac, DAC_CHANNEL_2);
HAL_ADC_Start_DMA(&hadc1, &value, 32);
/* USER CODE END 2 */

/* Infinite loop */
/* USER CODE BEGIN WHILE */
while (1)
{
    if (conversion_ended)
    {
        conversion_ended = 0;
        uint32_t value_acc = 0;
        uint8_t i;
        for (i = 0; i < 32; i++)
        {
            value_acc += value[i];
        }
        HAL_ADC_Start_DMA(&hadc1, &value, 32);
        value_avg = value_acc / 32;

        HAL_DAC_SetValue(&hdac, DAC_CHANNEL_2, DAC_ALIGN_12B_R, value_avg);
        size_t len = snprintf((char*)tx_buffer, sizeof(tx_buffer), "%d mV\r\n", value_avg * 3300 / 4095);
        HAL_UART_Transmit(&huart3, tx_buffer, len, 1000);

    }
    HAL_Delay(100);
}
/* USER CODE END WHILE */

```

The bottom console area shows the following messages:

```

<terminated> LD2_Task3 Debug [STM32 C/C++ Application] ST-LINK (ST-LINK GDB server) (Terminated Dec 18, 2025, 1:30:45 AM) [pid: 255]
Time elapsed during verifying operation: 00:00:00.205

Download verified successfully

Shutting down...
Exit.

```



## 7. Skaičiavimai nekeičiant potenciometro pozicijos

	A	B	C	D	E	F	G	H
1	ADC matavimai			ADC DMA suvidurkinti				
2				matavimai				
3	2267 mV	2267		2251 mV	2251			
4	2267 mV	2267		2251 mV	2251		Vidurkis ADC	2267,019
5	2269 mV	2269		2252 mV	2252		Vidurkis DMA	2251,630
6	2266 mV	2266		2251 mV	2251			
7	2266 mV	2266		2252 mV	2252		Variacija ADC	2,471349
8	2267 mV	2267		2252 mV	2252		Variacija DMA	0,237596
9	2267 mV	2267		2252 mV	2252			
10	2266 mV	2266		2252 mV	2252			
11	2267 mV	2267		2252 mV	2252			
12	2266 mV	2266		2252 mV	2252			
13	2264 mV	2264		2252 mV	2252			
14	2268 mV	2268		2251 mV	2251			
15	2267 mV	2267		2252 mV	2252			
16	2270 mV	2270		2252 mV	2252			
17	2267 mV	2267		2251 mV	2251			
18	2270 mV	2270		2251 mV	2251			
19	2269 mV	2269		2252 mV	2252			
20	2266 mV	2266		2252 mV	2252			
21	2270 mV	2270		2252 mV	2252			
22	2270 mV	2270		2252 mV	2252			
23	2267 mV	2267		2251 mV	2251			
24	2266 mV	2266		2251 mV	2251			
25	2266 mV	2266		2252 mV	2252			
26	2266 mV	2266		2252 mV	2252			
27	2265 mV	2265		2251 mV	2251			
28	2267 mV	2267		2251 mV	2251			
29	2265 mV	2265		2252 mV	2252			
30	2266 mV	2266		2252 mV	2252			
31	2266 mV	2266		2252 mV	2252			
32	2266 mV	2266		2252 mV	2252			

Su DMA apskaičiuotos reikšmės tikslesnės, nes DMA neapkrauna procesoriaus ir atlieka nuskaitymus vienodo laiko intervale. Reikšmės nekeliauja per magistrale, tad dar papildomai sumažėja triukšmo bei paprasčiausiai apskaičiuojamas vidurkis sumažina atsitiktinį triukšmą.