



VILNIAUS GEDIMINO TECHNIKOS UNIVERSITETAS

ELEKTRONIKOS FAKULTETAS

ELEKTRONINIŲ SISTEMŲ KATEDRA

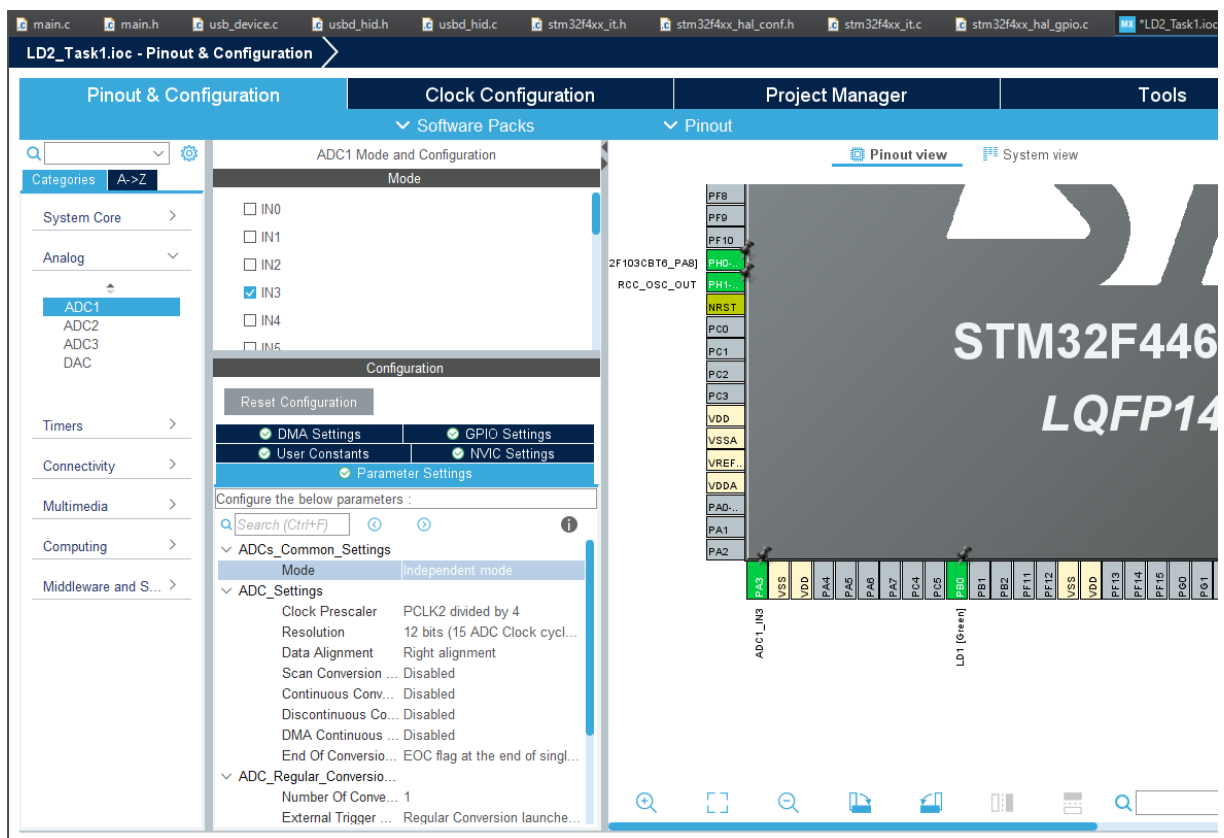
## **LABORATORINIS DARBAS 2**

Įterptinių sistemų inžinerija

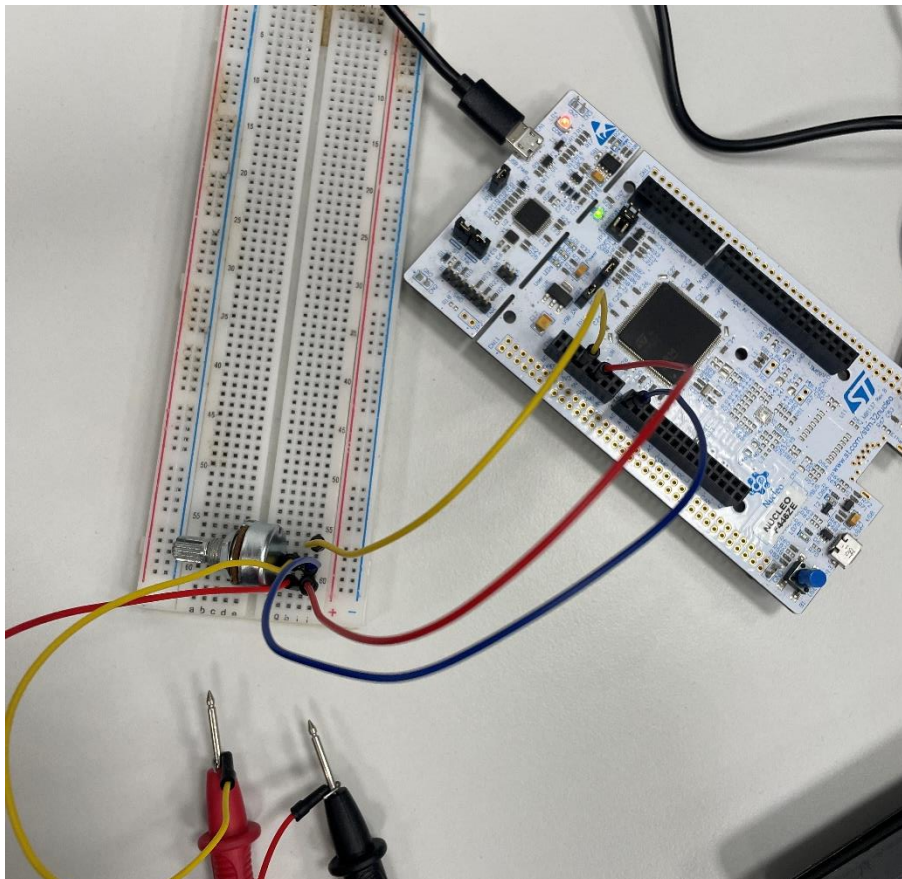
Atliko: EKSfm-24 gr. Ignas Malinauskas

Tikrino: dr. Eldar Šabanovič

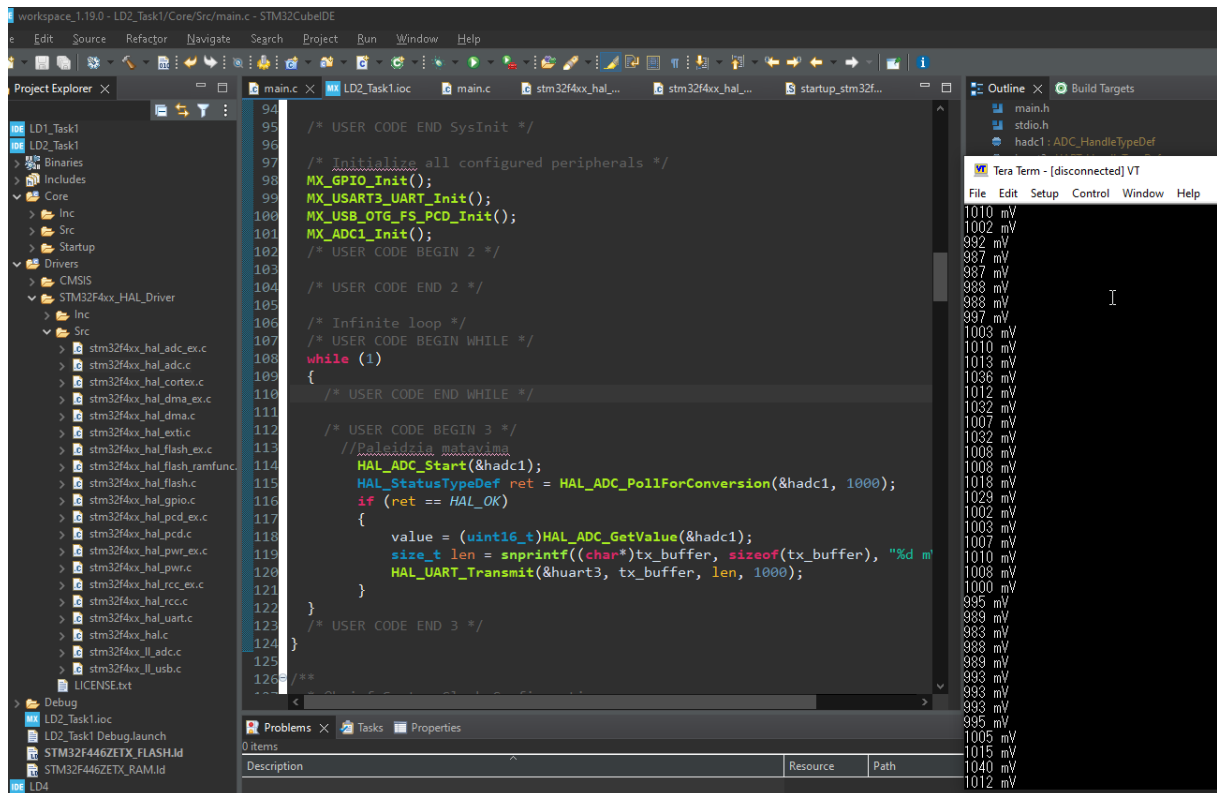
# 1. LD\_Tas1 projekto konfigūracija



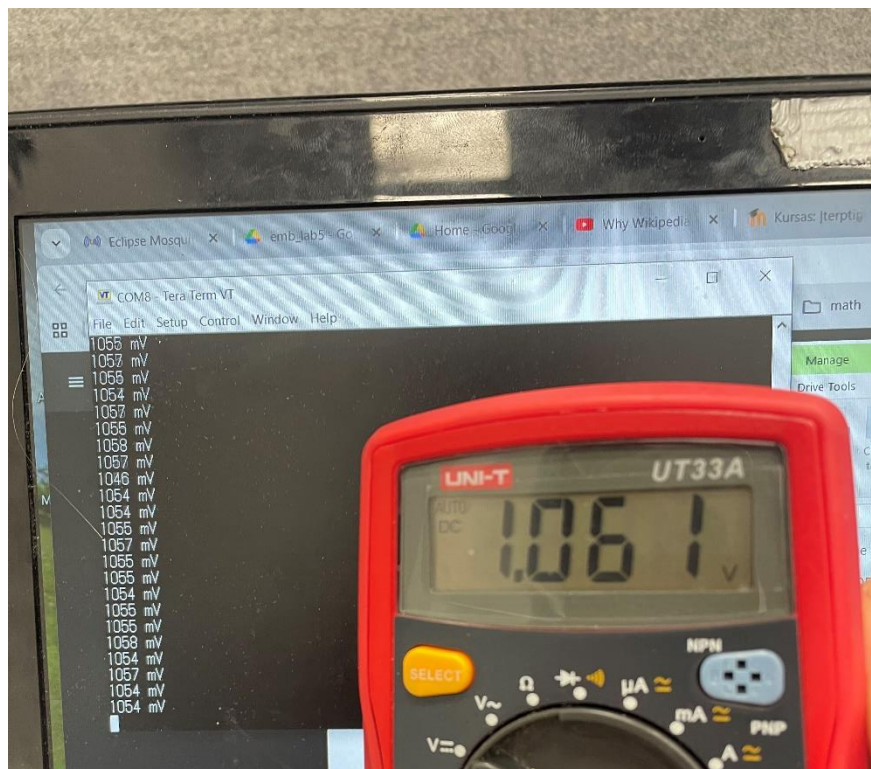
## 2. Sujungimas



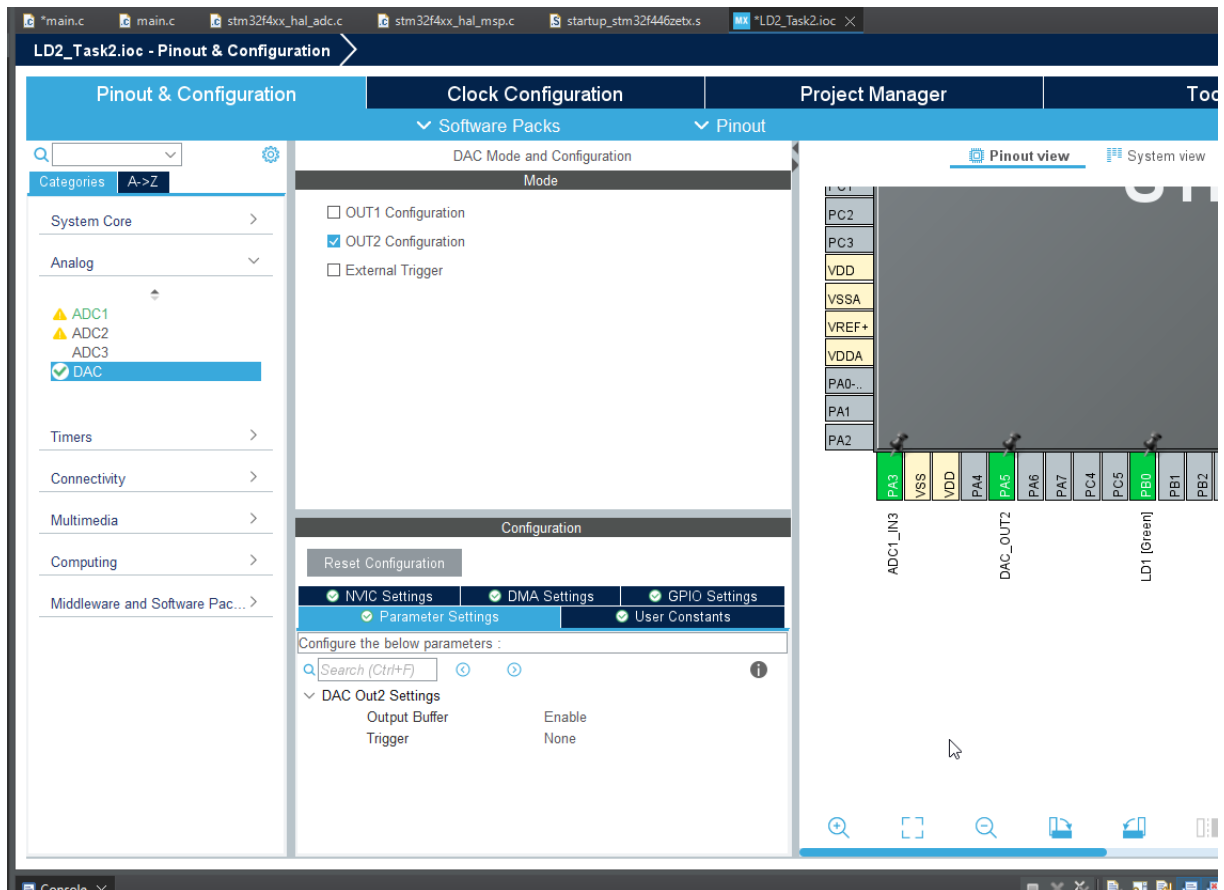
### 3. ADC (A0) kodo patikrinimas be potenciometro per Tera Term



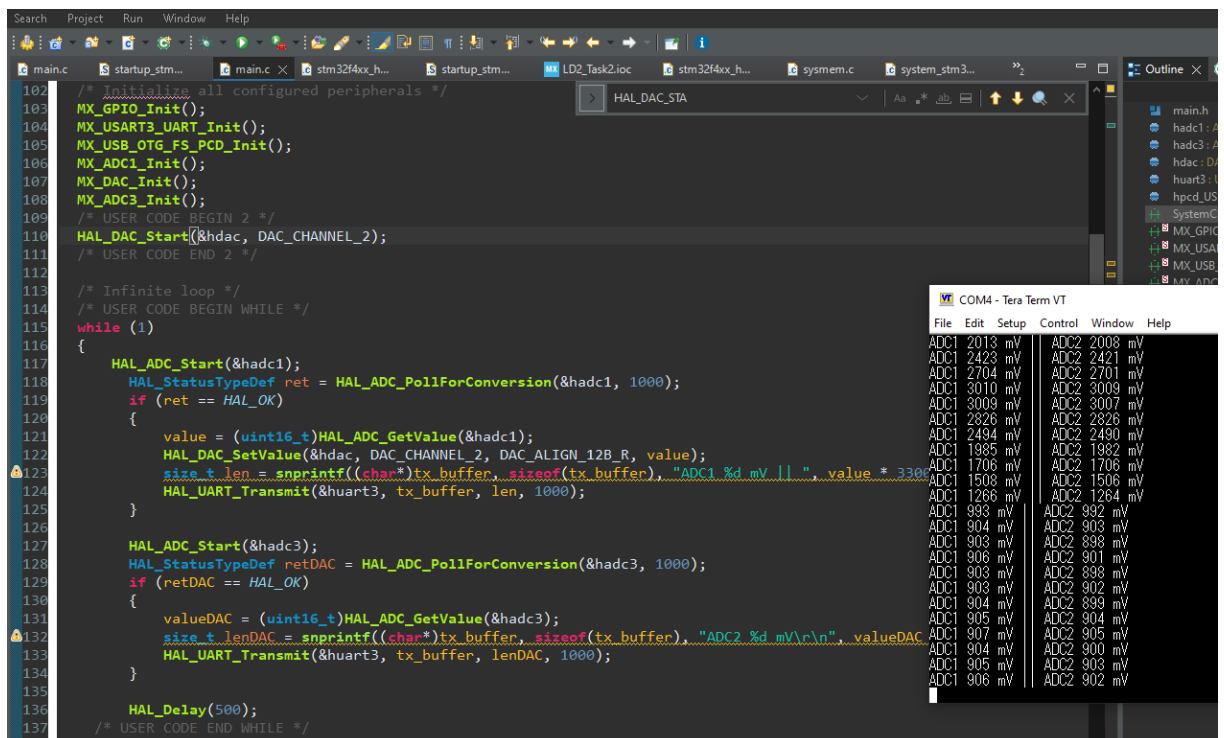
### 4. ADC nuskaitytos vertės iš potenciometro parodomos per UART ir multimetrą



## 5. LD2\_Task2 projekto konfigurācija

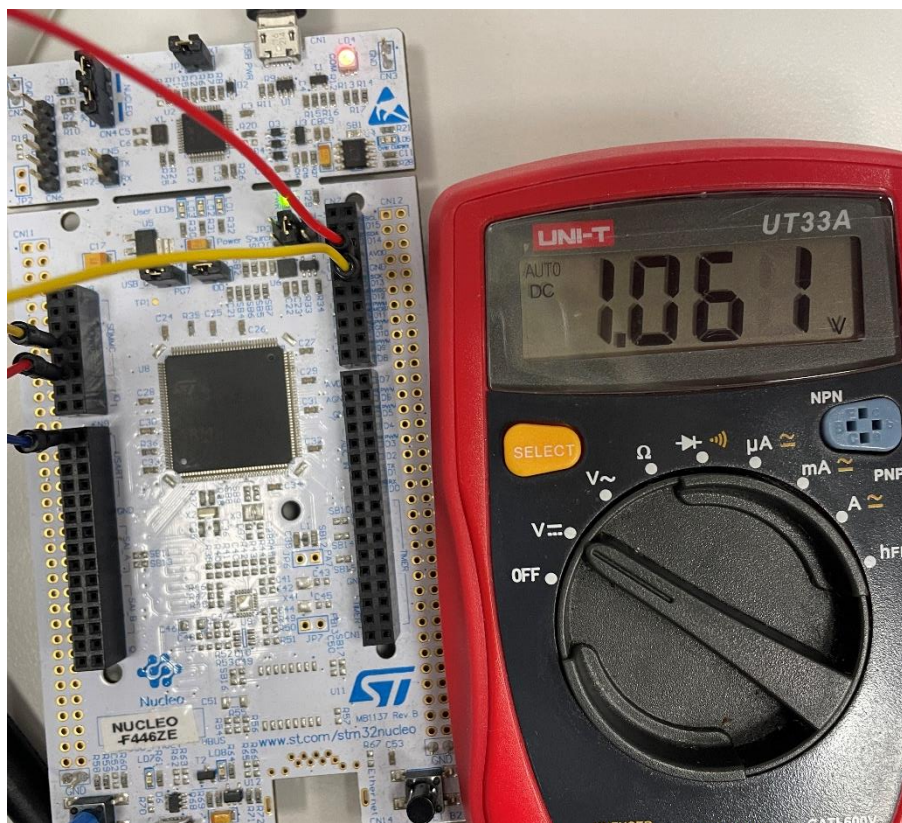
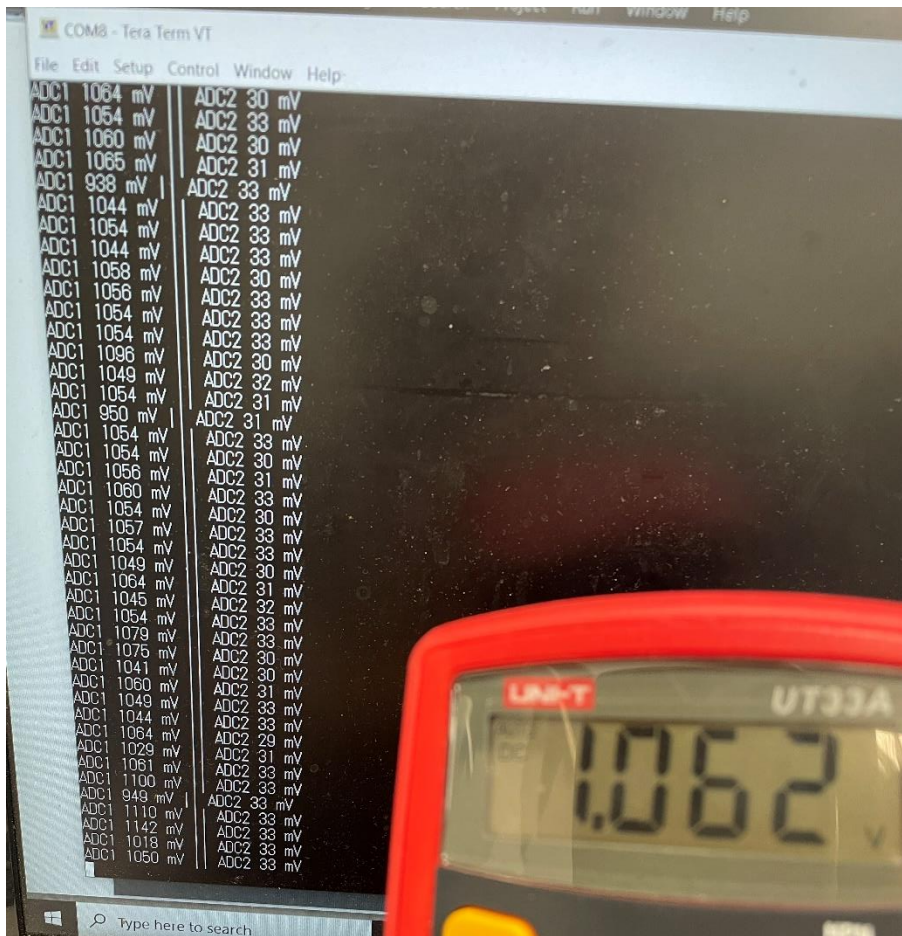


## 6. DAC (D13 (PA\_5)) išvesties kodas ir patikrinimas su ADC (A3(PF3))





## 7. DAC reikšmės patikrinimas multimetru (įtampa tokia kaip 4 punkte)



The image shows a screenshot of an IDE (Integrated Development Environment) with a dark theme. The main editor window displays C code for an STM32 microcontroller. The code includes comments and function calls for HAL (Hardware Abstraction Layer) and UART. The code is as follows:

```
113  /* USER CODE BEGIN 2 */
114  HAL_DAC_Start(&hdac, DAC_CHANNEL_2);
115  HAL_ADC_Start_DMA(&hadc1, &value, 32);
116  /* USER CODE END 2 */
117
118  /* Infinite loop */
119  /* USER CODE BEGIN WHILE */
120  while (1)
121  {
122
123      if (conversion_ended)
124      {
125          conversion_ended = 0;
126          uint32_t value_acc = 0;
127          uint8_t i;
128          for (i = 0; i < 32; i++)
129          {
130              value_acc += value[i];
131          }
132          HAL_ADC_Start_DMA(&hadc1, &value, 32);
133          value_avg = value_acc / 32;
134
135          HAL_DAC_SetValue(&hdac, DAC_CHANNEL_2, DAC_ALIGN_12B_R, value_avg);
136          size_t len = snprintf((char*)tx_buffer, sizeof(tx_buffer), "%d mV\r\n", value_avg * 3300 / 4095);
137          HAL_UART_Transmit(&huart3, tx_buffer, len, 1000);
138
139      }
140
141      HAL_Delay(100);
142  }
143  /* USER CODE END WHILE */
144
```

The bottom of the IDE shows a console window with the following output:

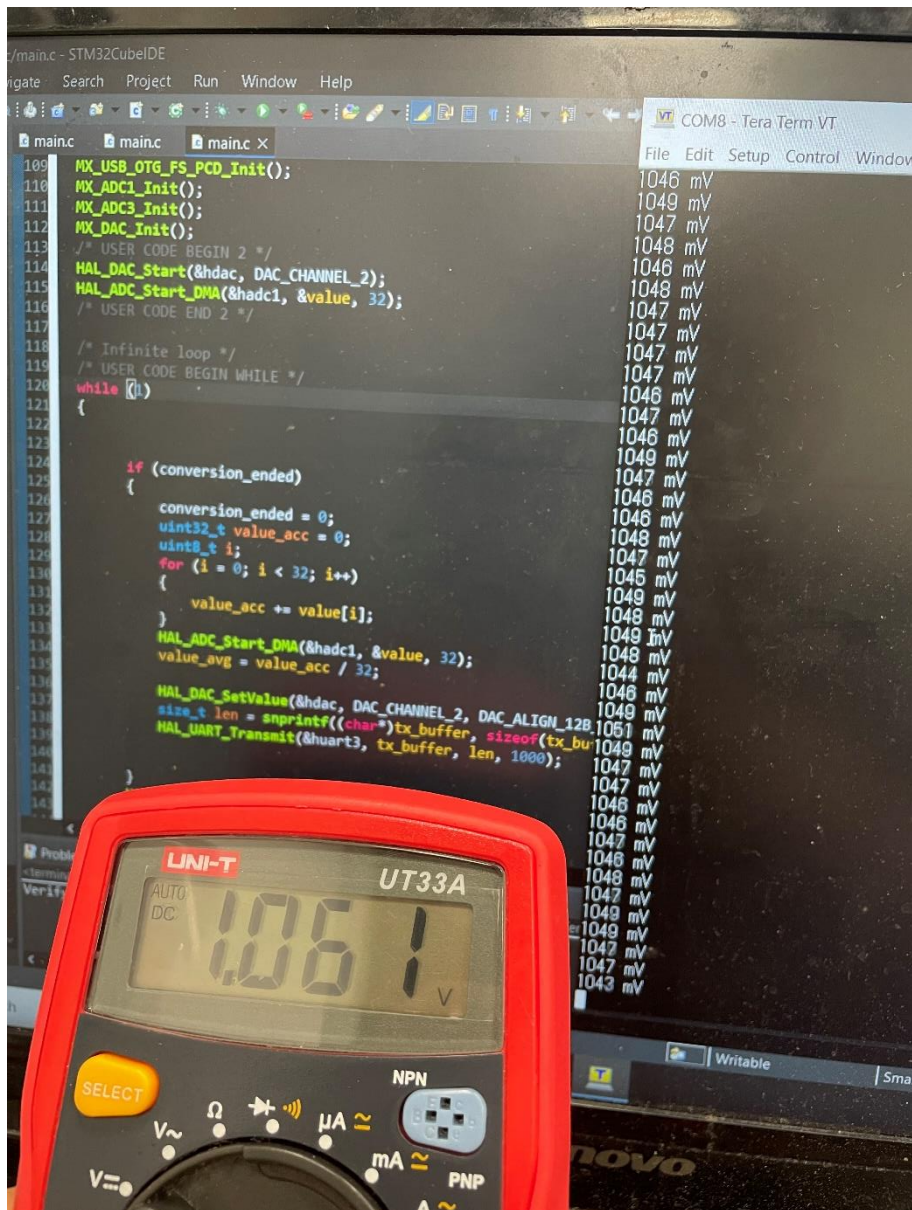
```
<terminated> LD2_Task3 Debug [STM32 C/C++ Application] ST-LINK (ST-LINK GDB server) (Terminated Dec 18, 2025, 1:30:45 AM) [pid: 255]
Time elapsed during verifying operation: 00:00:00.205

Download verified successfully

Shutting down...
Exit.
```

On the right side of the IDE, there is a graph showing voltage measurements. The graph has a vertical axis labeled 'mV' ranging from 2250 to 2252. The data points are represented by small blue squares, and the values are consistently around 2251 mV.

## 9. ADC DMA rezultatų tikrinimas multimetru (pagal 4 punkto įtampą)





## 10. Skaičiavimai nekeičiant potenciometro pozicijos

1	ADC matavimai		ADC DMA suvidurkinti matavimai			
2						
3	1054 mV	1054	1049 mV	1049		
4	1054 mV	1054	1048 mV	1048	Vidurkis ADC	1060.095
5	1051 mV	1051	1049 mV	1049	Vidurkis DMA	1047.905
6	1054 mV	1054	1047 mV	1047		
7	1054 mV	1054	1047 mV	1047	Variacija ADC	852.137
8	1054 mV	1054	1052 mV	1052	Variacija DMA	3.795587
9	1052 mV	1052	1047 mV	1047		
10	1055 mV	1055	1047 mV	1047		
11	1054 mV	1054	1053 mV	1053		
12	1061 mV	1061	1050 mV	1050		
13	1056 mV	1056	1051 mV	1051		
14	1062 mV	1062	1046 mV	1046		
15	1025 mV	1025	1047 mV	1047		
16	1152 mV	1152	1047 mV	1047		
17	1031 mV	1031	1048 mV	1048		
18	1142 mV	1142	1049 mV	1049		
19	1064 mV	1064	1048 mV	1048		
20	994 mV	994	1047 mV	1047		
21	1166 mV	1166	1046 mV	1046		
22	1051 mV	1051	1047 mV	1047		
23	1052 mV	1052	1048 mV	1048		
24	1089 mV	1089	1049 mV	1049		
25	1046 mV	1046	1050 mV	1050		
26	1055 mV	1055	1047 mV	1047		
27	1056 mV	1056	1046 mV	1046		
28	1057 mV	1057	1049 mV	1049		
29	1055 mV	1055	1049 mV	1049		
30	1054 mV	1054	1050 mV	1050		

DMA apskaičiuotas reikšmės turi mažiau triukšmo ir mažiau svyruoja aplink vidurkį, tačiau ADC nesuvidurkintos reikšmės yra arčiau tikrosios paduodamos įtampos (1.061V). Tai galėtų būti dėl per greito ADC DMA diskretizavimo laiko.