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## IR Receiver Register Configuration

### ▪ IR Receiver Register Configuration :

Sr.No	Name	Address (Hex)	Width (bits)	Access (R/W)	Description
1	edge_count_clear	0x00	1	W	Count Clear Register
2	edge_count_load	0x01	32	R/W	Count Load Register

### ▪ Detailed Explanation :

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#### 1. Count Clear Register:

- **Address** : 0x00
- **Description** : This register is used to set or clear the motor edge counts.
- **Example** : if edge\_count\_clear = 0 → edge\_count = 0  
if edge\_count\_clear = 1 → edge\_count = edge\_count

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#### 2. Count Load Register:

- **Address** : 0x01
- **Description** : This register is used to load the edge counts of the dc motors.  
When edge counts equals to count loaded in this register then an interrupt will be generated.
- **Example** : if ( edge-count != edge\_count\_load) → edge\_count++;  
if ( edge-count == edge\_count\_load) → irq = 1