BeMicro II Stick [1] Index [2] USB-Blaster [3] FPGA IO-Banks [4] FPGA System, Clock, Power [5] FPGA Config, DDR-RAM, Push-Buttons, Switch [6] Micro SD-Card, Temp. Sensor, LEDs, Ethernet [7] Power Supply, Extension Connector Fiducials Top Layer $\bigsqcup_{\mathrm{ADJ}_{-1}} \qquad \bigsqcup_{\mathrm{ADJ}_{-2}} \qquad \bigsqcup_{\mathrm{ADJ}_{-3}} \qquad \bigsqcup_{\mathrm{ADJ}_{-3}}$ Fiducials Bottom Layer This document may not be passed on, Checked By: Hitex Development Tools GmbH hitex BeMicroII-Stick duplicated or its contents utilized, unless Greschbachstr. 12 expressly permitted. Violating this stipulation Revision: 76229 Karlsruhe will result in the liability to pay damages. All DEVELOPMENT TOOLS Approved By: rights reserved in case of a patent registration Date: 06.09.2010 (c) Hitex Development Tools GmbH Sheet 1 of 7 or registered design. File: K:\Ent\HW\PROJ\Evaboards\FPGAStick\Baugruppen\BeMicroII-A\R2\Source\SCM\BeMicroII-A2 p1.SchDoc











