

PID Hardware Controller Register Configuration

Sr.No	Name	Address (Hex)	Width (bits)	Access	Description
1	kp	0x00	32	R/W	Proportional Parameter
2	ki	0x01	32	R/W	Integrational Parameter
3	kd	0x02	32	R/W	Derivative Parameter
4	ref_reg	0x03	32	R/W	Reference Register Reference count for the dc motor
5	fdb_reg	0x04	32	R/W	Feedback Register Feedback from the dc motor optical receivers which should be loaded into this register.
6	pid_en	0x05	1	R/W	PID Enable Register if pid_en = 1 then only PID operation can be started.
7	pid_out	0x06	32	R	Output of PID which is the addition of (P,I and D).
8	pid_cmp	0x07	1	R	PID complete bit, When PID operation completes, it is "1".