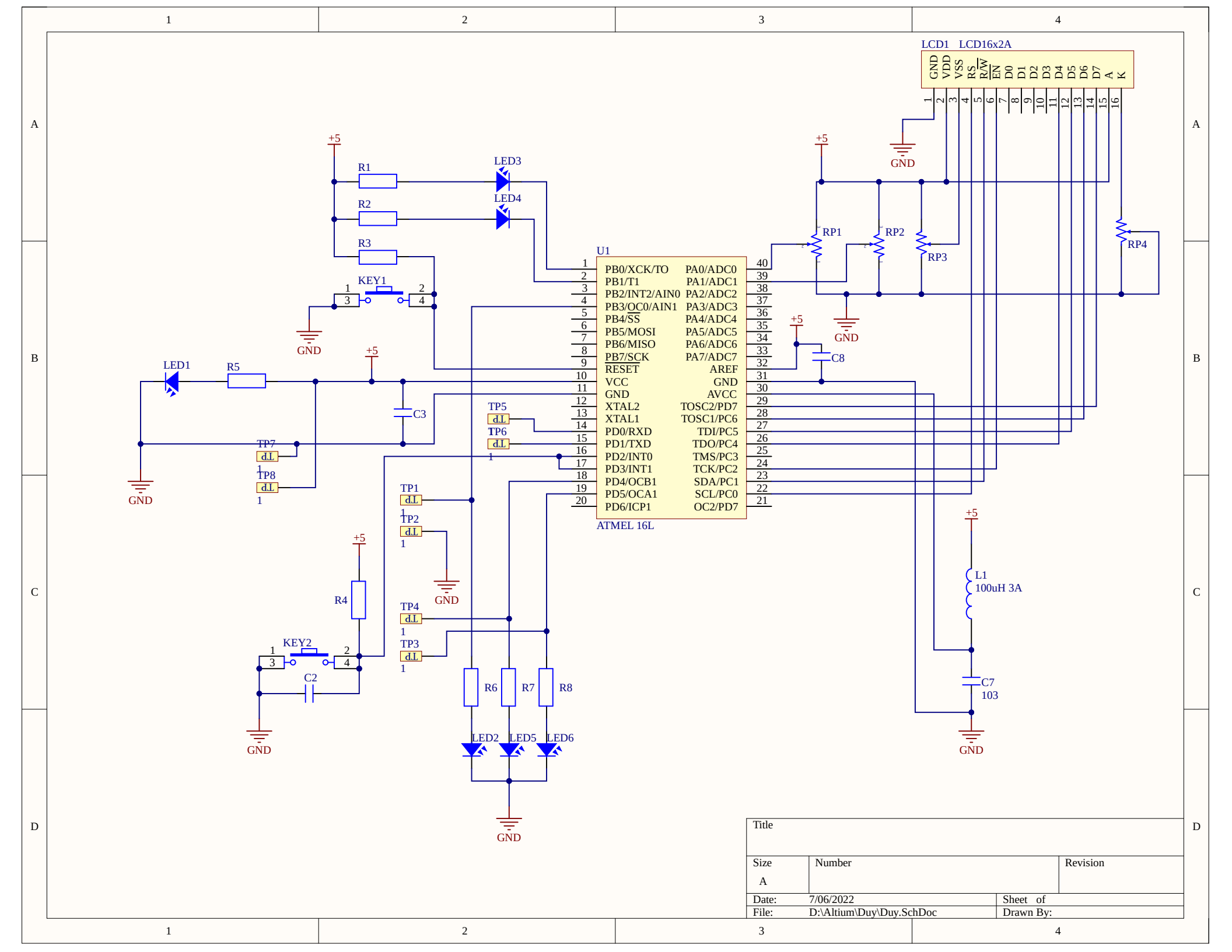
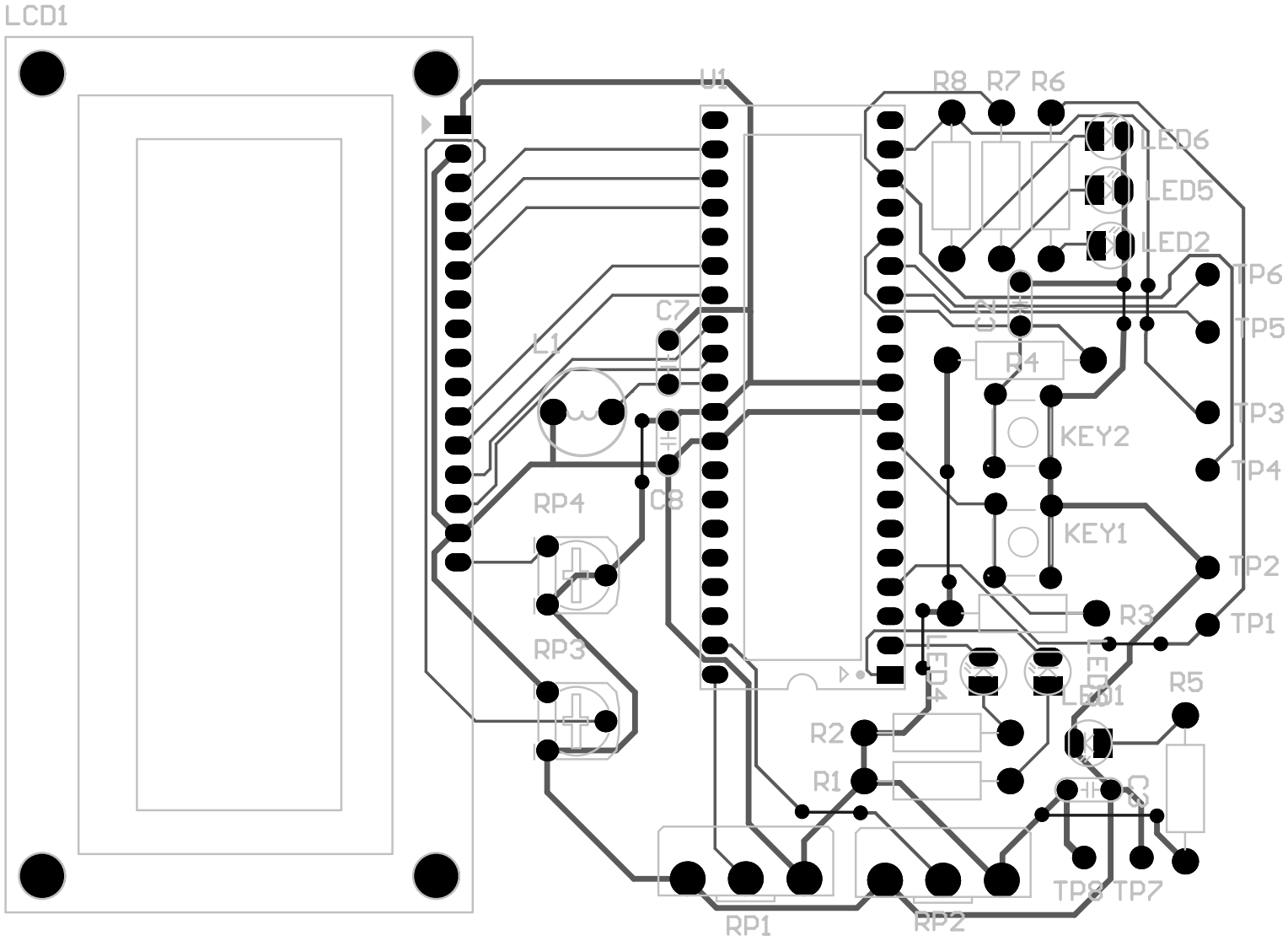

LCD1 LCD16x2A
GND VDD VSS RS R/W EN D0 D1 D2 D3 D4 D5 D6 D7 A K
U1
ATMEL 16L
PB0/XCK/TO PB1/T1 PB2/INT2/AIN0 PB3/OC0/AIN1 PB4/SS PB5/MOSI PB6/MISO PB7/SCK RESET VCC GND XTAL2 XTAL1 PD0/RXD PD1/TXD PD2/INT0 PD3/INT1 PD4/OCB1 PD5/OCA1 PD6/ICP1
PA0/ADC0 PA1/ADC1 PA2/ADC2 PA3/ADC3 PA4/ADC4 PA5/ADC5 PA6/ADC6 PA7/ADC7 AREF GND AVCC TOSC2/PD7 TOSC1/PC6 TDI/PC5 TDO/PC4 TMS/PC3 TCK/PC2 SDA/PC1 SCL/PC0 OC2/PD7
R1 R2 R3 R4 R5 R6 R7 R8
RP1 RP2 RP3 RP4
LED1 LED2 LED3 LED4 LED5 LED6
KEY1 KEY2
C2 C3 C7 103 C8
L1 100uH 3A
TP1 TP2 TP3 TP4 TP5 TP6 TP7 TP8
+5
GND
Title
Size A
Number
Revision
Date: 7/06/2022
Sheet of
File: D:\Altium\Duy\Duy.SchDoc
Drawn By:


LCD1
U1
R8
R7
R6
LED6
LED5
LED2
TP6
TP5
TP3
TP4
TP2
TP1
C7
L1
R4
KEY2
KEY1
C8
RP4
RP3
R3
R5
R2
R1
TP8
TP7
RP1
RP2

Board Stack Report