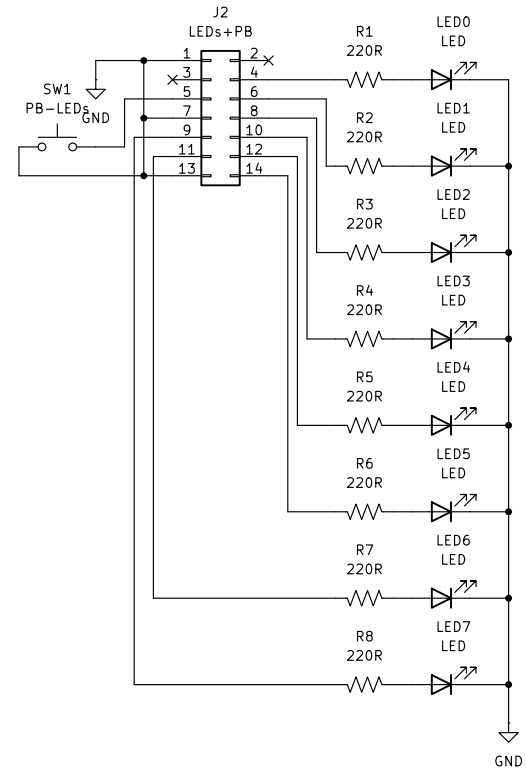
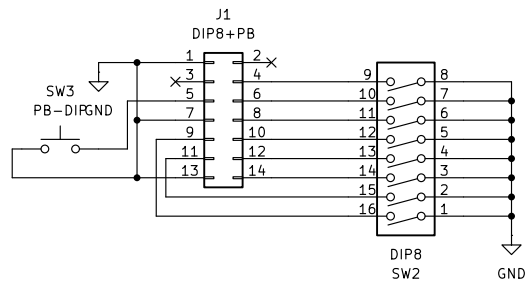


PWR\_FLAG



GND



Desenho por Diego Cirilo  
**LAICA – IFRN/Currais Novos**

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File: bitdog-fpga-io-module.kicad\_sch

**Title: Placa de interface para o FPGA Embarcatech**

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