

U_04
01_CoverSchDoc

U_02
02_Block diagram SchDoc

- Kiểm tra change log trước khi start project
- Đóng gói Project (Project Packager) trước khi tăng phiên bản project hoặc mỗi khi release cho sản xuất.

DESIGN CONSIDERATIONS

DESIGN NOTE:

Example text for
i-Value informational
design notes .

DESIGN NOTE:

Example text for critical
design notes.

DESIGN NOTE:

Example text for cautionary
design notes.

LAYOUT NOTE:

Example text for critical
layout guidelines.

TEMPLATE NOTES

Set Project Parameters

- 1) Go to Project -> Project Options -> Parameters
- 2) Set Company, Project and VersionRevision

Mark Not Fitted Components as

Net Class Example



Differential signal example



TITLE Examples (You can change the color to reflect your company color)

PAGE TITLE

Peripheral / Group of component title

Smaller Title

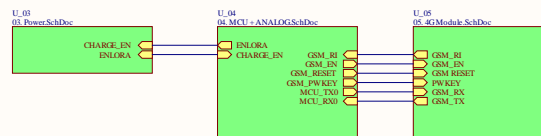
Schematic Status Explanation


DRAFT - Very early stage of schematic, ignore details.

PRELIMINARY - Close to final schematic.

CHECKED - There should not be any mistakes. Tell the engineer if you find one.

RELEASED - A board with this schematic has been sent to production.



HAWACO_DATALOGER		 BYTECH CÔNG NGHỆ XÂY DỰNG TƯƠNG LAI	
Project: HAWACO_DATALOGER_DT-G2_V1.0.PyPcb			
File: 00_Root_SchDoc			
REV: V1.0.0	Sheet: *	Drawn By: BT/PT	
Size: A2	6/10/2021	Checked By: *	
DRAFT		Approved By: *	


HAWACO_DATALOGER

Variant: [No Variations]

6/10/2021
V1.0.0

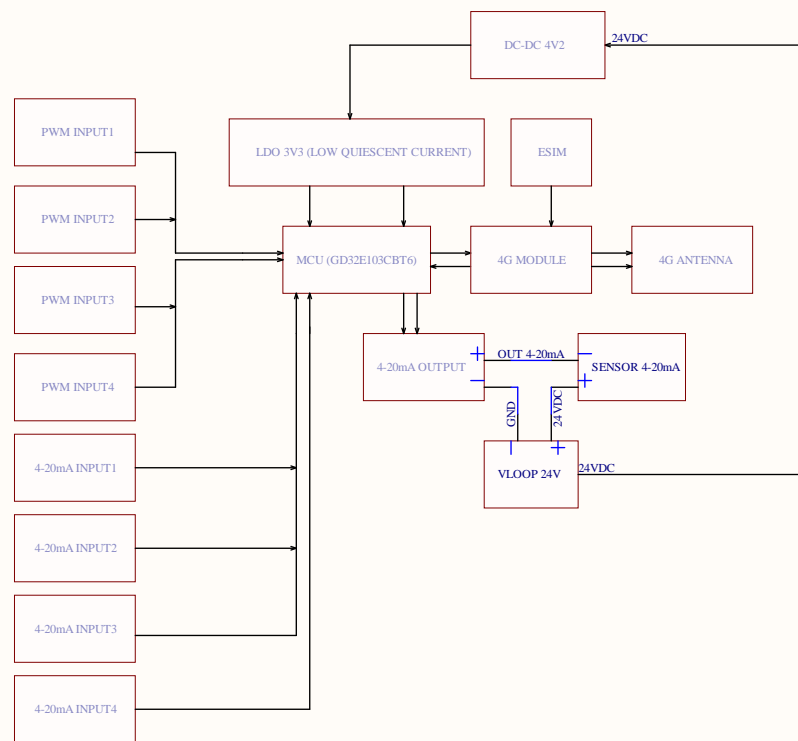
Version	Change Log
Ver 1.0.0	


Page	Index
1	COVER PAGE
2	BLOCK DIAGRAM
3
4
5
6
7
8
9
10
11
12
13
14
15

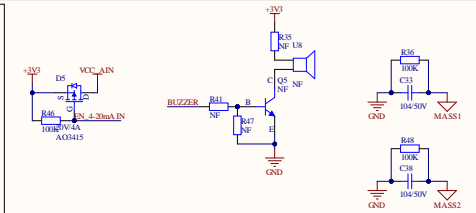
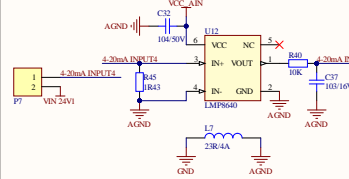
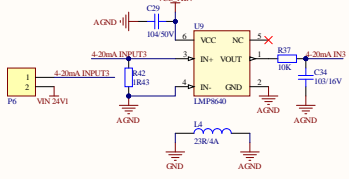
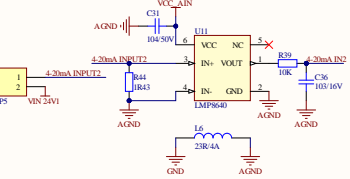
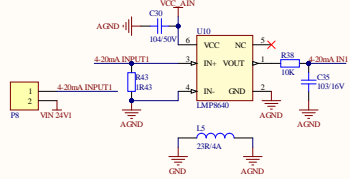
HAWACO_DATALOGER			
Project: HAWACO_DATALOGER_DT-G2_V1.0.PrjPcb			
File: 01_Cover.SchDoc			
REV: V1.0.0	Sheet: *	Drawn By:	BUTPT
Size: A2	6/10/2021	Checked By:	*
DRAFT		Approved By:	*

HAWACO_DATALOGER

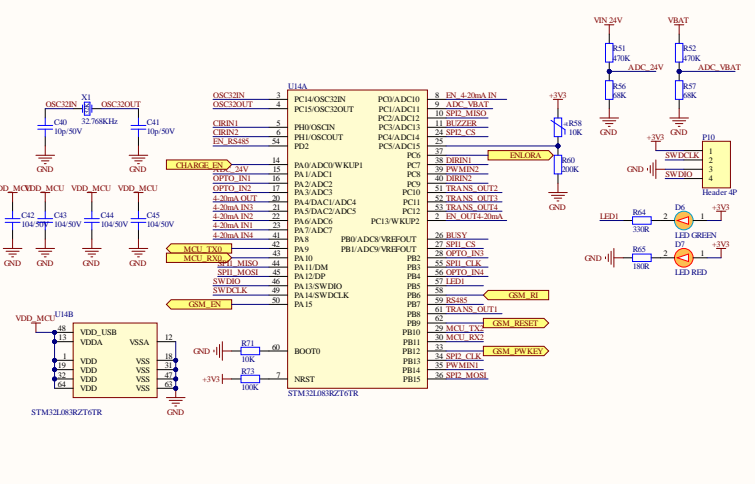
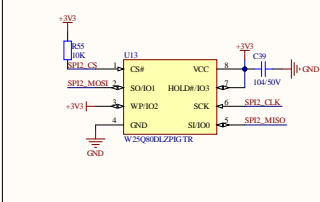
(Block Diagram)



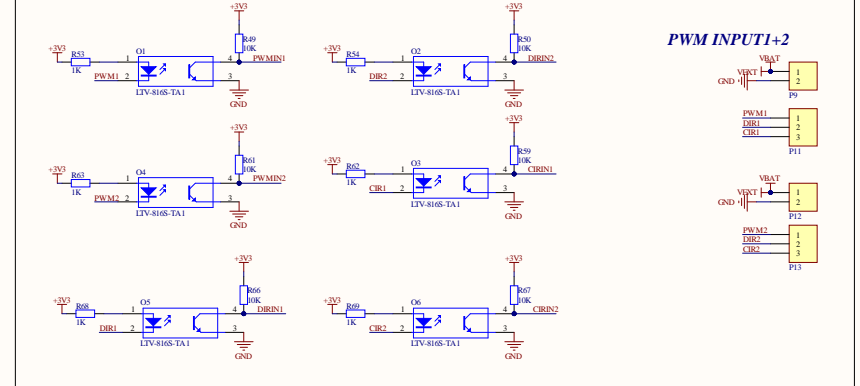
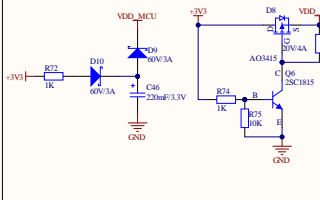
HAWACO_DATALOGER		 CÔNG NGHỆ KẾT NỐI TƯƠNG LAI	
Project: HAWACO_DATALOGER_DT-G2_V1.0.PjtPcb			
File: 02. Block diagram.SchDoc			
REV: V1.0.0		Sheet: *	Drawn By: BUTPT
Size: A2		6/10/2021	Checked By: *
DRAFT		Approved By: *	



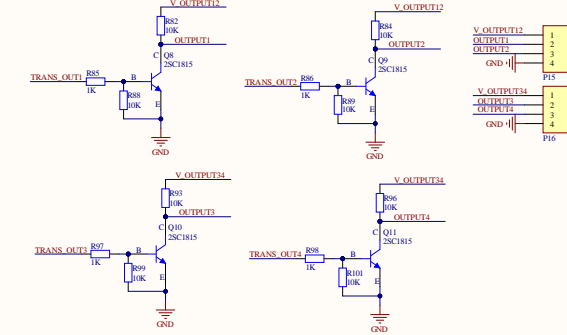
FLASH



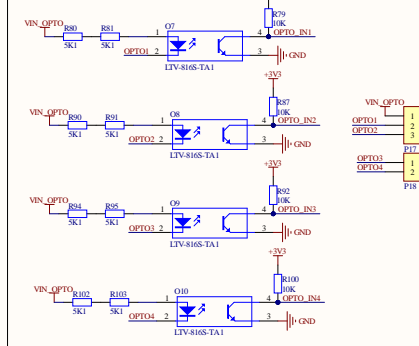
BACKUP VOLTAGE



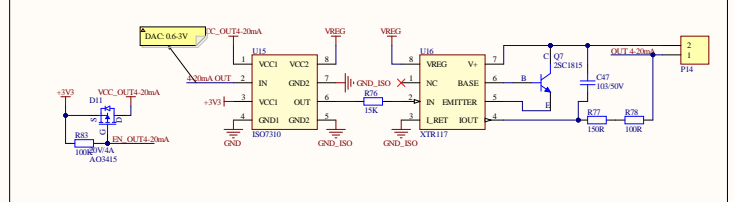
OUTPUT TRANS



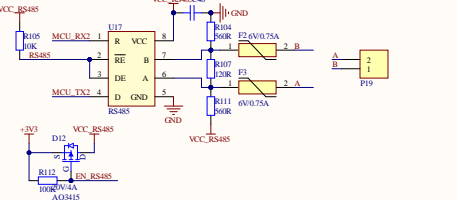
OPTO INPUT



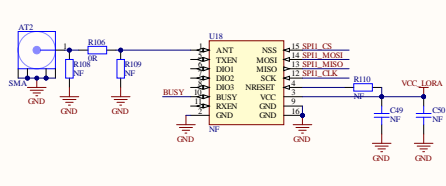
4-20mA OUTPUT

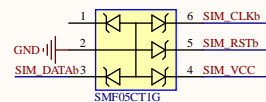
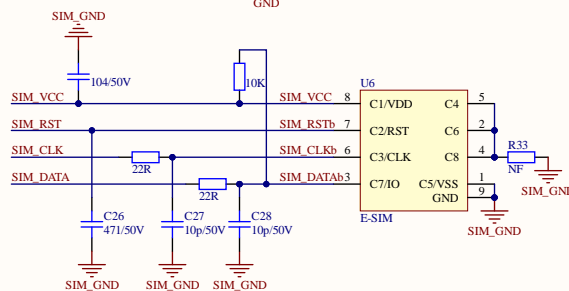
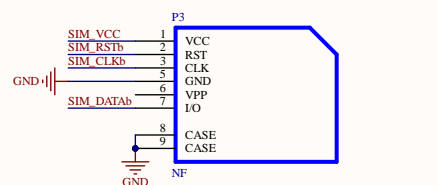
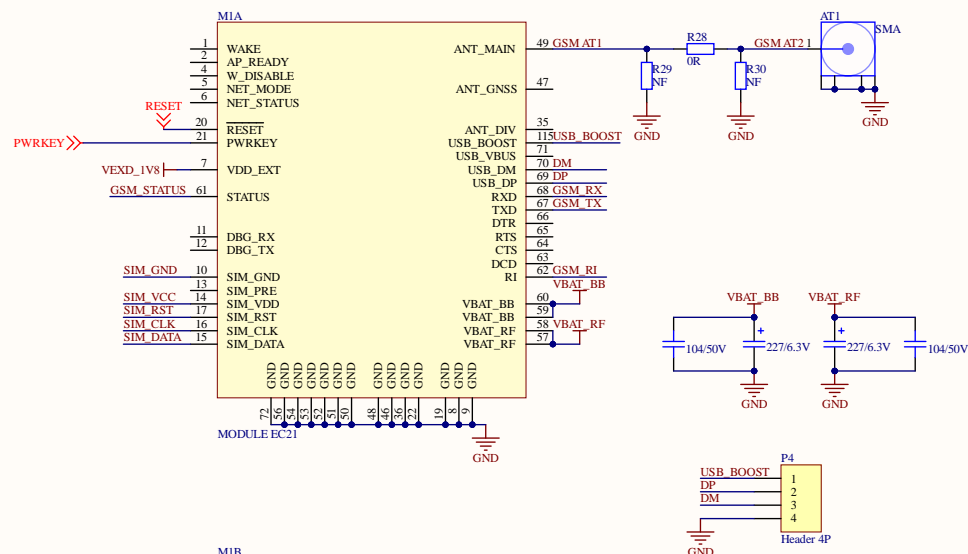
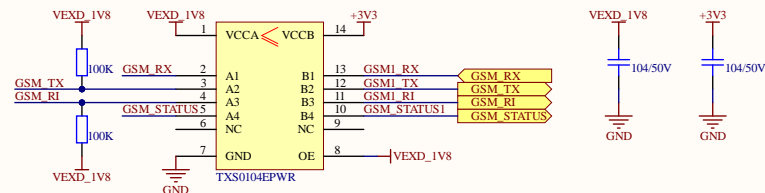
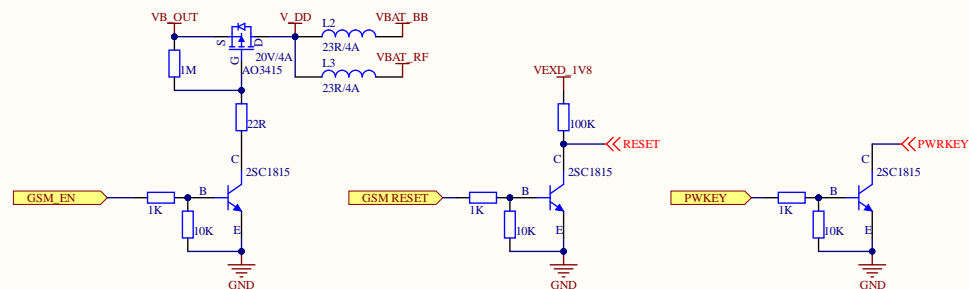


RS485



LORA





HAWACO_DATALOGGER



Project: HAWACO_DATALOGGER_DT-G2_V1.0.PrgPcb

File: 05_4G Module.SchDoc

REV: V1.0.0 Sheet: *

Drawn By: BUTPT

Size: A2 6/10/2021

Checked By: *

DRAFT

Approved By: *