LAYERS USAGE

- 🌐 BOARD OUTLINE
- 🌐 FAB. INDICATIONS
- 🌐 DRILL GUIDE

100.00mm

75.00mm

150.00mm

BOARD: DVID2_EVB			VERSION: A	
LAYER: Board description			DATE (D/M/YYYY): 11/11/2024	
DOCUMENT NUMBER: -	DRAWN BY: Justin MASSIOT	APPROVED BY: Arnaud COURTY	DVID2_EVB.PcbDoc PCB Rev: 56 PRJ Rev: Unknown revision	

BOARD OUTLINE

V-SCORING

MILLING

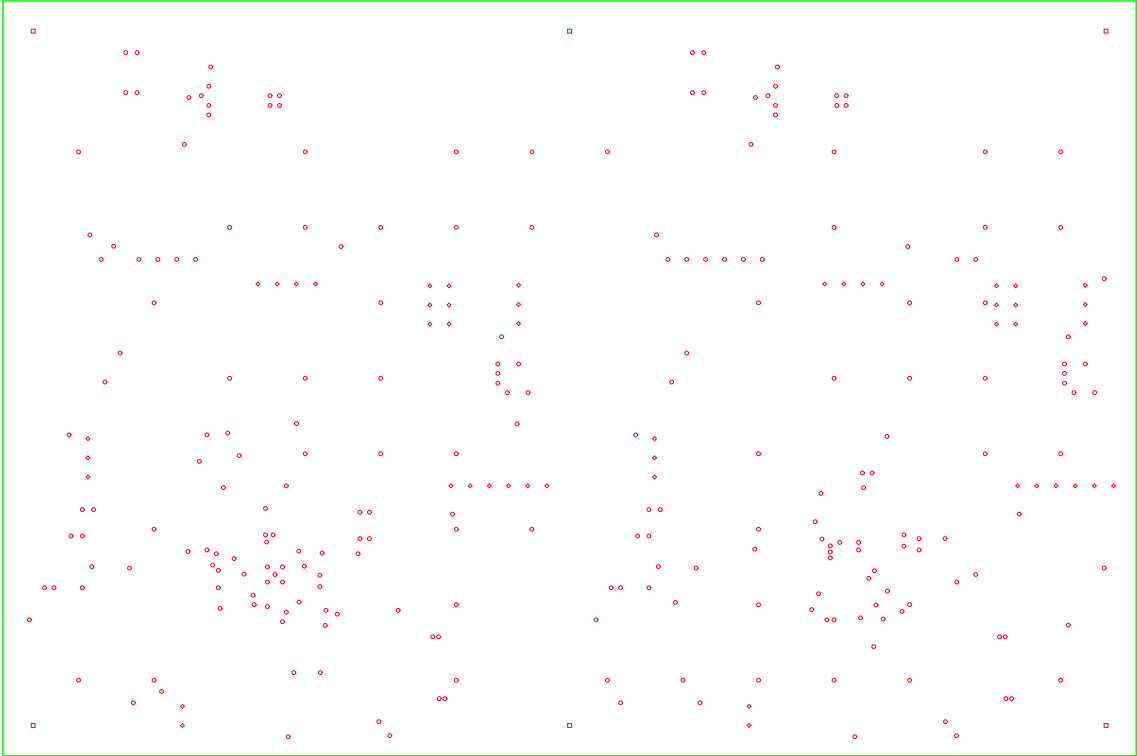| BOARD: DVID2_EVB			VERSION: A
LAYER: Board cutout			DATE (D/M/YYYY): 11/11/2024
DOCUMENT NUMBER: –	DRAWN BY: Justin MASSIOT	APPROVED BY: Arnaud COURTY	DVID2_EVB.PcbDoc PCB Rev: 56 PRJ Rev: Unknown revision

BOARD OUTLINE

DRILL DRAWING

BOARD OUTLINE

TOP COPPER

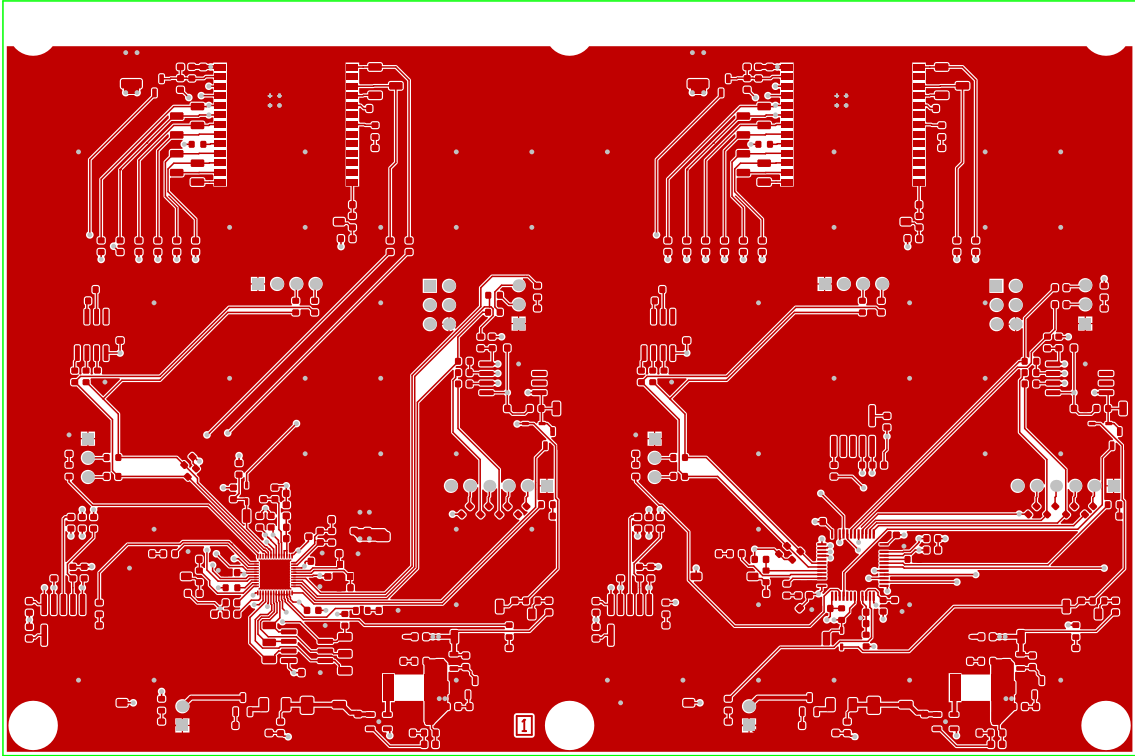| BOARD: DVID2_EVB			VERSION: A
LAYER: Top Layer			DATE (D/M/YYYY): 11/11/2024
DOCUMENT NUMBER: —	DRAWN BY: Justin MASSIOT	APPROVED BY: Arnaud COURTY	DVD2_EVB.PcbDoc PCB Rev: 56 PRJ Rev: Unknown revision

BOARD OUTLINE

INNER COPPER 1

2

BOARD:			VERSION:
DVID2_EVB			A
LAYER:			DATE (D/M/YYYY):
Mid-Layer 1			11/11/2024
DOCUMENT NUMBER:	DRAWN BY:	APPROVED BY:	DVD2_EVB.PcbDoc
—	Justin MASSIOT	Arnaud COURTY	PCB Rev: 56 PRJ Rev: Unknown revision

BOARD OUTLINE

INNER COPPER 2

BOARD OUTLINE

BOTTOM COPPER

BOARD:			VERSION:
DVID2_EVB			A
LAYER:			DATE (D/M/YYYY):
Bottom Layer			11/11/2024
DOCUMENT NUMBER:	DRAWN BY:	APPROVED BY:	DVD2_EVB.PcbDoc
–	Justin MASSIOT	Arnaud COURTY	PCB Rev: 56
			PRJ Rev: Unknown revision

BOARD OUTLINE

TOP SOLDER MASK

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BOARD OUTLINE

BOTTOM SOLDER MASK

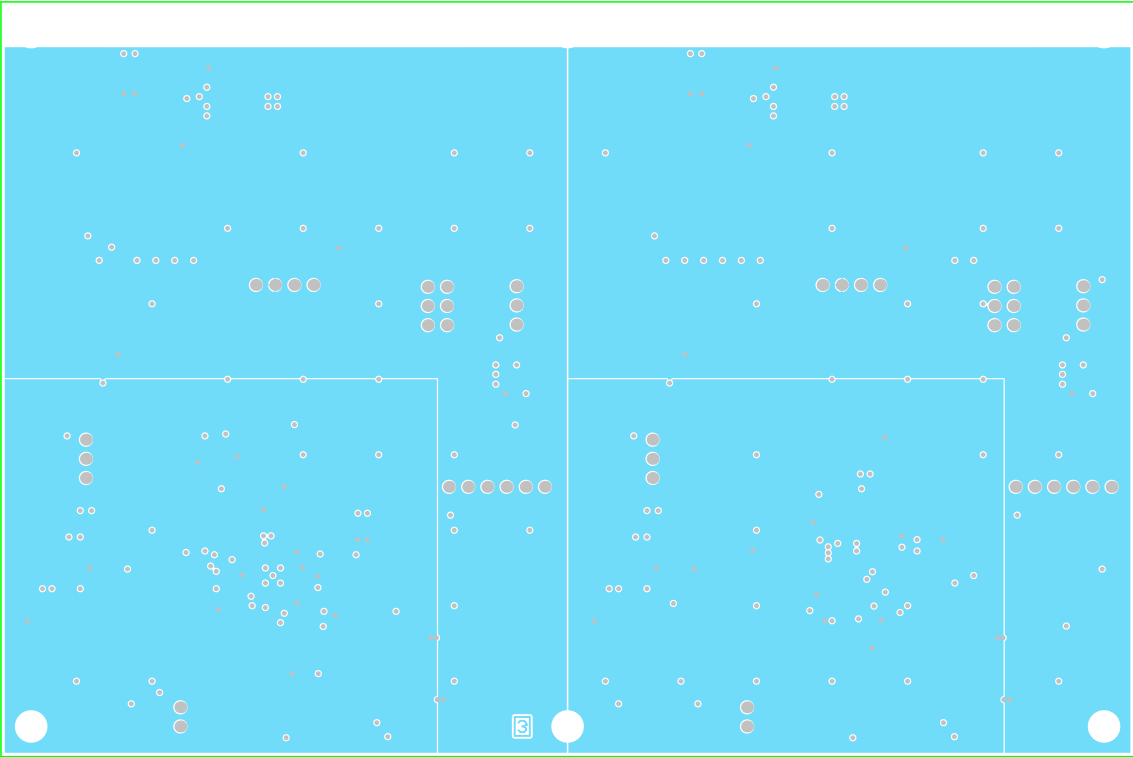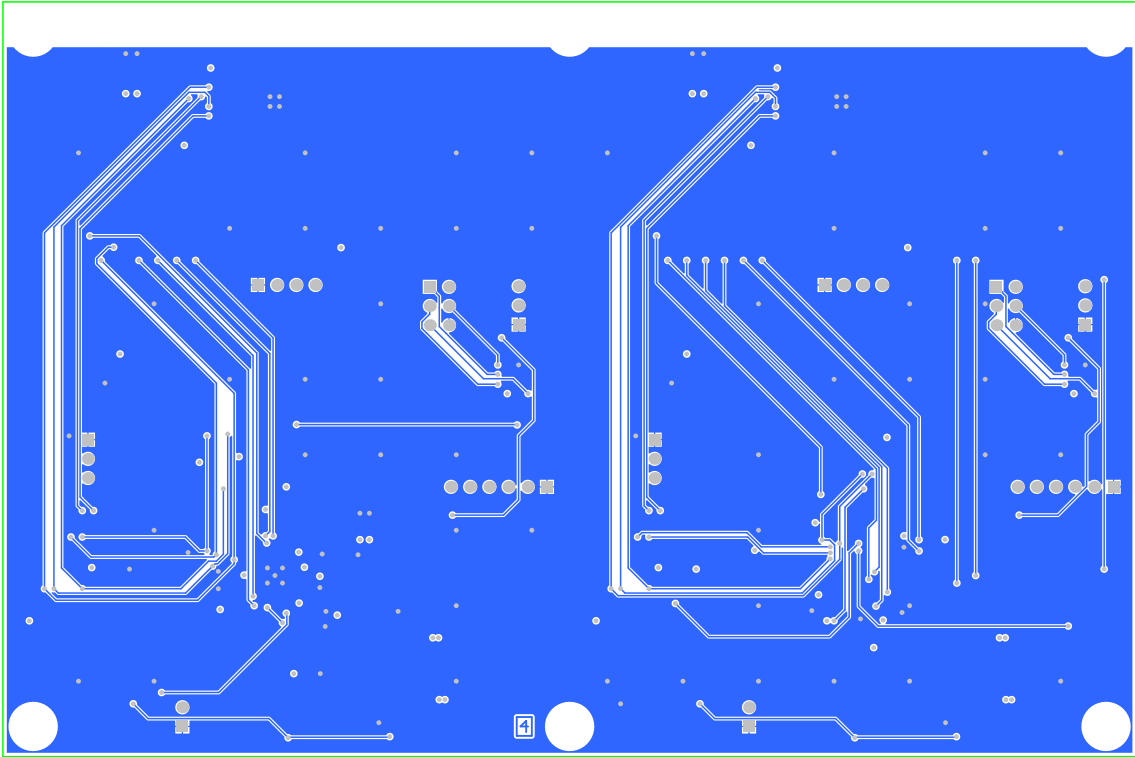| BOARD: DVID2_EVB			VERSION: A
LAYER: Bottom Solder Mask Print			DATE (D/M/YYYY): 11/11/2024
DOCUMENT NUMBER: —	DRAWN BY: Justin MASSIOT	APPROVED BY: Arnaud COURTY	DVD2_EVB.PcbDoc PCB Rev: 56 PRJ Rev: Unknown revision

ESP32 STM32

DVID

Get started on dvid.eu

BOARD OUTLINE

TOP LEGEND

A401A
MISO
TXD0/CS
RXD0
SCK
MOSI
RX
TX
SCL
SDA
DM
DP
ACTIVITY
SW401A
BTLD RADIO
ISP
UART
J102A
J401A
SCREEN
J402A
RX
TX
GND
U401A
U402A
RESET
IIC
J103A
SPI
J104A
MOSI
MISO
SCK
CS
GND
BTLD CORE
SW301A
U301A
DI
CK
HD
U302A
SPARE
WP
DO
CS
GND
RESET
SW201A
3V3 OUT
3V3
J201A
JTAG
SW202A
COLD RESTART
POWER IN
+5V
GND
J101A
+VIN
-VIN
U201A

A401B
MISO
TXD0/CS
RXD0
SCK
MOSI
RX
TX
SCL
SDA
DM
DP
ACTIVITY
SW401B
BTLD RADIO
ISP
UART
J102B
J401B
SCREEN
J402B
RX
TX
GND
U401B
U402B
RESET
IIC
J103B
SPI
J104B
MOSI
MISO
SCK
CS
GND
J301B
SWD
U301B
BTLD CORE
SW301B
SPARE
RESET
GND
RESET
SW201B
3V3 OUT
3V3
J201B
JTAG
SW202B
COLD RESTART
POWER IN
+5V
GND
J101B
+VIN
-VIN
U201B

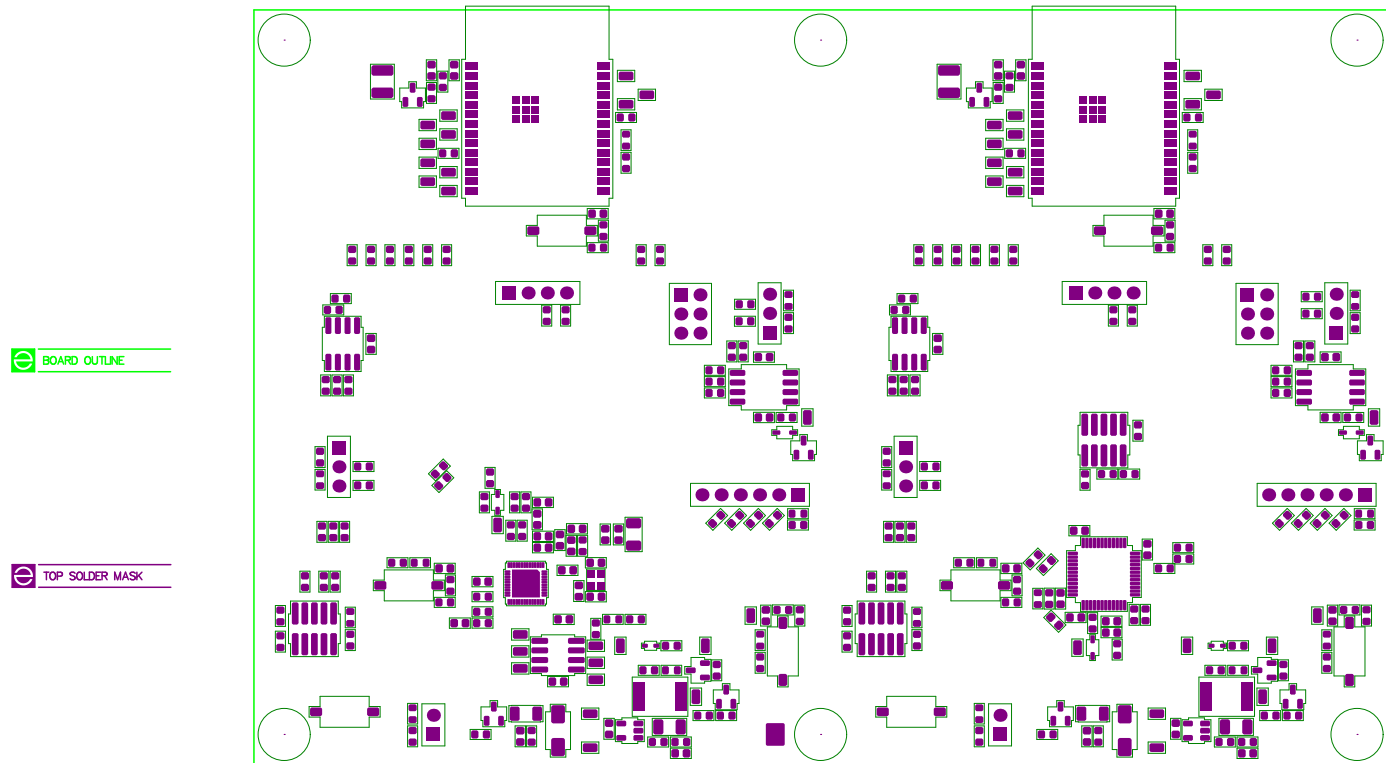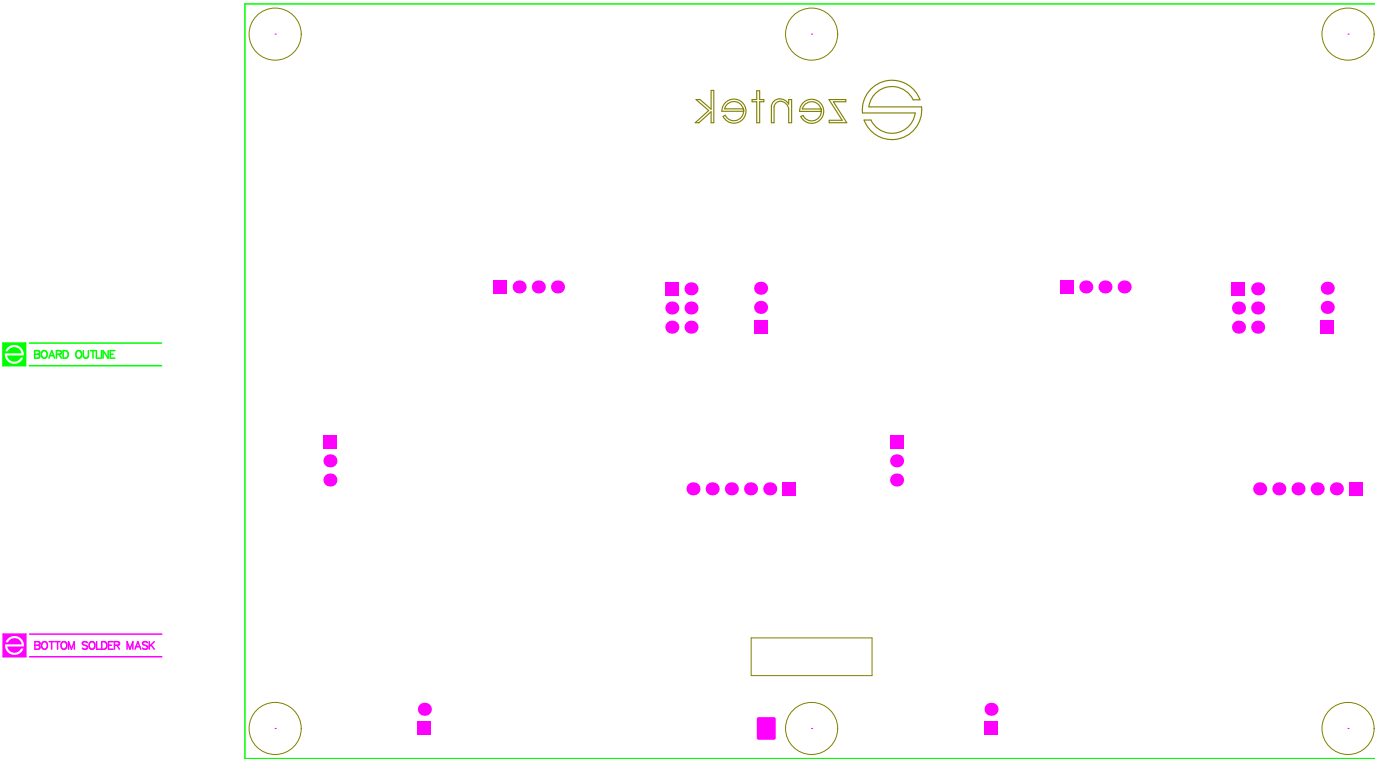| BOARD: DVID2_EVB			VERSION: A
LAYER: Top Silkscreen Overlay			DATE (D/M/YYYY): 11/11/2024
DOCUMENT NUMBER: –	DRAWN BY: Justin MASSIOT	APPROVED BY: Arnaud COURTY	DVID2_EVB.PcbDoc PCB Rev: 56 PRJ Rev: Unknown revision

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BOARD OUTLINE

BOTTOM LEGEND

A
DVID2_EVB

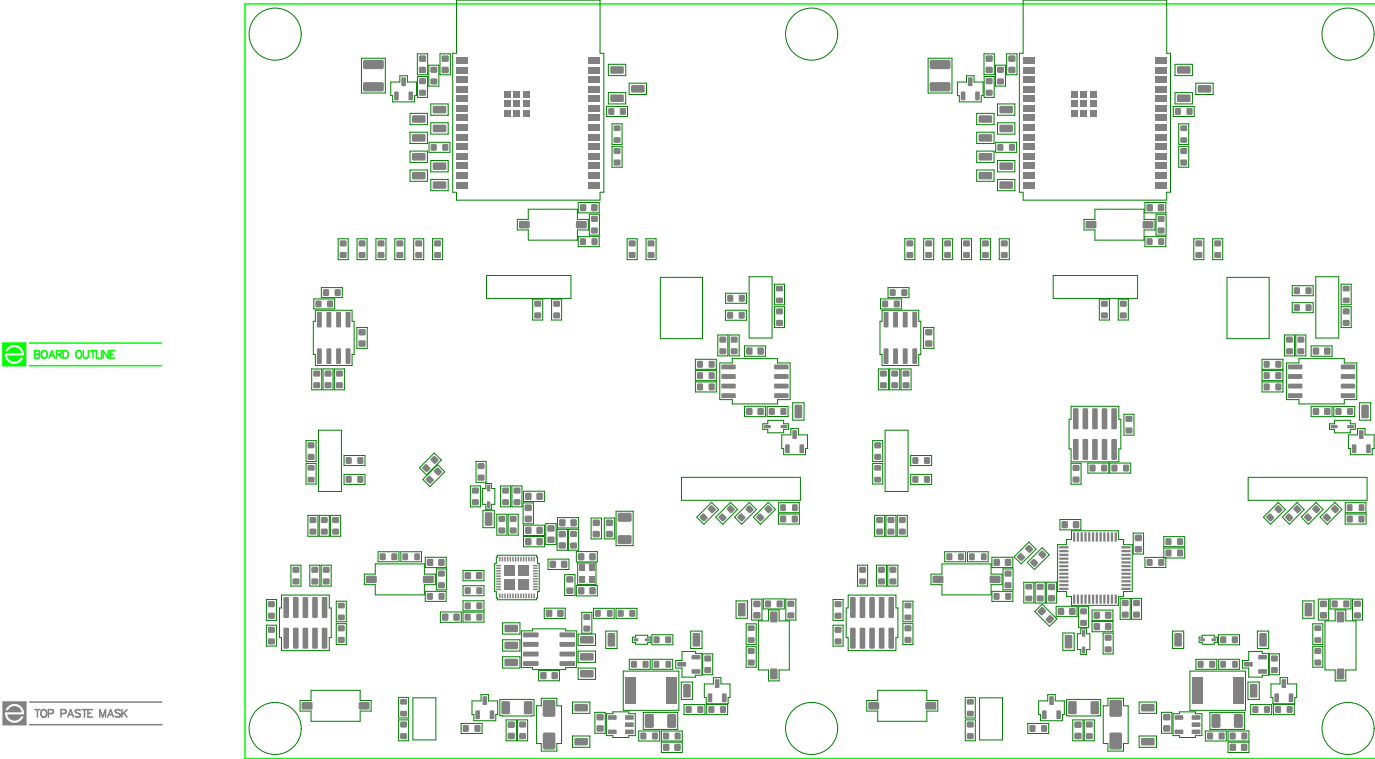| BOARD: DVID2_EVB			VERSION: A	
LAYER: Bottom Silkscreen Overlay			DATE (D/M/YYYY): 11/11/2024	
DOCUMENT NUMBER: -	DRAWN BY: Justin MASSIOT	APPROVED BY: Arnaud COURTY		DVID2_EVB.PcbDoc PCB Rev: 56 PRJ Rev: Unknown revision

BOARD OUTLINE

TOP PASTE MASK

BOARD: DVID2_EVB			VERSION: A
LAYER: Top Paste Mask			DATE (D/M/YYYY): 11/11/2024
DOCUMENT NUMBER: —	DRAWN BY: Justin MASSIOT	APPROVED BY: Arnaud COURTY	DVID2_EVB.PcbDoc PCB Rev: 56 PRJ Rev: Unknown revision

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BOARD OUTLINE

BOTTOM PASTE MASK

BOARD: DVID2_EVB			VERSION: A
LAYER: Bottom Paste Mask			DATE (D/M/YYYY): 11/11/2024
DOCUMENT NUMBER: –	DRAWN BY: Justin MASSIOT	APPROVED BY: Arnaud COURTY	DVD2_EVB.PcbDoc PCB Rev: 56 PRJ Rev: Unknown revision