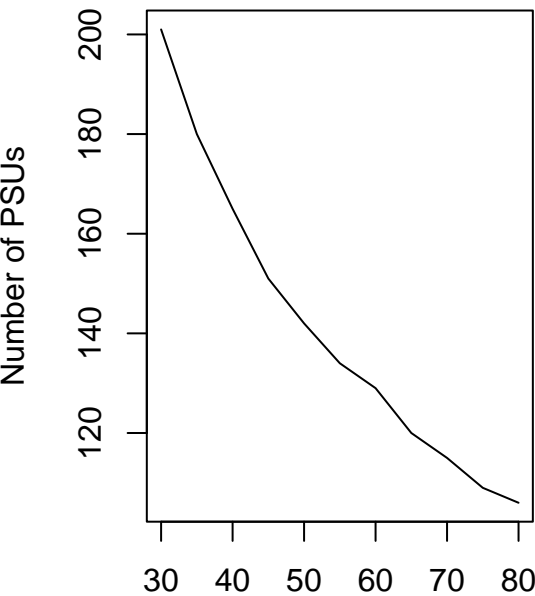
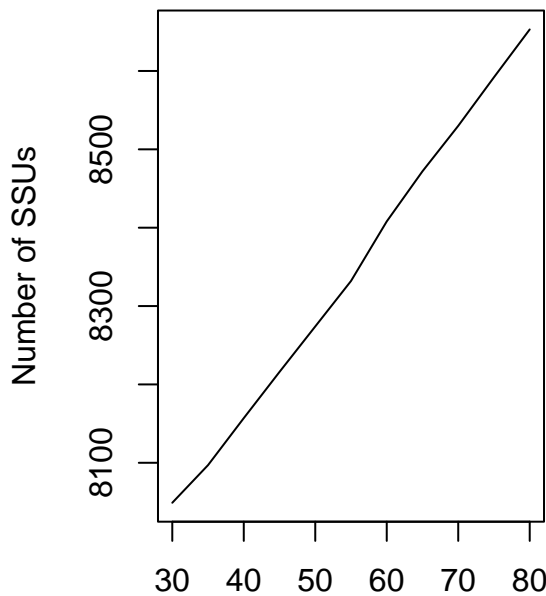


First Stage Size



Varying minimum SSUs per PSU

Second Stage Size



Varying minimum SSUs per PSU