# MSP430™ Ultra-Low-Power Microcontrollers





ti.com/msp430 2H13

**MSP430<sup>TM</sup> microcontrollers** (MCUs) from Texas Instruments (TI) are 16-bit, RISC-based, mixed-signal processors designed for ultra-low power. Our MCUs offer the lowest power consumption and the perfect mix of integrated peripherals for thousands of applications – including yours. We also provide all of the hardware and software tools you need to get started today! Not only that, TI has a plethora of complementary components to meet your needs. Learn more today at ti.com/msp430.

#### **Ultra-low power**

Battery Life > 20 Years

- <100 μA/MHz
- 0.1µA RAM Retention
- <1µA RTC Mode

#### More Performance Without Sacrificing Battery Life

- 7 Low-Power Modes
- Instant Wakeup
- Autonomous Peripherals



#### Integration

#### **Advanced Peripherals**

- High-Performance Analog
- Optimized Serial Communications
- Operate in Low-Power Modes

#### Minimize Physical Footprint and Bill of Materials

- USB
- LCD Drivers
- Sigma Delta ADCs



#### **Ease of Use**

#### Development

- Start with MSP430 LaunchPad **Evaluation Kit**
- Comprehensive Software Portfolio
- Application-Specific Ecosystem

#### Support

- Code Examples
- Direct Support Available at ti.com/e2e-msp430
- Developer Community at 43oh.com



#### **Grow with MSP**

#### **Scale Your Applications**

- 400+ Devices
- Up to 512 KB Flash and 64 KB RAM
- 25+ Package Options

#### **Unlimited Possibilities with TI**

- Data Collection
- Wireless Connectivity
- Power Solutions



Everyone says their MCUs are ultra-low power. So what makes us different? Ultra-low power is in our DNA! MSP430 MCUs are designed from the ground up specifically for ultra-low-power applications.

#### Multiple low-power modes

The MSP430 MCU clock system has the ability to enable and disable various clocks and oscillators which allow the device to enter several low-power modes (LPMs). The flexible clocking system optimizes overall current consumption by only enabling the required clocks when appropriate. This means that MSP430 MCUs can operate for decades on a single coin cell battery.

#### **Autonomous peripherals**

Intelligent analog and digital peripherals can run autonomously in low-power modes. This allows our MCUs to operate as efficiently as possible.

#### **Instant wakeup**

The ultra-fast digitally controlled oscillator (DCO), with start-up times as fast as 1  $\mu$ s, allows MSP430-based systems to remain in low-power modes for the longest possible interval – extending battery life. The DCO is fully user programmable.

#### **Real-time clock**

The low-power real-time clock (RTC), available on select MSP430 MCUs, precisely keeps real time and enables wakeup at specified intervals. Some variants also include a switchable battery backup system that maintains operations when the primary power supply fails.

#### **Direct memory access**

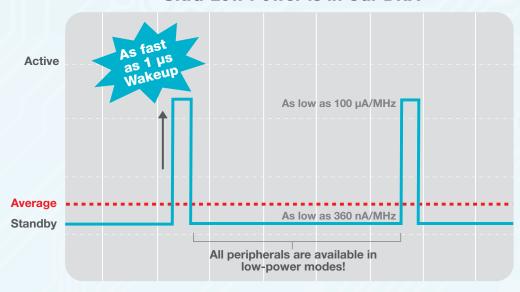
MSP430 MCUs also feature a direct memory access controller, enabling memory transfer with no CPU intervention. This means higher throughput of peripheral data and lower system power.

#### **Embedded FRAM enables lowest power**

- Industry-leading active power consumption (<100 µA/MHz)
- 250× less power than Flash writes
- Fast non-volatile writes

Learn more about MSP430 MCUs with embedded, non-volatile FRAM memory on page 10 or on the web at ti.com/fram

#### **Ultra-Low Power is in Our DNA**



# **Productive Low-Power Modes Allow:**

- Take ADC samples
- Transfer data throughout memory range
- Output PWM signal
- Update LCD
- Send and receive serial communication

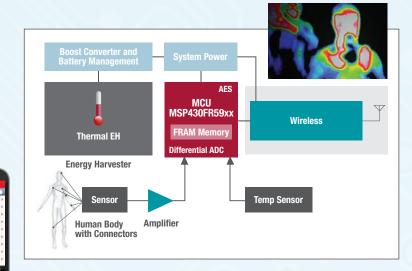
## Integration

Did you know that our MCUs offer integration designed with your applications in mind? The 400+ MSP430 devices offer high-performance peripherals including USB, RF, LCD controllers and Sigma-Delta ADCs. This allows designers to find the appropriate MSP430 device for many low power applications. This integration enables solutions with smaller physical footprints and reduced bill of materials costs.

#### **Energy Harvesting Sensor Network**

The MSP430FR59xx MCU with FRAM technology can control wireless sensor networks by harnessing the body heat of the user

- Differential ADC connect directly to sensors and limit interference
- Industry-standard communication protocols
- AES module protect your important data



# Application Processor 1.8 V PC SPI ADC MSP430F5229 Always On Co-Processor 1.8 V PC/SPI ADC Temperature Sensors TouchPad Inertial Sensors Fuel Gauge

#### **Co-Processor for Smart Devices**

The MSP430F52xx MCU can operate as an always on ultra-low-power co-processor to an applications processor. The MSP430 MCU can be used to offload functions such as sensor hub, keyboard control, battery and power management, capacitive touch, haptics and proximity detection.

 Split rail 1.8 V/3.3 V – directly connect to applications processors and sensors alike





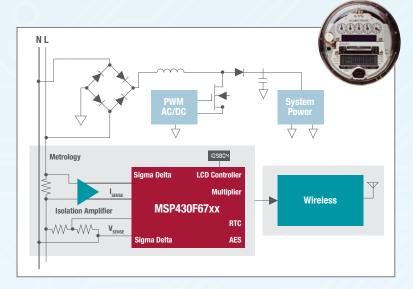


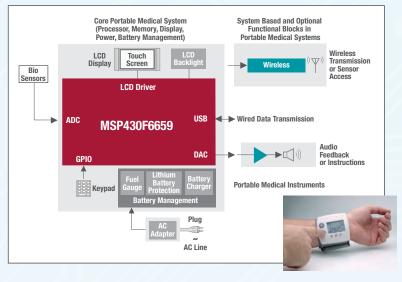
Our peripherals have been designed to give you maximum functionality and provide system-level interrupts, resets and bus arbitration at the lowest power. Many peripherals function autonomously, thereby minimizing CPU time spent in active mode. This means that MSP430 MCUs offer more performance with less power. The potential applications are endless!

#### **Intelligent Utility Meter**

The MSP430F67xx MCU is perfect for precisely measuring electricity usage.

- Up to 7 24-bit Sigma-Delta ADCs 2000:1 dynamic range
- Multiplier accelerate calculations
- RTC Module reliable meter readings
- Auxiliary power supply back up system support
- Meets or exceeds global regulatory requirements





#### **Portable Medical Devices**

The MSP430F6659 MCU has integrated peripherals to meet the needs of many portable medical applications.

- 512 KB Flash run virtually any wireless stack and enable over-the-air updates
- USB and LCD controllers
- 6 serial ports
- 16-channel, 12-bit ADC directly interface with analog sensors
- DAC drive speakers; generate bias for sensors







#### **Ease of Use**

#### **Software**

Our software ecosystem can help you tap into the ultra-low-power performance and intelligent peripherals of your MSP430 MCU. Support is available for a variety of professional and open-source integrated development environments. Using our complete suite of software tools, you can quicken time to market and maximize code efficiency. This means you can get started quicker with MSP430 MCUs!

#### **Integrated Development Environments**



TI's Code Composer Studio™ IDE: Free code-limited version



IAR Embedded Workbench®: Free code-limited version





MSPGCC and Energia: Free, open source, community driven and supported

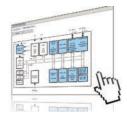
#### **Software Support Tools**



MSP430Ware<sup>™</sup> software: Driver library and a collection of code, data sheets and user guides



ULP Advisor™ software: Ensures code is optimized for ultra-low power



Grace<sup>™</sup> software: Easy-to-use GUI for enabling and configuring peripherals

# Don't Forget to Check Out Our Royalty-Free Software Libraries

**Peripheral Driver Library:** Intuitive function calls for configuring and using integrated peripherals

**Capacitive Touch Library:** Support for buttons, sliders, wheels and proximity sensors. Touch Pro GUI now available to evaluate, diagnose and tune capacitive touch sensors.

**Math Library:** Newly optimized to increase performance in your applications

**NFCLink:** Quickly and easily create NFC applications for TRF79xx NFC transceivers using an MSP430 MCU

**Energy Library:** Designed for quick ramp-up in metering, smart grid, energy monitoring and home/building automation applications where precise measurements are essential.

SimpliciTI<sup>™</sup>: Open-source software for building a network with battery-operated devices when pairing an MSP430 MCU and a TI RF transceiver such as the CC1101.

**Bluetooth® Stack:** Software stack for standard profiles like SPP and GATT provided when pairing an MSP430 MCU with a TI *Bluetooth* transceiver such as the CC256x.

Get started now at ti.com/msp430tools

#### **Hardware**

Now, let's talk hardware! MSP430 MCUs are supported by a broad collection of hardware development tools for beginners as well as experienced engineers. Our tools range from low-cost development kits like the MSP430 LaunchPad Evaluation Kit to highly-integrated, application-specific platforms and target boards for integrating MSP430 into your designs.

#### MSP430 LaunchPad and BoosterPack Ecosystem

#### LaunchPad Evaluation Kits

provide customers everything needed to get started, at \$9.99.

Try it out with Energia for the simplified user experience! Learn more about this easy-to-use IDE at **energia.nu/** 

**BoosterPacks** are plug-in modules for the LaunchPad, which enable customers to stack additional functionality such as wireless, capacitive touch and more.

Explore the ecosystem at **ti.com/launchpad** 



#### **Full-Featured Development Kits**



#### eZ430-CHRONOS

CC430-based RF wireless development kit in a sports watch form factor in 433, 868 and 915 MHz frequencies **Price: \$58** 



#### MSP-EXP430FG4618

Featuring MSP430FG4618 and MSP430F2013 on-board segmented display, buzzer, RS-232, capacitive touch, microphone, RF headers, JTAG

Price: \$117



#### MSP-EXP430F5529

Featuring MSP430F5529 complete USB development platform, on-board dot matrix display, JTAG, RF headers, on-board emulation, accelerometer, microSD, capacitive touch

Price: \$149



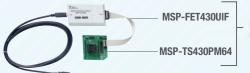
#### MSP-EXPCC430RF

Featuring CC430F6137 and CC430F5137 on-board emulation, segmented LCD, light sensor, includes F6137-based motherboard and F5137-based satellite board

Price: \$175

#### **Hardware Support Tools**

*One tool to rule them all.* The MSP430 Flash Emulation Tool (MSP-FET430UIF) supports all MSP430 devices when paired with the appropriate target board.



Bundle available MSP-FET430U64

#### **Production Programmer**

The MSP-GANG can program up to eight identical MSP430 Flash or FRAM devices at the same time and allows the user to fully customize the process.



#### We're Here for You!

- Reference designs available for many applications
- Direct support through our E2E<sup>™</sup> Forum at ti.com/e2e-msp430

Learn more at ti.com/msp430tools

MSP430<sup>™</sup> MCUs and TI can scale with your applications. The MSP430 portfolio consists of over 400 devices ranging from the MSP430 Value Line to our revolutionary, highly integrated microcontrollers with embedded FRAM memory.

	Low							FRAM	
Series	Voltage	Value Line	1 Series	2 Series	4 Series	5 Series	6 Series	Series	RF SoC
Part Number	L092	G2xxx	F1xx	F2xx	F4xx	F5xx	F6xx	FR5xxx	CC430
Max speed (MHz)	4	16	8	16	16	25	25	24	20
NVM (max KB)	0	56	60	120	120	512	512	64	32
SRAM (max KB)	2	4	10	8	8	66	66	2	4
GPI0	11	4-32	14-48	10-48	14-80	29-87	72-90	17-40	30-44
Comparator	•	•	•	•	•	•	•	•	•
Timer	•	•	•	•	•	•	•	•	•
ADC	•	•	•	•	•	•	•	•	•
DAC	•		•	•	•	•	•		
UART		•	•	•	•	•	•	•	•
I <sup>2</sup> C		•	•	•	•	•	•	•	•
SPI		•	•	•	•	•	•	•	•
Capacitive touch		•						•	
Multiplier		•	•	•	•	•	•	•	•
DMA			•	•	•	•	•	•	•
Op amps				•	•				
LCD					•		•		•
RTC					•	•	•	•	•
PMM						•	•	•	•
1.8-V I/O						•			
CRC						•	•	•	•
High-resolution timer						•			
USB						•	•		
Hardware encryption (AES)							•	•	•
FRAM								•	
RF									•

#### Check out the other great products from TI and unlock the full potential of your applications:

#### **Collect data**

Sensors – The TMP006 sensor measures the temperature of an object without the need to make contact with the object.

#### ti.com/sensorproducts

Analog Front Ends – Devices like the LMP91000 bridge the gap between sensors and the MSP430 MCU which can significantly simplify a system.

#### ti.com/analog



#### Power the system

Power – TI offers regulators, fuel gauges, and battery monitors. The TPS709 linear drop out regulator can regulate voltage to the MCU, while devices such as the TPS3839 reset IC can accurately track battery activity to ensure the MSP430 MCU remains in a safe state.

ti.com/power

#### Transmit and receive data

*Wireless* – TI offers radios ranging from sub-1 GHz to Wi-Fi®. The CC1101 is a highly integrated RF transceiver for low-power wireless applications in the 315-/433-/868-/915-MHz ISM bands. Devices like the high-performance TRF7970 13.56-MHz transceiver enable low-power NFC solutions. The CC2564 paired with an MSP430 MCU offer a dual-mode solution for *Bluetooth*® connectivity. The CC3000 is a self-contained Wi-Fi solution that simplifies Internet connectivity. **ti.com/wireless** 

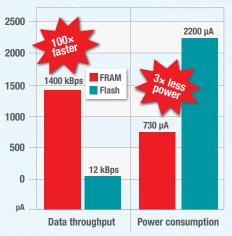


Technology	Hardware	Software	Additional information
NFC	TRF7970	NFCLink	ti.com/nfclink
Sub-1 GHz	CC1101	SimpliciTI™	ti.com/simpliciti
Bluetooth®/BLE	CC256x	Stonestreet One BT Stacks	ti.com/tool/stonestreetone-bt-sdk
Wi-Fi®	CC3000	SimpleLink	ti.com/simplelink
GPS	CC4000	SimpleLink	ti.com/simplelink

# **Device Catalog**

#### FRAM: The Future of Embedded Memory

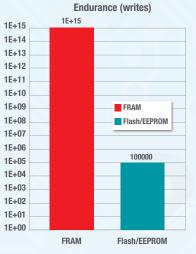
FRAM, or Ferroelectric Random Access Memory, is a non-volatile memory that combines the speed, ultra-low-power, endurance and flexibility of SRAM with the reliability and stability of Flash to combine program and data into one unified memory space for the lowest power and easiest-to-use microcontroller architecture. **ti.com/fram** 

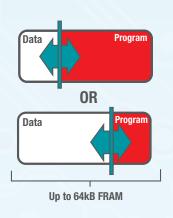


#### 9

#### What does this mean for you?

- Lower power
- Faster data throughput
- Virtually unlimited write endurance
- · Configurable as program or data memory





ED57v	Performance	Data
rnj/A	r ci iui illalice	Data

MSP430	FRA	M Se	ries	– Ul	o to	24	MHz												
						Time	rs			USCI									
Part Number	FRAM (KB)	SRAM (B)	CPU Speed (MHz)	GPIO (max)	Total	A*	В*	Watchdog and RTC	PMM: BOR, SVS, SVM, LDO	Ch A: UART/ Lin/irda/spi	Ch B: I <sup>2</sup> C/SPI	DMA	MPY	Comp E	Temp Sensor	ADC Ch/Res	Additional Features <sup>†</sup>	Pin/Package	1 ku Price (U.S. \$)
FR572x																			
MSP430FR5720	4	512	8	21	3	3, 3	3	•	•	1	1	•	•	•	•	6 ch ADC10	MPU	24RGE, 28PW	1.75
MSP430FR5721	4	512	8	32	5	3, 3	3, 3, 3	•	•	2	1	•	•	•	•	14 ch ADC10	MPU	38DA, 40RHA	1.80
MSP430FR5722	8	1024	8	21	3	3, 3	3	•	•	1	1	•	•	•	•	6 ch ADC10	MPU	24RGE, 28PW	1.85
MSP430FR5723	8	1024	8	32	5	3, 3	3, 3, 3	•	•	2	1	•	•	•	•	_	MPU	38DA, 40RHA	1.90
MSP430FR5724	8	1024	8	21	3	3, 3	3	•	•	1	1	•	•	•	•	6 ch ADC10	MPU	24RGE, 28PW	1.95
MSP430FR5725	8	1024	8	32	5	3,3	3, 3, 3	•	•	2	1	•	•	•	•	14 ch ADC10	MPU	38DA, 40RHA	2.05
MSP430FR5726	16	1024	8	21	3	3, 3	3	•	•	1	1	•	•	•	•	6 ch ADC10	MPU	24RGE, 28PW	2.00
MSP430FR5727	16	1024	8	32	5	3, 3	3, 3, 3	•	•	2	1	•	•	•	•	_	MPU	38DA, 40RHA	2.20
MSP430FR5728	16	1024	8	21	3	3, 3	3	•	•	1	1	•	•	•	•	6 ch ADC10	MPU	24RGE, 28PW	2.10
MSP430FR5729	16	1024	8	32	5	3, 3	3, 3, 3	•	•	2	1	•	•	•	•	14 ch ADC10	MPU	38DA, 40RHA	2.15
FR573x																			
MSP430FR5730	4	512	24	21	3	3, 3	3	•	•	1	1	•	•	•	•	6 ch ADC10	MPU	24RGE, 28PW	1.90
MSP430FR5731	4	512	24	32	5	3, 3	3, 3, 3	•	•	2	1	•	•	•	•	14 ch ADC10	MPU	38DA, 40RHA	1.95
MSP430FR5732	4	1024	24	21	3	3, 3	3	•	•	1	1	•	•	•	•	6 ch ADC10	MPU	24RGE, 28PW	2.00
MSP430FR5733	8	1024	24	32	5	3, 3	3, 3, 3	•	•	2	1	•	•	•	•	-	MPU	38DA, 40RHA	2.05
MSP430FR5734	8	1024	24	21	3	3, 3	3	•	•	1	1	•	•	•	•	6 ch ADC10	MPU	24RGE, 28PW	2.10
MSP430FR5735	8	1024	24	32	5	3, 3	3, 3, 3	•	•	2	1	•	•	•	•	14 ch ADC10	MPU	38DA, 40RHA	2.20
MSP430FR5736	16	1024	24	21	3	3, 3	3	•	•	1	1	•	•	•	•	6 ch ADC10	MPU	24RGE, 28PW	2.19
MSP430FR5737	16	1024	24	32	5	3, 3	3, 3, 3	•	•	2	1	•	•	•	•	_	MPU	38DA, 40RHA	2.30
MSP430FR5738	16	1024	24	21	3	3, 3	3	•	•	1	1	•	•	•	•	6 ch ADC10	MPU	24RGE, 28PW	2.35
MSP430FR5739	16	1024	24	32	5	3, 3	3, 3, 3	•	•	2	1	•	•	•	•	14 ch ADC10	MPU	38DA, 40RHA	2.45

<sup>1</sup>Prices are quoted in U.S. dollars and represent year 2013 suggested resale price. \*Represents number of capture/compare registers per timer. †MPU: Memory Protection Unit is used for memory segmentation and access management for code protection.

#### **Wolverine: Industry's Lowest Power MCU Platform**

#### Ultra-low-leakage process technology

- Unique mixed-signal ultra-low-leakage process technology
- Enables variety of new low-power peripherals
- Consistent low power over entire temperature range

#### Unparalleled performance with unified FRAM

- World's lowest-power write to a non-volatile memory is 250× less energy per bit
- Speed and flexibility of traditional RAM
- Near infinite endurance and 100% non-volatile

#### MSP430™ DNA evolved

- Continuing to pioneer the low-power landscape
- Leading power efficiency over entire system architecture
- Industry leading analog integration
- Complete software package for easiest development



I	Lowest Power Write	Lowest Standby Power	Lowest Active Power	Lowest Peripheral Power
	250× lower	500nA standby current	<100 µA/MHz	<b>75 μA</b>
	than Flash	with RTC		ADC12

MSP430	FRAI	M Se	ries	– UI	p to	<b>24</b>	MHz	(cont	inued)										
Part Number	FRAM (KB)	SRAM (B)	CPU Speed (MHz)	GPIO (max)	Total	Timers	B*	Watchdog and RTC	PMM: BOR, SVS, SVM, LDO	USCI Ch A: UART/ Lin/irda/spi	Ch B: I <sup>2</sup> C/SPI	DMA	MPY	Comp E	Temp Sensor	ADC Ch/Res	Additional Features <sup>†</sup>	Pin/Package	1 ku Price (U.S. \$)
FR58xx – Buil	t on the	e Wolv	erine Pl	latfori	'n				'	'	'						'		
MSP430FR5869	64	2	16	40	5	3, 3 2, 2	7	•	•	2	1	•	•	•	•	18 ch ADC12	MPU	48RGZ	_
MSP430FR5868	48	2	16	40	5	3, 3	7	•	•	2	1	•	•	•	•	18 ch ADC12	MPU	48RGZ	_
MSP430FR5867	32	1	16	40	5	3, 3	7	•	•	2	1	•	•	•	•	18 ch ADC12	MPU	48RGZ	_
MSP430FR5849	64	2	16	33	5	3, 3	7	•	•	2	1	•	•	•	•	16 ch ADC12	MPU	40RHA, 38DA	_
MSP430FR5848	48	2	16	33	5	3, 3 2, 2	7	•	•	2	1	•	•	•	•	16 ch ADC12	MPU	40RHA, 38DA	_
MSP430FR5847	32	1	16	33	5	3, 3 2, 2	7	•	•	2	1	•	•	•	•	16 ch ADC12	MPU	40RHA, 38DA	_
MSP430FR5859	64	2	16	33	5	3, 3 2, 2	7	•	•	2	1	•	•	•	•	16 ch ADC12	MPU	40RHA, 38DA	_
MSP430FR5858	48	2	16	33	5	3, 3 2, 2	7	•	•	2	1	•	•	•	•	16 ch ADC12	MPU	40RHA, 38DA	_
MSP430FR5857	32	1	16	33	5	3, 3 2, 2	7	•	•	2	1	•	•	•	•	16 ch ADC12	MPU	40RHA, 38DA	_
FR59xx – Buil	t on th	e Wolv	erine Pl	latfori	n														
MSP430FR5969	64	2	16	40	5	3, 3 2, 2	7	•	•	2	1	•	•	•	•	18 ch ADC12	MPU, AES	48RGZ	3.35
MSP430FR5968	48	2	16	40	5	3, 3 2, 2	7	•	•	2	1	•	•	•	•	18 ch ADC12	MPU, AES	48RGZ	_
MSP430FR5967	32	1	16	40	5	3, 3 2, 2	7	•	•	2	1	•	•	•	•	18 ch ADC12	MPU, AES	48RGZ	_
MSP430FR5949	64	2	16	33	5	3, 3 2, 2	7	•	•	2	1	•	•	•	•	16 ch ADC12	MPU, AES	40RHA, 38DA	_
MSP430FR5948	48	2	16	33	5	3, 3 2, 2	7	•	•	2	1	•	•	•	•	16 ch ADC12	MPU, AES	40RHA, 38DA	_
MSP430FR5947	32	1	16	33	5	3, 3 2, 2	7	•	•	2	1	•	•	•	•	16 ch ADC12	MPU, AES	40RHA, 38DA	_
MSP430FR5959	64	2	16	33	5	3, 3 2, 2	7	•	•	2	1	•	•	•	•	16 ch ADC12	MPU, AES	40RHA, 38DA	_
MSP430FR5958	48	2	16	33	5	3, 3 2, 2	7	•	•	2	1	•	•	•	•	16 ch ADC12	MPU, AES	40RHA, 38DA	_
MSP430FR5957	32	1	16	33	5	3, 3 2, 2	7	•	•	2	1	•	•	•	•	16 ch ADC12	MPU, AES	40RHA, 38DA	_

<sup>1</sup>Prices are quoted in U.S. dollars and represent year 2013 suggested resale price.

\*Represents number of capture/compare registers per timer.

†MPU: Memory Protection Unit is used for memory segmentation and access management for code protection.

New products are listed in **bold red**.



# **Device Catalog**

#### **Value Line**

#### High performance for cost-sensitive applications

The MSP430G2xx 16-bit microcontroller features Flash-based ultra-low-power MCUs up to 16 MIPS with 1.8V — 3.6V operation. Includes the very-low power oscillator (VLO), internal pull-up/pull-down resistors and low-pin-count options.

#### Device parameters

Flash options: 0.5 KB – 56 KB
RAM options: 128 B – 4 KB
GPIO options: 10, 16, 24, 32 pins

• ADC options: Slope, 10-bit SAR

• Other integrated peripherals: Capacitive Touch I/O (CT), High Frequency Oscillator (HF)

W3P43U	uZXX	vait	ie Lin			· up	to 16 MHz									
Part Number	Flash (KB)	SRAM (B)	I/O (max)	Total	imers A <sup>*</sup>	B*	Watchdog	BOR	USI: I <sup>2</sup> C/SPI	USCI: I <sup>2</sup> C/SPI/ UART	Comp_A+	Temp Sensor	ADC Ch/Res	Additional Features	Packages	1k Pric (U.S
32xx0	0	100					_						Olean		00010	0.0
/ISP430G2210	2	128	4	1	2	_	•	•	_	_	•	_	Slope	_	8S0IC	0.3
/ISP430G2230	2	128	4	1	2	_	•	•	•		_	•	4 ch ADC10		8SOIC	0.4
2xx1																
/ISP430G2001	0.5	128	10	1	2	_	•		_	_	_	_	_	_	14PW, N; 16RSA	0.
//SP430G2101	1	128	10	1	2	_	•		_	_	_	_	_	_	14PW, N; 16RSA	0.
MSP430G2121	1	128	10	1	2	_	•		•	_	_	_	_	_	14PW, N; 16RSA	0.
/ISP430G2201	2	128	10	1	2	_	•	•	_	_	_	_	_	_	14PW, N; 16RSA	0.
/ISP430G2221	2	128	10	1	2	_	•	•	•	_	_	_	_	_	14PW, N; 16RSA	0.
MSP430G2111	1	128	10	1	2	_	•		_	_	•	_	_	_	14PW, N; 16RSA	0.
MSP430G2211	2	128	10	1	2	_			_	_		_	_	_	14PW, N; 16RSA	0.
MSP430G2131	1	128	10	1	2	_			•	_	- i	•	8 ch ADC10	_	14PW, N; 16RSA	0.
ASP430G2231	2	128	10	1	2	_					_		8 ch ADC10	_	14PW, N; 16RSA	0.
		120	10					_				_	O CITADOTO		14FW, N, TOROA	U.
2xx2		0.00	4.0	-										27	4 4 DULY 00 DULY 11 4 0 DO 4	
/ISP430G2102	1	256	16	1	3	_	•	•	•	_	_	_		CT	14PW; 20PW, N; 16RSA	0.
/ISP430G2202	2	256	16	1	3	_	•	•	•	_	_	_	_	CT	14PW; 20PW, N; 16RSA	0.
/ISP430G2302	4	256	16	1	3	_	•	•	•	_	_	_	_	CT	14PW; 20PW, N; 16RSA	0.
/ISP430G2402	8	256	16	1	3	_	•		•	_	_	_	_	CT	14PW; 20PW, N; 16RSA	0.
/ISP430G2112	1	256	16	1	3	_	•		•	_		_	_	CT	14PW; 20PW, N; 16RSA	0.
MSP430G2212	2	256	16	1	3	_	•		•	_	•	_	_	CT	14PW; 20PW, N; 16RSA	0.
//SP430G2312	4	256	16	1	3	_	•	•	•	_	•	_	_	CT	14PW; 20PW, N; 16RSA	0.
/ISP430G2412	8	256	16	1	3	_	•		•	_	•	_	_	CT	14PW; 20PW, N; 16RSA	0.
ISP430G2132	1	256	16	1	3	_				_	_	•	8 ch ADC10	CT	14PW: 20PW. N: 16RSA	0.
ISP430G2232	2	256	16	1	3	_				_	_		8 ch ADC10	CT	14PW; 20PW, N; 16RSA	0.
MSP430G2332	4	256	16	1	3	_				_	_		8 ch ADC10	CT	14PW; 20PW, N; 16RSA	0.
MSP430G2432	8	256	16	1	3	_				_			8 ch ADC10	CT	14PW; 20PW, N; 16RSA	0.
MSP430G2152	1	256	16	1	3						•		8 ch ADC10	CT	14PW; 20PW, N; 16RSA	0.
ASP430G2252	2	256	16	1	3									CT		
						_	_		_	_	_		8 ch ADC10		14PW; 20PW, N; 16RSA	0.
MSP430G2352	4	256	16	1	3	_	•	•	•	_	•	•	8 ch ADC10	CT	14PW; 20PW, N; 16RSA	0.
/ISP430G2452	8	256	16	1	3	_	•	•	•		•	•	8 ch ADC10	CT	14PW; 20PW, N; 16RSA	0.
i2xx3																
/ISP430G2203	2	256	24	2	3	_	•		_	•	_	_	_	CT	20PW, N; 28PW; 32RHB	0.
MSP430G2303	4	256	24	2	3	_	•		_		_	_	_	CT	20PW, N; 28PW; 32RHB	0.
/ISP430G2403	8	512	24	2	3	_	•		_	•	_	_	_	CT	20PW, N; 28PW; 32RHB	0.
MSP430G2213	2	256	24	2	3	_	•	•	_	•	•	_	_	CT	20PW, N; 28PW; 32RHB	0.
ASP430G2313	4	256	24	2	3	_	•		_	•	•	_	_	CT	20PW, N; 28PW; 32RHB	0.
MSP430G2413	8	512	24	2	3	_			_			_	_	CT	20PW, N; 28PW; 32RHB	0.
MSP430G2513	16	512	24	2	3	_							_	CT	20PW, N; 28PW; 32RHB	0.
MSP430G2133	1	256	24	2	3						_		8 ch ADC10	CT	20PW, N; 28PW; 32RHB	0.
1SP430G2233	2	256	24	2	3	_					_		8 ch ADC10	CT	20PW, N; 28PW; 32RHB	0.
	4	256	24		3							_		CT		
1SP430G2333				2		_	_			_		•	8 ch ADC10		20PW, N; 28PW; 32RHB	0.
ISP430G2433	8	512	24	2	3	_	•	•	_	•	_	•	8 ch ADC10	CT	20PW, N; 28PW; 32RHB	0.
1SP430G2533	16	512	24	2	3	_	•		_	•	_		8 ch ADC10	CT	20PW, N; 28PW; 32RHB	0.
ISP430G2153	1	256	24	2	3	_	•	•	_	•	•	•	8 ch ADC10	CT	20PW, N; 28PW; 32RHB	0.
ISP430G2253	2	256	24	2	3	_	•	•	_	•		•	8 ch ADC10	CT	20PW, N; 28PW; 32RHB	0.
ISP430G2353	4	256	24	2	3	_	•		_	•	•		8 ch ADC10	CT	20PW, N; 28PW; 32RHB	0.
ISP430G2453	8	512	24	2	3	_	•	•	_	•	•	•	8 ch ADC10	CT	20PW, N; 28PW; 32RHB	0.
ISP430G2553	16	512	24	2	3	_	•	•	_	•	•	•	8 ch ADC10	CT	20PW, N; 28PW; 32RHB	0.
2xx4																
1SP430G2444	8	512	32	2	3	3	•	•		•	_	•	12 ch ADC10	HF	38DA, 40RHA, 49YFF	1.
ASP430G2544	16	512	32	2	3	3					_		12 ch ADC10	HF	38DA, 40RHA, 49YFF	1.
ISP430G2744	32	1024	32	2	3	3							12 ch ADC10	HF	38DA, 40RHA, 49YFF	
	32	1024	32		J	J	_	_		_	_	-	12 UTADUTU	ПГ	JOUA, 4UNTA, 49TFF	1.
2xx5		4004	00	_	_	_							40 1 10015	OT	0004 100111	
/ISP430G2755	32	4094	32	3	3	3	•	•	_	•	•	•	12 ch ADC10	CT, HF	38DA, 40RHA	1.
/ISP430G2855	48	4094	32	3	3	3	•	•	_	•	•	•	12 ch ADC10	CT, HF	38DA, 40RHA	1.
ASP430G2955	56	4094	32	3	3	3	•	•	_				12 ch ADC10	CT, HF	38DA, 40RHA	1.

<sup>&</sup>lt;sup>1</sup>Prices are quoted in U.S. dollars and represent year 2013 suggested resale price for TSSOP package.

New products are listed in **bold red**.

<sup>\*</sup>Represents number of capture/compare registers per timer.

#### F - Family

#### Ultra-low power with increased integration and performance

Our largest family of devices, offers ultra-low power with options featuring unmatched analog and digital integration.

#### Device parameters

• Up to 25 MHz

• Flash options: 0.5 KB – 512 KB • RAM options: 128 B − 64 KB • GPIO options: Up to 90 pins

	Flash	SRAM	1/0		Timers					USART				Temp	ADC	Additional		1 ku Price
Part Number	(KB)	(B)	(max)	Total	A*	B*	Watchdog	BOR	svs	(UART/SPI)	DMA	MPY	Comp A	Sensor	Ch/Res	Features	Package(s)	(U.S. \$)
F11x1			, ,							· í			, i -				• • • • • • • • • • • • • • • • • • • •	
MSP430F1101A	1	128	14	1	3	_	•	_	_	_	_	_	•	_	Slope	_	20DGV, DW, PW; 24RGE	1.06
MSP430F1111A	2	128	14	1	3	_	•	_	_	_	_	_	•	_	Slope	_	20DGV, DW, PW; 24RGE	1.22
WSP430F1121A	4	256	14	1	3	_	•	_	_	_	_	_	•	_	Slope	_	20DGV, DW, PW; 24RGE	1.56
F11x2																		
MSP430F1122	4	256	14	1	3	_	•	•	_	_	_	_	_	•	5 ch ADC10	_	20DW, PW; 32RHB	1.67
MSP430F1132	8	256	14	1	3	_		•	_	_	_	_	_		5 ch ADC10	_	20DW, PW: 32RHB	1.78
F12x																	, ,	
MSP430F122	4	256	22	1	3	_	•	_	_	1	_	_	•	_	Slope	_	28DW, PW; 32RHB	1.67
MSP430F123	8	256	22	1	3	_	•	_	_	1	_	_	•	_	Slope	_	28DW, PW; 32RHB	1.72
F12x2																		
MSP430F1222	4	256	22	1	3	_	•	•	_	1	_	_	_	•	8 ch ADC10	_	28DW, PW; 32RHB	1.72
MSP430F1232	8	256	22	1	3	_	•	•	_	1	_	_	_	•	8 ch ADC10	_	28DW, PW; 32RHB	1.83
F13x																		
MSP430F133	8	256	48	2	3	3	•	_	_	1	_	_	•	•	8 ch ADC12	_	64PM, PAG, RTD	2.94
MSP430F135	16	512	48	2	3	3	•	_	_	1	_	_	•	•	8 ch ADC12	_	64PM, PAG, RTD	3.28
F13x1																		
MSP430F1331	8	256	48	2	3	3	•	_	_	1	_	_	•	_	Slope	_	64PM, RTD	_
MSP430F1351	16	512	48	2	3	3	•	_	_	1	_	_	•	_	Slope	_	64PM, RTD	_
F14x																		
MSP430F147	32	1024	48	2	3	7	•	_	_	2	_	16×16	•	•	8 ch ADC12	_	64PM, PAG, RTD	4.00
MSP430F148	48	2048	48	2	3	7	•	_	_	2	_	16×16	•	•	8 ch ADC12	_	64PM, PAG, RTD	4.33
MSP430F149	60	2048	48	2	3	7	•	_	_	2	_	16×16	•	•	8 ch ADC12	_	64PM, PAG, RTD	4.66
MSP430F1471	32	1024	48	2	3	7	•	_	_	2	_	16×16	•	_	Slope	_	64PM, RTD	4.55
MSP430F1481	48	2048	48	2	3	7	•	_	_	2	_	16×16	•	_	Slope	_	64PM, RTD	5.11
MSP430F1491	60	2048	48	2	3	7	•	_	_	2	_	16×16	•	_	Slope	_	64PM, RTD	5.11
F15x																		
MSP430F155	16	512	48	2	3	3	•	•	•	1 with I <sup>2</sup> C	•	_	•	•	8 ch ADC12	(2) DAC12	64PM, RTD	5.38
MSP430F156	24	1024	48	2	3	3	•	•	•	1 with I <sup>2</sup> C	•	_	•		8 ch ADC12	(2) DAC12	64PM, RTD	5.61
MSP430F157	32	1024	48	2	3	3	•	•	•	1 with I <sup>2</sup> C	•		•	•	8 ch ADC12	(2) DAC12	64PM, RTD	5.95
F16x																		
MSP430F167	32	1024	48	2	3	7	•	•	•	2 with I2C	•	16×16	•	•	8 ch ADC12	(2) DAC12	64PM, RTD	6.88
MSP430F168	48	2048	48	2	3	7	•	•	•	2 with I <sup>2</sup> C	•	16×16	•	•	8 ch ADC12	(2) DAC12	64PM, RTD	7.61
MSP430F169	60	2048	48	2	3	7	•	•	•	2 with I <sup>2</sup> C	•	16×16	•	•	8 ch ADC12	(2) DAC12	64PM, RTD	8.16
MSP430F1610	32	5120	48	2	3	7	•	•	•	2 with I <sup>2</sup> C	•	16×16	•	•	8 ch ADC12	(2) DAC12	64PM, RTD	8.72
MSP430F1611	48	10240	48	2	3	7	•	•	•	2 with I <sup>2</sup> C	•	16×16	•	•	8 ch ADC12	(2) DAC12	64PM, RTD	9.16
WSP430F1612	55	5120	48	2	3	7	•			2 with I2C		16×16	•	•	8 ch ADC12	(2) DAC12	64PM, RTD	9.50

<sup>&</sup>lt;sup>1</sup>Prices are quoted in U.S. dollars and represent year 2013 suggested resale price. \*Represents number of capture/compare registers per timer.

Analog Fr	ont En	id (MS	P430A	FE2	(x) <b>S</b>	Seri	es – Up	to 1	2 MH	z						
				1	imers											1 ku
Part Number	Flash (KB)	SRAM (B)	I/O (max)	Total	A*	B*	Watchdog	BOR	svs	USART (UART/SPI)	MPY	Temp Sensor	ADC Ch/Res	Additional Features	Package(s)	Price <sup>1</sup> (U.S. \$)
AFE2xx																
MSP430AFE221	4	256	11	1	3	_	•	•	•	•	•	•	(1) SD24	_	24PW	1.80
MSP430AFE222	4	256	11	1	3	_	•	•	•	•	•	•	(2) SD24	_	24PW	1.95
MSP430AFE223	4	256	11	1	3	_	•	•	•	•	•	•	(3) SD24	_	24PW	2.10
MSP430AFE231	8	512	11	1	3	_	•	•	•	•	•	•	(1) SD24	_	24PW	1.85
MSP430AFE232	8	512	11	1	3	_	•	•		•	•	•	(2) SD24	_	24PW	2.00
MSP430AFE233	8	512	11	1	3	_	•	•	•	•	•	•	(3) SD24	_	24PW	2.10
MSP430AFE251	16	512	11	1	3	_	•	•		•	•		(1) SD24	_	24PW	1.90
MSP430AFE252	16	512	11	1	3	_	•	•	•	•	•	•	(2) SD24	_	24PW	2.05
MSP430AFE253	16	512	11	1	3	_	•	•		•	•	•	(3) SD24	_	24PW	2.20

<sup>&</sup>lt;sup>1</sup>Prices are quoted in U.S. dollars and represent year 2013 suggested resale price.

				Ti	imers						USC	CI								
Part Number F20xx	Flash (KB)	SRAM (B)	I/O (max)	Total	A*	B.	Watchdog	BOR	svs	USI: (I <sup>2</sup> C/SPI)	Ch A: UART/LIN/ IrDA/SPI	Ch B: I <sup>2</sup> C/SPI	DMA	МРҮ	Comp_A+	Temp Sensor	ADC Ch/Res	Additional Features	Package(s)	1 k Prio (U.S
MSP430F2001	1	128	10	1	2	_			_	_	_	_	_	_		_	Slope	105 °C	14PW, N; 16RSA	0.4
MSP430F2011	2	128	10	1	2	_			_	_	_	_	_	_		_	Slope	105 °C	14PW, N; 16RSA	0.
MSP430F2002	1	128	10	1	2	_			_	•	_	_	_	_	_		8 ch ADC12	105 °C	14PW, N; 16RSA	0.0
MSP430F2012	2	128	10	1	2	_	•	•	_	•	_	_	_	_	_	•	8 ch ADC12	105 °C	14PW, N; 16RSA	0.8
MSP430F2003	1	128	10	1	2	_	•	•	_	•	_	_	_	_	_	•	4 ch, SD16_A	105 °C	14PW, N; 16RSA	0.
MSP430F2013	2	128	10	1	2	_	•	•	_	•	_	_	_	_	_	•	4 ch, SD16_A	105 °C	14PW, N; 16RSA	1.
-21xx																				
MSP430F2101	1	128	16	1	3	_	•	•	_	_	_	_	_	_	•	_	Slope	105 °C	20DGV, DW, PW; 24RGE	0.
MSP430F2111	2	128	16	1	3	_	•	•	_	_	_	_	_	_	•	_	Slope	105 °C	20DGV, DW, PW; 24RGE	0.
MSP430F2121	4	256	16	1	3	_	•	•	_	_	_	_	_	_	•	_	Slope	105 °C	20DGV, DW, PW; 24RGE	0.
MSP430F2131	8	256	16	1	3	_	•	•	_	_	_	_	_	_	•	_	Slope	105 °C	20DGV, DW, PW; 24RGE	1.
MSP430F2112	2	256	22	2	3, 2		•	•	_	_	1	1	-	_	•	•	8 ch ADC12	105 °C	28PW; 32RHB, RTV	1.1
MSP430F2122	4	512	22		3, 2		•	•	_	_	1	1	_	_	•	•	8 ch ADC12	105 °C	28PW; 32RHB, RTV	1.
MSP430F2132	8	512	22	2	3, 2	_	•	•	_	_	1	1	_	_	•	•	8 ch ADC12	105 °C	28PW; 32RHB, RTV	1.5
22x2																				
MSP430F2232	8	512	32	2	3	3	•	•	_	_	1	1	_	_	_	•	12 ch ADC10	105 °C	38DA; 40RHA; 49YFF	1.
MSP430F2252	16	512	32	2	3	3	•	•	_	_	1	1	_	_	_	•	12 ch ADC10	105 °C	38DA; 40RHA; 49YFF	1.
MSP430F2272 F22x4	32	1024	32	2	3	3	•	•	Ē	_	1	1	Ē	_	_	•	12 ch ADC10	105 °C	38DA; 40RHA; 49YFF	2.
MSP430F2234	8	512	32	2	3	3	•	•	_	_	1	1	-	_	_	•	12 ch ADC10	(2) OPAMP, 105 °C	38DA; 40RHA; 49YFF	1.5
/ISP430F2254	16	512	32	2	3	3	•	•	_	_	1	1	_	_	_	•	12 ch ADC10	(2) OPAMP, 105 °C	38DA; 40RHA; 49YFF	2.
MSP430F2274	32	1024	32	2	3	3	•	•	_	_	1	1	_	_	_	•	12 ch ADC10	(2) OPAMP, 105 °C	38DA; 40RHA; 49YFF	2.
-23x0																				
MSP430F2330	8	1024	32	2	3	3	•	•	_	_	1	1	_	16×16	•	_	Slope	105 °C	40RHA; 49YFF	1.
MSP430F2350	16	2048	32	2	3	3	•	•	_	_	1	1	_	16×16	•	_	Slope	105 °C	40RHA; 49YFF	1.5
MSP430F2370	32	2048	32	2	3	3	•	•	_	_	1	1	_	16×16	•	_	Slope	105 °C	40RHA; 49YFF	1.
-23x																				
MSP430F233	8	1024	48	2	3	3	•	•	•	_	1	1	_	16×16	•	•	8 ch ADC12	105 °C	64PM, RGC	2.
MSP430F235	16	2048	48	2	3	3				_	1	1	_	16×16	•		8 ch ADC12	105 °C	64PM, RGC	2.
-24x/10	10	2010	10	_		U	_			_				10/10		Ť	OUITIDOTE	100 0	o ii iii, itao	
MSP430F247	32	4096	48	2	3	7				_	2	2	_	16×16	•	•	8 ch ADC12	105 °C	64PM, RGC	2.
MSP430F247	48	4096		2	3					_	2	2	_				8 ch ADC12	105 °C		
			48			7				_			_	16×16	•				64PM, RGC	3.
MSP430F249	60 EG	2048	48	2	3	7		•		_	2	2	_	16×16	•		8 ch ADC12	105 °C	64PM, RGC	3.
MSP430F2410	56	4096	48	2	3	7	•	•	•	_	2	2		16×16	•	•	8 ch ADC12	105 °C	64PM, RGC	4.
F24x1	25	400-	10											46 ::			C	40= 5=	04211 262	
MSP430F2471	32	4096	48	2	3	7	•	•	•	_	2	2	_	16×16	•	_	Slope	105 °C	64PM, RGC	3.
MSP430F2481	48	4096	48	2	3	7	•	•	•	_	2	2	_	16×16	•	_	Slope	105 °C	64PM, RGC	3.
MSP430F2491	60	2048	48	2	3	7	•	•	•		2	2	_	16×16	•	_	Slope	105 °C	64PM, RGC	3.
241x																				
MSP430F2416	92	4096	48/64	2	3	7	•	•	•	_	2	2	_	16×16	•	•	8 ch ADC12	105 °C	64PM; 80PN; 113ZQW	4.
MSP430F2417	92	8192	48/64	2	3	7	•	•	•	_	2	2	_	16×16	•	•	8 ch ADC12	105 °C	64PM; 80PN; 113ZQW	4.
MSP430F2418	116	8192	48/64	2	3	7	•	•	•	_	2	2	_	16×16	•	•	8 ch ADC12	105 °C	64PM; 80PN; 113ZQW	5.0
MSP430F2419	120	4096	48/64	2	3	7	•	•	•	_	2	2	_	16×16	•	•	8 ch ADC12	105 °C	64PM; 80PN;113ZQW	5.3
-261x																				
MSP430F2616	92	4096	48/64	2	3	7	•	•	•	-	2	2	•	16×16	•	•	8 ch ADC12	(2) DAC12, 105 °C	64PM; 80PN; 113ZQW	5.8
MSP430F2617	92	8192	48/64	2	3	7	•	•	•	-	2	2	•	16×16	•	•	8 ch ADC12	(2) DAC12, 105 °C	64PM; 80PN; 113ZQW	6.0
MSP430F2618	116	8192	48/64	2	3	7	•	•	•	_	2	2	•	16×16	•	•	8 ch ADC12	(2) DAC12, 105 °C	64PM; 80PN; 113ZQW	6.3
																		100 0		

<sup>&</sup>lt;sup>1</sup>Prices are quoted in U.S. dollars and represent year 2013 suggested resale price. \*Represents number of capture/compare registers per timer.

MSP430I	-4X)	c Sei	ies	– u	pτ	O II		WI	III L	עט												
	Flash	SRAM	1/0		Timers		Watchdog and Basic		2112	USART	USC Ch A: UART/LIN/	Ch B:	LCD				Temp	CPU Speed	ADC	Additional		1 ku Price
art Number F41x	(KB)	(B)	(max)	Total	A*	B*	Timer	BOR	SVS	(UART/SPI)	IrDA/SPI	I <sup>2</sup> C/SPI	Segments	DMA	MPY	Comp_A	Sensor	(MIPS)	Ch/Res	Features	Package(s)	(U.S.
NSP430F412	4	256	48	1	3	-	•	•	•	/ –		_	96	_	_	•	7-	8	Slope	<del></del>	64PM, RTD	1.30
ISP430F413	8	256	48	1	3	-	•	•	•	_	_	-	96	_	_	•	_	8	Slope	_	64PM, RTD	1.35
ISP430F415	16	512	48	2	3, 5	-	•	•	•	_	_	_	96	_	_	•	_	8	Slope	_	64PM, RTD	1.50
ISP430F417	32	1024	48	2	3, 5	_	•	•	•		_	_	96	_	_	•	_	8	Slope		64PM, RTD	1.75
41x2																						
ISP430F4132	8	512	56	2	3, 5	_	•	•	•	_	1	1	144	_	_	•	•	8	8 ch ADC10	_	48RGZ; 64PM	1.50
ISP430F4152	16	512	56	2	3, 5	_	•	•	•	_	1	1	144	_	_	•	•	8	8 ch ADC10	_	48RGZ; 64PM	1.70
42x																						
/ISP430F423A	8	256	14	1	3	_	•	•	•	1	_	_	128	_	16×16	_	•	8	(3) SD16	_	64PM	2.04
NSP430F425A	16	512	14	1	3	_	•	•	•	1	_	_	128	_	16×16	_	•	8	(3) SD16	_	64PM	2.30
/ISP430F427A	32	1024	14	1	3	_	•	•	•	1	_	_	128	_	16×16	_	•	8	(3) SD16	_	64PM	2.47
W42x																						
NSP430FW423	8	256	48	2	3, 5	_	•	•	•	_	_	_	96	_	_	•	_	8	Slope	SCAN IF	64PM	2.50
NSP430FW425	16	512	48	2	3, 5	_	•	•	•	_	_	_	96	_	_	•	_	8	Slope	SCAN_IF	64PM	2.80
ISP430FW427	32	1024	48	2	3, 5	_	•	•	•	_	_	_	96	_	_	•	_	8	Slope	SCAN_IF	64PM	3.10
MSP430FW428	48	2048	48	2	3, 5	_	•	•	•	_	_	_	96	_	_	•	_	8	Slope	SCAN_IF	64PM	3.30
1SP430FW429	60	2048	48	2	3, 5	_	•	•	•	_	_	_	96	_	_	•	_	8	Slope	SCAN_IF	64PM	3.55
E42x																						
ISP430FE423A	8	256	14	1	3	_	•	•	•	1	_	_	128	_	16×16	_	_	8	(3) SD16	ESP430	64PM	3.78
ISP430FE425A	16	512	14	1	3	_	•	•	•	1	_	_	128	_	16×16	_	_	8	(3) SD16	ESP430	64PM	5.12
ISP430FE427A	32	1024	14	1	3	_	•	•	•	1	_	_	128	_	16×16	_	_	8	(3) SD16	ESP430	64PM	4.39
ISP430FE4232	8	256	14	1	3	_	•	•	•	1	_	_	128	_	16×16	_	_	8	(2) SD16	ESP430	64PM	2.15
ISP430FE4242	12	512	14	1	3	_	•	•	•	1	_	_	128	_	16×16	_	_	8	(2) SD16	ESP430	64PM	2.25
ISP430FE4252	16	512	14	1	3	_	•	•	•	1	_	_	128	_	16×16	_	_	8	(2) SD16	ESP430	64PM	2.35
ISP430FE4272 42x0	32	1024	14	1	3	_	•	•	•	1	_	_	128	_	16×16	_	_	8	(2) SD16	ESP430	64PM	2.45
1SP430F4250	16	256	32	1	3	_						_	56					0	E oh CD16 A	DAC12	40DL DC7	2.05
/ISP430F4260	24	256	32	1	3	Ξ	•		_	_	_	_	56	_	_	_	_	8	5 ch, SD16_A 5 ch, SD16_A	DAC12 DAC12	48DL, RGZ 48DL, RGZ	3.05
1SP430F4200 1SP430F4270	32	256	32	1	3								56						5 ch, SD16_A	DAC12	48DL, RGZ	3.65
G42x0	JL	200	JL	÷	J		_	_	-			_	30	-	_	-	-	U	3 GII, 0D IO_A	DAUTZ	HODE, HOZ	0.00
U42XU																				DA040		
MSP430FG4250	16	256	32	1	3	-	•	•	-	_	_	-	56	-	-	_	•	8	5 ch, SD16_A	DAC12, (2) OPAMP	48DL, RGZ	3.30
MSP430FG4260	24	256	32	1	3	-	•	•	_	_	_	-	56	-	-	-	•	8	5 ch, SD16_A	DAC12, (2) OPAMP	48DL, RGZ	3.60
MSP430FG4270	32	256	32	1	3	_	•	•	_	_	_	_	56	_	_	_	•	8	5 ch, SD16_A	DAC12, (2) OPAMP	48DL, RGZ	4.00
43x																						
NSP430F435	16	512	48	2	3	3	•	•	•	1	_	_	128/160	-	_	•	•	8	8 ch ADC12	_	80PN; 100PZ	3.40
NSP430F436	24	1024	48	2	3	3	•	•	•	1		_	128/160	_	_	•	•	8	8 ch ADC12	_	80PN; 100PZ	3.70
ISP430F437	32	1024	48	2	3	3	•	•	•	1	_	_	128/160	_	_	•	•	8	8 ch ADC12	_	80PN; 100PZ	
43x1																						
MSP430F4351	16	512	48	2	3	3	•			1	_		128/160					8	Slope	_	80PN; 100PZ	2.65
ISP430F4361	24	1024	48	2	3	3				1	_		128/160	_			_	8	Slope	_	80PN; 100PZ	3.50
ISP430F4361	32	1024	48	2	3	3				1			128/160		_		_	8	Slope	_	80PN; 100PZ 80PN; 100PZ	
G43x	JZ	1024	+0	2	J	J							120/100					U	olohe		JULIN, TUUFZ	5.00
MSP430FG437	32	1024	48	2	3	3	•	•	•	1	_	_	128	•	_			8	12 ch ADC12	(2) DAC12,	80PN	3.85
							_			-				_					12 ch ADC12	(3) OPAMP (2) DAC12,		
MSP430FG438	48	2048	48	2	3	3	•	•	•	1	_	_	128	•	_	•	•			(3) OPAMP (2) DAC12,	80PN	4.50
ISP430FG439	60	2048	48	2	3	3	•	•	•	1	_	_	128	•	_	•	•	8	12 ch ADC12	(3) OPAMP	80PN	5.25
44x																						
ISP430F4481	48	2048	48	2	3	7	•	•	•	2	_	_	160		16×16	•	_	8	_	_	100PZ	4.05
ISP430F4491	60	2048	48	2	3	7	•	•	•	2	_	_	160		16×16	•	_	8	_	_	100PZ	4.40
ISP430F447	32	1024	48	2	3	7	•	•	•	2	_	_	160		16×16	•	•	8	8 ch ADC12	_	100PZ	4.05
NSP430F448	48	2048	48	2	3	7	•	•	•	2	_	_	160		16×16	•	•	8	8 ch ADC12	_	100PZ	4.35
ISP430F449	60	2048	48	2	3	7				2			160		16×16			8	8 ch ADC12		100PZ	4.65

# **Device Catalog**

14301	-4X)	Ser	ies				D IVINZ	WII	III L	CD (co												
Part Number G461x	Flash (KB)	SRAM (B)	I/0 (max)	Ti Total	mers A <sup>*</sup>	B*	Watchdog and Basic Timer	BOR	svs	USART (UART/SPI)	USC Ch A: UART/LIN/ IrDA/SPI	Ch B: I <sup>2</sup> C/SPI	LCD Segments	DMA	MPY	Comp_A	Temp Sensor	CPU Speed (MIPS)	ADC Ch/Res	Additional Features	Package(s)	1   Pri (U.S
MSP430FG4616	92	4096	80	2	3	7	•	•	•	1	1	1	160	•	16×16	•	•	8	12 ch ADC12	(2) DAC12, +RTC (3) OPAMP	100PZ; 113ZQW	7.
MSP430FG4617	92	8192	80	2	3	7	•	•	•	1	1	1	160	•	16×16	•	•	8	12 ch ADC12	(2) DAC12, +RTC (3) OPAMP	100PZ; 113ZQW	7.
MSP430FG4618	116	8192	80	2	3	7	•	•	•	1	1	1	160	•	16×16	•	•	8	12 ch ADC12	(2) DAC12, +RTC (3) OPAMP	100PZ; 113ZQW	8.
MSP430FG4619	120	4096	80	2	3	7	•	•	•	1	1	1	160	•	16×16	•	•	8	12 ch ADC12	(2) DAC12, +RTC (3) OPAMP	100PZ; 113ZQW	7.
F461x		-		•			-								-			-		(0) 0.7		
MSP430F46161	92	4096	80	2	3	7	•	•	•	1	1	1	160	•	16×16	•	_	8	_	_	100PZ	5.
MSP430F46171	92	8192	80	2	3	7	•	•	•	1	1	1	160	•	16×16		_	8	_	_	100PZ	5.
MSP430F46181	116	8192	80	2	3	7	•	•	•	1	1	1	160	•	16×16	•	_	8	_	_	100PZ	6.
MSP430F46191	120	4096	80	2	3	7	•	•	•	1	1	1	160	•	16×16	•	_	8	_	_	100PZ	5.
MSP430F4616	92	4096	80	2	3	7	•	•	•	1	1	1	160	•	16×16	•	•	8	12 ch ADC12	_	100PZ	6.
MSP430F4617	92	8192	80	2	3	7	•	•	•	1	1	1	160	•	16×16	•	•	8	12 ch ADC12	_	100PZ	6.
MSP430F4618	116	8192	80	2	3	7	•	•	•	1	1	1	160	•	16×16	•	•	8	12 ch ADC12	_	100PZ	7.
MSP430F4619	120	4096	80	2	3	7	•	•	•	1	1	1	160	•	16×16	•	•	8	12 ch ADC12	_	100PZ	6.
F47xx																						
MSP430F4783	48	2048	72	2	3	3	•	•	•	_	2	2	160	_	32×32 <sup>‡</sup>	•	•	16	(3) SD16_A	_	100PZ	4.
MSP430F4793	60	2560	72	2	3	3	•	•	•	_	2	2	160	_	$32 \times 32^{\ddagger}$	•	•	16	(3) SD16_A	_	100PZ	4.
MSP430F4784	48	2048	72	2	3	3	•	•	•	_	2	2	160	-	32×32 <sup>‡</sup>	•	•	16	(4) SD16_A	_	100PZ	4.
MSP430F4794 F471xx	60	2560	72	2	3	3	•	•	•	_	2	2	160	_	32×32 <sup>‡</sup>	•	•	16	(4) SD16_A	_	100PZ	4.
MSP430F47163	92	4096	68	2	3	3	•	•		_	2	2	160	•	32×32 <sup>‡</sup>	•	•	16	(3) SD16_A	RTC C	100PZ	5.
MSP430F47173	92	8192	68	2	3	3		•	•	_	2	2	160	_	32×32 <sup>‡</sup>			16	(3) SD16_A	RTC_C	100PZ	5.
MSP430F47183	116	8192	68	2	3	3	•	•	•	_	2	2	160	•	32×32 <sup>‡</sup>	•	•	16	(3) SD16_A	RTC_C	100PZ	5.
MSP430F47193	120	4096	68	2	3	3	•	•	•	_	2	2	160	•	32×32 <sup>‡</sup>	•	•	16	(3) SD16_A	RTC_C	100PZ	5.
MSP430F47126	56	4096	68	2	3	3	•	•	•	_	2	2	160	•	32×32 <sup>‡</sup>	•	•	16	(6) SD16_A	RTC_C	100PZ	5.
MSP430F47166	92	4096	68	2	3	3	•	•	•	_	2	2	160	•	$32 \times 32^{\ddagger}$	•	•	16	(6) SD16_A	RTC_C	100PZ	5.
MSP430F47176	92	8192	68	2	3	3	•	•	•	_	2	2	160	•	32×32 <sup>‡</sup>	•	•	16	(6) SD16_A	RTC_C	100PZ	5.
MSP430F47186	116	8192	68	2	3	3	•	•	•	_	2	2	160	•	32×32‡	•	•	16	(6) SD16_A	RTC_C	100PZ	7.
MSP430F47196	120	4096	68	2	3	3	•	•	•	_	2	2	160	•	32×32 <sup>‡</sup>	•	•	16	(6) SD16_A	RTC_C	100PZ	7.
MSP430F47127 MSP430F47167		4096 4096	68 68	2	3	3				_	2 2	2	160 160		32×32 <sup>‡</sup> 32×32 <sup>‡</sup>			16 16	(7) SD16_A (7) SD16_A	RTC_C RTC_C	100PZ 100PZ	5. 4.
MSP430F47167	92	8192	68	2	3	3				_	2	2	160		32×32 <sup>‡</sup>			16	(7) SD16_A (7) SD16_A	RTC_C	100PZ 100PZ	4.
MSP430F47187		8192	68	2	3	3				_	2	2	160		32×32 <sup>‡</sup>			16	(7) SD16_A	_	100PZ	6.
MSP430F47197			68	2		3	•	•	•	_	2	2	160	•	32×32 <sup>‡</sup>	•		16	(7) SD16_A		100PZ	6.
FG47x																			., _			
MSP430FG477	32	2048	48	2	3	3	•	•	•	_	1	1	128	-	-	•	•	8	(5) SD16_A	DAC12, (2) OPAMP	80PN; 113ZQW	4.
MSP430FG478	48	2048	48	2	3	3	•	•	•	-	1	1	128	-	-	•	•	8	(5) SD16_A	DAC12, (2) OPAMP	80PN; 113ZQW	5.
MSP430FG479	60	2048	48	2	3	3	•	•	•	_	1	1	128	-	-	•	•	8	(5) SD16_A	DAC12, (2) OPAMP	80PN; 113ZQW	6.
F47x																						
MSP430F477	32	2048	48	2	3	3	•	•	•	_	1	1	128	_	-	•	•	8	(5) SD16_A	DAC12	80PN; 113ZQW	4.
MSP430F478	48	2048	48	2	3	3	•	•	•	_	1	1	128	_	-	•	•	8	(5) SD16_A	DAC12	80PN; 113ZQW	5.
																					80PN;	

‡Supports 64-bit result length.

MSP430I	5xx	Serie	s –	Up	to 2	o Mi	lz 💮												
Part Number	Program (KB)	SRAM (KB)	I/0 (max)	Total	Timers	B*	Watchdog Timer	PMM: BOR, SVS, SVM, LDO	USCI Ch A: UART/ Lin/irda/SPI	Ch B: I <sup>2</sup> C/SPI	DMA	MPY	Comp B	Temp Sensor	ADC Ch/Res	DAC	Additional Features	Package(s)	1 Pi (U
51xx	(KD)	(KD)	(IIIax)	IULAI	I A	ь	IIIIIGI	LUU	LIN/II DA/SFI	1 0/3/1	DIVIA	IVIT	ь	3611301	UII/IIG3	DAG	I Galui GS	r ackaye(s)	η(υ
		_	29	0	0	0.0	_	_			0.1	00 00+	40.1				IIID DIAMA EVIVO	40RSB (QNF),	
/ISP430F5131	8	1	27	3	3	3, 3	•	•	1	1	3 cn	32×32 <sup>‡</sup>	16 CN	_	_	-	HiRes PWM, 5V I/Os	38DA (TSSOP)	
MSP430F5132	8	1	29 27	3	3	3, 3	•	•	1	1	3 ch	32×32 <sup>‡</sup>	16 ch	•	8 ch ADC10_A	_	HiRes PWM, 5V I/Os	40RSB (QNF), 38DA (TSSOP)	
/ISP430F5151	16	2	29 27	3	3	3, 3	•	•	1	1	3 ch	32×32 <sup>‡</sup>	16 ch	_	_	-	HiRes PWM, 5V I/Os	40RSB (QNF), 38DA (TSSOP)	
MSP430F5152	16	2	29 27	3	3	3, 3	•	•	1	1	3 ch	32×32 <sup>‡</sup>	16 ch	•	8 ch ADC10_A	_	HiRes PWM, 5V I/Os	40RSB (QNF), 38DA (TSSOP)	
/ISP430F5171	32	2	29 27	3	3	3, 3	•	•	1	1	3 ch	32×32 <sup>‡</sup>	16 ch	_	_	_	HiRes PWM, 5V I/Os	40RSB (QNF), 38DA (TSSOP)	
MSP430F5172	32	2	29 27	3	3	3, 3	•	•	1	1	3 ch	32×32 <sup>‡</sup>	16 ch	•	8 ch ADC10_A	_	HiRes PWM, 5V I/Os	40RSB (QNF), 38DA (TSSOP)	
52xx																			
WSP430F5212	64	8	37	4	5, 3, 3	7	•	•	2	2	3 ch	32×32 <sup>‡</sup>	•	_	_	_	1.8V I/O	48RGZ	2
/ISP430F5213	96	8	37	4	5, 3, 3	7	•	•	2	2	3 ch	32×32 <sup>‡</sup>	•	_	_	_	1.8V I/O	48RGZ	
ISP430F5214	128	8	37	4	5, 3, 3	7	•	•	2	2		32×32‡		_	_	_	1.8V I/O	48RGZ	
NSP430F5217	64	8	53	4	5, 3, 3	7	•	•	2	2		32×32‡	•	_	_	_	1.8V I/O	64YFF, 64RGC, 80ZQE	
/ISP430F5219	128	8	53	4	5, 3, 3	7	•	•	2	2	3 ch	32×32‡	•	_	_	_	1.8V I/O	64YFF, 64RGC, 80ZQE	
/ISP430F5222	64	8	37	4	5, 3, 3	7	•	•	2	2	3 ch	32×32 <sup>‡</sup>	•	•	8 ch ADC10	_	1.8V I/O	48RGZ	
NSP430F5223	96	8	37	4	5, 3, 3	7	•	•	2	2	3 ch	32×32 <sup>‡</sup>	•	•	10 ch ADC10	_	1.8V I/O	48RGZ	
/ISP430F5224	128	8	37	4	5, 3, 3	7	•	•	2	2		32×32 <sup>‡</sup>			8 ch ADC10	_	1.8V I/O	48RGZ	
NSP430F5227	64	8	53	4	5, 3, 3	7	•	•	2	2		32×32‡	•	•	10 ch ADC10	_	1.8V I/O	64YFF, 64RGC, 80ZQE	
/ISP430F5229	128	8	53	4	5, 3, 3	7	•	•	2	2	3 ch	32×32‡	•	•	10 ch ADC10	_	1.8V I/O	64YFF, 64RGC, 80ZQE	
-53x							_	_	_									00242	ш
/ISP430F5304	8	6	31	4	5, 3, 3	7	•	•	1	1	3 ch	32×32 <sup>‡</sup>	_	•	8 ch (6/2) ADC10	_	_	48RGZ , 48PT	T
NSP430F5308	16	6	47	4	5, 3, 3	7	•	•	2	2	3 ch	32×32 <sup>‡</sup>	•	•	12 ch (10/2) ADC10	_	_	48RGZ, 48PT, 64RGC, 80ZQE	
/ISP430F5309	24	6	47	4	5, 3, 3	7	•	•	2	2	3 ch	32×32‡	•	•	12 ch (10/2) ADC10	-	_	48RGZ, 48PT, 64RGC, 80ZQE	
/ISP430F5310	32	6	47	4	5, 3, 3	7	•	•	2	2	3 ch	32×32 <sup>‡</sup>	•	•	12 ch (10/2) ADC10	-	_	48RGZ, 48PT, 64RGC, 80ZQE	
NSP430F5324	64	6	48	4	5, 3, 3	7	•	•	2	2		32×32 <sup>‡</sup>	•	•	16 ch ADC12 A	_	_	64RGC, 80ZQE	
MSP430F5325	64	6	63	4	5, 3, 3	7	•	•	2	2		32×32‡	•	•	16 ch ADC12 A	_	_	80PN	1
MSP430F5326	96	8	48	4	5, 3, 3	7	•	•	2	2	3 ch	32×32‡	•	•	16 ch ADC12 A	_	_	64RGC, 80ZQE	
MSP430F5327	96	8	63	4	5, 3, 3	7	•	•	2	2		32×32 <sup>‡</sup>	•		16 ch ADC12 A	_	_	80PN	-
MSP430F5328	128	10	48	4	5, 3, 3	7		•	2	2		32×32 <sup>‡</sup>	•		16 ch ADC12 A	_	_	64RGC, 80ZQE	
MSP430F5329 MSP430F5333	128 128	10 10	63 74	4	5, 3, 3 5, 3, 3		•		2	2		32×32 <sup>‡</sup> 32×32 <sup>‡</sup>	•		16 ch ADC12 A 16 ch ADC12 A	_	RTC_B	80PN 100PZ, 113ZQW	1
MSP430F5335	256	18	74	4	5, 3, 3	7			2	2		32×32 <sup>‡</sup>			16 ch ADC12 A	_	RTC_B	100PZ, 113ZQW	
MSP430F5336	128	18	74	4	5, 3, 3	7			2	2		32×32 <sup>‡</sup>		•	16 ch ADC12 A	•	RTC_B	100PZ, 113ZQW	
MSP430F5338	256	18	74	4	5, 3, 3				2	2		32×32 <sup>‡</sup>			16 ch ADC12 A	•	RTC_B	100PZ, 113ZQW	
MSP430F5340	64	6	38	4	5, 3, 3	7	•	•	2	2		32×32 <sup>‡</sup>	•	•	9 ch ADC12 A	_	_	48RGZ	
MSP430F5341	96	8	38	4	5, 3, 3	7			2	2		32×32 <sup>‡</sup>			9 ch ADC12 A	_	_	48RGZ	
NSP430F5342	128	10	38	4	5, 3, 3	7	•	•	2	2		32×32 <sup>‡</sup>	•	•	9 ch ADC12 A	_	_	48RGZ	
NSP430F5358	384	32 + 2 <sup>†</sup>	74	4	5, 3, 3	7	•	•	3	3		32×32 <sup>‡</sup>	•	•	16 ch ADC12	•	_	100PZ, 113ZQW	
/ISP430F5359	512	64 + 2 <sup>†</sup>		4	5, 3, 3		•	•	3	3		32×32 <sup>‡</sup>		•	16 ch ADC12	•	_	100PZ, 113ZQW	
54x	,		0-												40 1 4				
MSP430F5418A	128	16	87	3	5, 3	7	•	•	2	2		32×32‡		•	16 ch ADC12 A	-	_	80PN	
MSP430F5419A	128	16	87	3	5, 3	7	•	•	4	4		32×32 <sup>‡</sup>		•	16 ch ADC12 A	_	_	100PZ, 113ZQW	
MSP430F5435A	192	16	87	3	5, 3	7	•	•	2	2		32×32 <sup>‡</sup>		•	16 ch ADC12 A	-	_	80PN	, ;
	192	16	87	3	5, 3	7	•		4	4	3 ch	32×32 <sup>‡</sup>	_		16 ch ADC12 A	_	_	100PZ, 113ZQW	1
MSP430F5436A MSP430F5437A	256	16	87	3	5, 3	7	•	•	2	2	0 .	32×32 <sup>‡</sup>	_		16 ch ADC12 A	_	_	80PN	-

<sup>&</sup>lt;sup>1</sup>Prices are quoted in U.S. dollars and represent year 2013 suggested resale price.

\*Represents number of capture/compare registers per timer.

<sup>1</sup>Additional 2K of SRAM available if USB is disabled.

\*Supports 64 bit result length.

New products are listed in **bold red**.

					Timers			PMM: BOR,	USCI										l
Part Number	Program (KB)	SRAM (KB)	I/0	Total	A*	B*	Watchdog Timer		Ch A: UART/ LIN/IrDA/SPI	Ch B: I <sup>2</sup> C/SPI	DMA	MPY	Comp B	Temp Sensor	ADC Ch/Res	DAC	Additional Features	Package(s)	P (U
55xx	(ND)	(VD)	(IIIax)	IULAI	A"	D"	Hiller	LDU	LIN/II DA/SFI	1-0/371	DIVIA	WIFT	D	Selisui	UII/NES	DAG	realures	Package(s)	Įψ
	8	4 + 2 <sup>†</sup>	31	4	5, 3, 3	7	•	•	1	1	3 ch	32×32 <sup>‡</sup>	4			_	USB	48RGZ	ŀ
SP430F5500 SP430F5501	16	4 + 2 <sup>†</sup>	31	4	5, 3, 3	7			1	1	3 ch	32×32 <sup>‡</sup>	4		_	_	USB	48RGZ	
SP430F5502	24	4 + 2 <sup>†</sup>	31	4	5, 3, 3	7			1	1	3 ch	32×32 <sup>‡</sup>	4		_		USB	48RGZ	
SP430F5503	32	4 + 2 <sup>†</sup>	31	4	5, 3, 3	7			1	1	3 ch	32×32 <sup>‡</sup>	4	_			USB	48RGZ	
SP430F5504	8	4 + 2 <sup>†</sup>	31	4	5, 3, 3	7			1	1	3 ch	32×32‡	_		8 ch (6/2) ADC10		USB	48RGZ, 48PT	
SP430F5505	16	4 + 2 <sup>†</sup>	31	4	5. 3. 3	7			1	1	3 ch	32×32‡	_		8 ch (6/2) ADC10	_	USB	48RGZ	
ISP430F5506	24	4 + 2 <sup>†</sup>	31	4	5. 3. 3	7			1	1	3 ch	32×32 <sup>‡</sup>	_		8 ch (6/2) ADC10	_	USB	48RGZ	
SP430F5507	32	4 + 2 <sup>†</sup>	31	4	5, 3, 3	7			1	1	3 ch	32×32 <sup>‡</sup>	_		8 ch (6/2) ADC10	_	USB	48RGZ	
ISP430F5508	16	4 + 2†	47	4	5, 3, 3	7	•	•	2	2	3 ch	32×32 <sup>‡</sup>	8	•	12 ch (10/2) ADC10		USB	48RGZ, 48PT, 64RGC, 80ZQE	
SP430F5509	24	$4 + 2^{\dagger}$	47	4	5, 3, 3	7	•	•	2	2	3 ch	32×32 <sup>‡</sup>	8	•	12 ch (10/2) ADC10	_	USB	48RGZ, 48PT, 64RGC, 80ZQE	
SP430F5510	32	$4 + 2^{\dagger}$	47	4	5, 3, 3	7	•	•	2	2	3 ch	32×32‡	8	•	12 ch (10/2) ADC10	_	USB	48RGZ, 48PT, 64RGC, 80ZQE	
SP430F5513	32	$4 + 2^{\dagger}$	47	4	5, 3, 3	7	•	•	2	2	3 ch	32×32 <sup>‡</sup>	•	_	_	_	USB	64RGC, 80ZQE	
SP430F5514	64	$4 + 2^{\dagger}$	47	4	5, 3, 3	7	•	•	2	2	3 ch	32×32 <sup>‡</sup>	•	_	_	_	USB	64RGC, 80ZQE	
SP430F5515	64	$4 + 2^{\dagger}$	63	4	5, 3, 3	7	•	•	2	2	3 ch	32×32 <sup>‡</sup>	•	_	_	_	USB	80PN	
SP430F5517	96	$6 + 2^{\dagger}$	63	4	5, 3, 3	7	•	•	2	2	3 ch	32×32 <sup>‡</sup>	•	_	_	-	USB	80PN	
SP430F5519	128	$8 + 2^{\dagger}$	63	4	5, 3, 3	7	•	•	2	2	3 ch	32×32 <sup>‡</sup>	•	_	_	_	USB	80PN	
SP430F5521	32	6 + 2 <sup>†</sup>	63	4	5, 3, 3	7	•	•	2	2	3 ch	32×32‡	•	•	16 ch ADC12 A	-	USB	80PN	
SP430F5522	32	8 + 2†	47	4	5, 3, 3	7	•	•	2	2	3 ch	32×32‡	•	•	12 ch ADC12 A	_	USB	64RGC, 80ZQE	
ISP430F5524	64	4 + 2†	47	4	5, 3, 3	7	•	•	2	2	3 ch	32×32 <sup>‡</sup>	•	•	12 ch ADC12 A	_	USB	64RGC, 80ZQE	
SP430F5525	64	4 + 2†	63	4	5, 3, 3	7	•	•	2	2	3 ch	32×32 <sup>‡</sup>	•	•	16 ch ADC12 A	_	USB	80PN	
SP430F5526	96	$6 + 2^{\dagger}$	47	4	5, 3, 3	7	•	•	2	2	3 ch	32×32 <sup>‡</sup>	•	•	12 ch ADC12 A	_	USB	64RGC, 80ZQE	
SP430F5527	96	6 + 2†	63	4	5, 3, 3	7	•	•	2	2	3 ch	32×32‡	•	•	16 ch ADC12 A	_	USB	80PN	
SP430F5528	128	8 + 2 <sup>†</sup>	47	4	5, 3, 3	7	•	•	2	2	3 ch	32×32‡	•	•	16 ch ADC12 A	_	USB	64RGC, 80ZQE	
SP430F5529	128	8 + 2 <sup>†</sup>	63	4	5, 3, 3	7	•	•	2	2	3 ch	32×32 <sup>‡</sup>	•	•	16 ch ADC12 A	_	USB	80 PN	Ц
563x ISP430F5630	128	16 + 2 <sup>†</sup>	74	4	5, 3, 3	7	•	•	2	2	6 ch	32×32‡	•	_	_	_	USB, Backup battery switch	100PZ, 113ZQW	
SP430F5631	192	16 + 2 <sup>†</sup>	74	4	5, 3, 3	7	•	•	2	2	6 ch	32×32‡	•	_	_	_	USB, Backup battery switch	100PZ, 113ZQW	,
SP430F5632	256	16 + 2 <sup>†</sup>	74	4	5, 3, 3	7	•	•	2	2	6 ch	32×32‡	•	_	_	-	USB, Backup battery switch	100PZ, 113ZQW	
SP430F5633	128	16 + 2 <sup>†</sup>	74	4	5, 3, 3	7	•	•	2	2	6 ch	32×32 <sup>‡</sup>	•	•	16 ch ADC12 A	-	USB, Backup battery switch	100PZ, 113ZQW	
SP430F5634	192	16 + 2 <sup>†</sup>	74	4	5, 3, 3	7	•	•	2	2	6 ch	32×32 <sup>‡</sup>	•	•	16 ch ADC12 A	-	USB, Backup battery switch	100PZ, 113ZQW	
SP430F5635	256	16 + 2 <sup>†</sup>	74	4	5, 3, 3	7	•	•	2	2	6 ch	32×32‡	•	•	16 ch ADC12 A	-	USB, Backup battery switch	100PZ, 113ZQW	,
SP430F5636	128	16 + 2 <sup>†</sup>	74	4	5, 3, 3	7	•	•	2	2	6 ch	32×32‡	•	•	16 ch ADC12 A	•	USB, Backup battery switch	100PZ, 113ZQW	
SP430F5637	192	16 + 2 <sup>†</sup>	74	4	5, 3, 3	7	•	•	2	2	6 ch	32×32 <sup>‡</sup>	•	•	16 ch ADC12 A	•	USB, Backup battery switch	100PZ, 113ZQW	
SP430F5638 565x	256	16 + 2 <sup>†</sup>	74	4	5, 3, 3	7	•	•	2	2	6 ch	32×32‡	•	•	16 ch ADC12 A	•	USB, Backup battery switch	100PZ, 113ZQW	

<sup>&</sup>lt;sup>1</sup>Prices are quoted in U.S. dollars and represent year 2013 suggested resale price. 
<sup>\*</sup>Represents number of capture/compare registers per timer. 
<sup>†</sup>Additional 2K of SRAM available if USB is disabled. 
<sup>‡</sup>Supports 64-bit result length. 
New products are listed in **bold red**.

	I UA	Sell	es -	- սի	י נט	الک	IVINZ VV	ith LCD											
				16-	Bit Tim	ners		PMM:	USCI										1 k
Part Number	Flash (KB)	SRAM (KB)	I/O (max)	Total	A*	B*	Watchdog and RTC	BOR, SVS, SVM, LDO	Ch A: UART/ LIN/IrDA/SPI	Ch B: I <sup>2</sup> C/SPI	DMA	MPY	Comp B	Temp Sensor	ADC Ch/Res	DAC	Additional Features	Packages	Pric (U.S.
F663x	(ILD)	(110)	(IIIux)	Iotai			unu mro	OTIN, LDO	LIN/II DA/OI I	1 0/011	DINA		_	0011301	011/1103	DAU	Touturos	1 dokagos	(0.0.
MSP430F6630	128	16 + 2 <sup>†</sup>	74	4	5, 3, 3	3 7	•	•	2	2	6 ch	32×32 <sup>‡</sup>	•	_	_	_	USB, LCD	100PZ, 113ZQW	4.6
MSP430F6631	192	16 + 2 <sup>†</sup>	74	4	5, 3, 3				2	2	6 ch	32×32 <sup>‡</sup>		_	_	_	USB, LCD	100PZ, 113ZQW	4.8
MSP430F6632	256	16 + 2 <sup>†</sup>	74	4	5, 3, 3		•	•	2	2	6 ch	32×32 <sup>‡</sup>	•	_	_	_	USB, LCD	100PZ, 113ZQW	5.1
MSP430F6633	128	$16 + 2^{\dagger}$	74	4	5, 3, 3	3 7	•	•	2	2	6 ch	32×32 <sup>‡</sup>	•	•	16 ch ADC12 A	_	USB, LCD	100PZ, 113ZQW	4.6
MSP430F6634	192	$16 + 2^{\dagger}$	74	4	5, 3, 3	3 7	•	•	2	2	6 ch	32×32‡	•	•	16 ch ADC12 A	_	USB, LCD	100PZ, 113ZQW	4.
/ISP430F6635	256	$16 + 2^{\dagger}$	74	4	5, 3, 3	3 7	•	•	2	2	6 ch	32×32 <sup>‡</sup>	•	•	16 ch ADC12 A	_	USB, LCD	100PZ, 113ZQW	5.
/ISP430F6636	128	16 + 2 <sup>†</sup>	74	4	5, 3, 3		•	•	2	2	6 ch	32×32 <sup>‡</sup>	•	•	16 ch ADC12 A	•	USB, LCD	100PZ, 113ZQW	4.
/ISP430F6637	192	16 + 2 <sup>†</sup>	74	4	5, 3, 3		•	•	2	2	6 ch	32×32‡	•	•	16 ch ADC12 A	•	USB, LCD	100PZ, 113ZQW	5.
/ISP430F6638	256	16 + 2 <sup>†</sup>	74	4	5, 3, 3	3 7	•	•	2	2	6 ch	32×32 <sup>‡</sup>	•	•	16 ch ADC12 A	•	USB, LCD	100PZ, 113ZQW	5.4
F643x	400	10													40 1 100401		1.00	40007 4407011	
ISP430F6433	128	18	74	4	5, 3, 3		•	•	2	2	6 ch	32×32‡	•	•	16 ch ADC12 A	_	LCD	100PZ, 113ZQW	3.
MSP430F6435	256	18	74	4	5, 3, 3		•	•	2	2	6 ch	32×32 <sup>‡</sup>	•	•	16 ch ADC12 A	_	LCD	100PZ, 113ZQW	4.
NSP430F6436 NSP430F6438	128 256	18	74	4	5, 3, 3		•		2	2	6 ch	32×32 <sup>‡</sup> 32×32 <sup>‡</sup>	•	•	16 ch ADC12 A	•	LCD LCD	100PZ, 113ZQW	4.
	200	18	74	4	5, 3, 3	o /	•	•	2	2	o CII	32×32+	•	•	16 ch ADC12 A	•	LUD	100PZ, 113ZQW	4.
645x	384	34	74	4	500	7			3	3	6 ch	32×32 <sup>‡</sup>			16 ch ADC12		I CD	100D7 11270M	7.
ISP430F6458 ISP430F6459	512	66	74	4	5, 3, 3 5, 3, 3		•		3	3	6 ch	32×32 <sup>‡</sup>	•	•	16 ch ADC12	•	LCD LCD	100PZ, 113ZQW 100PZ, 113ZQW	7.
665x	312	00	74	4	J, J, C	<i>J</i> 1	•	_	J	J	0 CII	32×32	•	•	TO GITADOTZ	•	LUD	100FZ, 113ZQW	1.
	204	00 . Ot	7.1	4	E 2 1	0 7			2	2	G ob	20. 20t			16 oh ADC10		HCD LCD	100D7 11070W	7
ISP430F6658	384	32 + 2†	74	4	5, 3, 3		•	•	3	3	6 ch	32×32 <sup>‡</sup>	•	•	16 ch ADC12	•	USB, LCD	100PZ, 113ZQW	7.
ISP430F6659	512	64 + 2†	74	4	5, 3, 3	3 /	•	•	3	3	6 ch	32×32 <sup>‡</sup>	•	•	16 ch ADC12	•	USB, LCD	100PZ, 113ZQW	7.
-67xx																			
ISP430F6720	16	1	72	4	5, 3, 3		•	•	3	1	6 ch	32×32 <sup>‡</sup>	_	•	8 ch ADC10	_	LCD	100LQFP, 80LQFP	2
ISP430F6721	32	2	72	4	5, 3, 3		•	•	3	1	6 ch	32×32 <sup>‡</sup>	_	•	8 ch ADC10	_	LCD	100LQFP, 80LQFP	2.
ISP430F6723 ISP430F6724	64 96	4	72 72	4	5, 3, 3 5, 3, 3		•		3	1	6 ch	32×32 <sup>‡</sup> 32×32 <sup>‡</sup>	_	•	8 ch ADC10 8 ch ADC10	_	LCD LCD	100LQFP, 80LQFP 100LQFP, 80LQFP	2
ISP430F6725	128	4	72	4	5, 3, 3				3	1	6 ch	32×32 <sup>‡</sup>	_		8 ch ADC10	_	LCD	100LQFP, 80LQFP	2.
ISP430F6726	128	8	72	4	5, 3, 3				3	1	6 ch	32×32 <sup>‡</sup>	_		8 ch ADC10	_	LCD	100LQFP, 80LQFP	2
SP430F6730	16	1	72	4	5, 3, 3				3	1	6 ch	32×32 <sup>‡</sup>	_		8 ch ADC10	_	LCD	100LQFP, 80LQFP	2
SP430F6731	32	2	72	4	5, 3, 3				3	1	6 ch	32×32 <sup>‡</sup>	_		8 ch ADC10	_	LCD	100LQFP, 80LQFP	2
SP430F6733	64	4	72	4	5, 3, 3		•	•	3	1	6 ch	32×32‡	_	•	8 ch ADC10	_	LCD	100LQFP, 80LQFP	2
SP430F6734	96	4	72	4	5, 3, 3		•	•	3	1	6 ch	32×32‡	_	•	8 ch ADC10	_	LCD	100LQFP, 80LQFP	3.
SP430F6735	128	4	72	4	5, 3, 3		•	•	3	1	6 ch	32×32‡	_	•	8 ch ADC10	_	LCD	100LQFP, 80LQFP	3
ISP430F6736	128	8	72	4	5, 3, 3				3	1	6 ch	32×32 <sup>‡</sup>	_		8 ch ADC10	_	LCD	100LQFP, 80LQFP	3.
ISP430F6745	128	32	90	4	5, 3, 3		•	•	4	2	6 ch	32×32‡	•	•	8 ch ADC10/ 4 ch SD24	_	LCD, AES	100LQFP, 128LQFP	4.
ISP430F67451	128	16	90	4	5, 3, 3	3 7	•	•	4	2	6 ch	32×32‡	•	•	8 ch ADC10/ 4 ch SD24	_	LCD	100LQFP, 128LQFP	3.
ISP430F6746	256	16	90	4	5, 3, 3	3 7	•	•	4	2	6 ch	32×32‡	•	•	8 ch ADC10/ 4 ch SD24	-	LCD, AES	100LQFP, 128LQFP	4.
SP430F67461	256	16	90	4	5, 3, 3	3 7	•	•	4	2	6 ch	32×32‡	•	•	8 ch ADC10/ 4 ch SD24	-	LCD	100LQFP, 128LQFP	4.
SP430F6747	256	32	90	4	5, 3, 3	3 7	•	•	4	2	6 ch	32×32‡	•	•	8 ch ADC10/ 4 ch SD24	-	LCD, AES	100LQFP, 128LQFP	5.
ISP430F67471		32	90	4	5, 3, 3		•	•	4	2		32×32 <sup>‡</sup>	•	•	8 ch ADC10/ 4 ch SD24 8 ch ADC10/	-	LCD	100LQFP, 128LQFP	4.
SP430F6748	512	16	90	4	5, 3, 3		•	•	4	2		32×32 <sup>‡</sup>	•	•	4 ch SD24 8 ch ADC10/	-	LCD, AES	100LQFP, 128LQFP	6.
SP430F67481		16	90	4	5, 3, 3		•	•	4	2		32×32‡		•	4 ch SD24 8 ch ADC10/		LCD AES	100LQFP, 128LQFP	5.
SP430F6749	512	32	90	4	5, 3, 3			•	4	2	6 ch	32×32 <sup>‡</sup>	•		4 ch SD24 8 ch ADC10/	_	LCD, AES	100LQFP, 128LQFP	6
SP430F67491	512	32	90	4	5, 3, 3	3 7	•	•	4	2	6 ch	32×32 <sup>‡</sup>	•	•	4 ch SD24		LCD	100LQFP, 128LQFP	6

<sup>1</sup>Prices are quoted in U.S. dollars and represent year 2013 suggested resale price. \*Represents number of capture/compare registers per timer. †Additional 2K of SRAM available if USB is disabled. †Supports 64-bit result length. New products are listed in **bold red**.

MSP430	F6xx	Seri	es –	Up	to	25	MHz w	ith LCD	(contin	ued)									
				16-	Bit Tin	ners		PMM:	USCI										1 ku
Part Number	Flash (KB)	SRAM (KB)	I/O (max)	Total	A*	B*	Watchdog and RTC	BOR, SVS, SVM, LDO	Ch A: UART/ Lin/irda/spi	Ch B: I <sup>2</sup> C/SPI	DMA	MPY	Comp B	Temp Sensor	ADC Ch/Res	DAC	Additional Features	Packages	Price <sup>1</sup> (U.S. \$)
F67xx (continu	ied)																		
MSP430F6765	128	16	90	4	5, 3,	3 7	•	•	4	2	6 ch	32×32‡	•	•	8 ch ADC10/ 6 ch SD24	_	LCD, AES	100LQFP, 128LQFP	4.36
MSP430F67651	128	16	90	4	5, 3,	3 7	•	•	4	2	6 ch	32×32‡	•	•	8 ch ADC10/ 6 ch SD24	_	LCD	100LQFP, 128LQFP	4.09
MSP430F6766	256	16	90	4	5, 3,	3 7	•	•	4	2	6 ch	32×32 <sup>‡</sup>	•	•	8 ch ADC10/ 6 ch SD24	_	LCD, AES	100LQFP, 128LQFP	5.17
MSP430F67661	256	16	90	4	5, 3,	3 7	•	•	4	2	6 ch	32×32 <sup>‡</sup>	•	•	8 ch ADC10/ 6 ch SD24	_	LCD	100LQFP, 128LQFP	4.90
MSP430F6767	256	32	90	4	5, 3,	3 7	•	•	4	2	6 ch	32×32 <sup>‡</sup>	•	•	8 ch ADC10/ 6 ch SD24	_	LCD	100LQFP, 128LQFP	5.29
MSP430F67671	256	32	90	4	5, 3,	3 7	•	•	4	2	6 ch	32×32 <sup>‡</sup>	•	•	8 ch ADC10/ 6 ch SD24	_	LCD	100LQFP, 128LQFP	5.02
MSP430F6768	512	16	90	4	5, 3,	3 7	•	•	4	2	6 ch	32×32‡	•	•	8 ch ADC10/ 6 ch SD24	_	LCD	100LQFP, 128LQFP	6.41
MSP430F67681	512	16	90	4	5, 3,	3 7	•	•	4	2	6 ch	32×32 <sup>‡</sup>	•	•	8 ch ADC10/ 6 ch SD24	-	LCD	100LQFP, 128LQFP	6.14
MSP430F6769	512	32	90	4	5, 3,	3 7	•	•	4	2	6 ch	32×32 <sup>‡</sup>	•	•	8 ch ADC10/ 6 ch SD24	_	LCD	100LQFP, 128LQFP	6.52
MSP430F67691	512	32	90	4	5, 3,	3 7	•	•	4	2	6 ch	32×32‡	•	•	8 ch ADC10/ 6 ch SD24	_	LCD	100LQFP, 128LQFP	6.25
MSP430F6775	128	16	90	4	5, 3,	3 7	•	•	4	2	6 ch	32×32‡	•	•	8 ch ADC10/ 7 ch SD24	_	LCD	100LQFP, 128LQFP	4.59
MSP430F67751	128	16	90	4	5, 3,	3 7	•	•	4	2	6 ch	32×32‡	•	•	8 ch ADC10/ 7 ch SD24	_	LCD	100LQFP, 128LQFP	4.32
MSP430F6776	256	16	90	4	5, 3,	3 7	•	•	4	2	6 ch	32×32‡	•	•	8 ch ADC10/ 7 ch SD24	_	LCD	100LQFP, 128LQFP	5.40
MSP430F67761	256	16	90	4	5, 3,	3 7	•	•	4	2	6 ch	32×32‡	•	•	8 ch ADC10/ 7 ch SD24	_	LCD	100LQFP, 128LQFP	5.13
MSP430F6777	256	32	90	4	5, 3,	3 7	•	•	4	2	6 ch	32×32‡	•	•	8 ch ADC10/ 7 ch SD24	_	LCD	100LQFP, 128LQFP	5.52
MSP430F67771	256	32	90	4	5, 3,	3 7	•	•	4	2	6 ch	32×32‡	•	•	8 ch ADC10/ 7 ch SD24	_	LCD	100LQFP, 128LQFP	5.25
MSP430F6778	512	16	90	4	5, 3,	3 7	•	•	4	2	6 ch	32×32 <sup>‡</sup>	•	•	8 ch ADC10/ 7 ch SD24	-	LCD	100LQFP, 128LQFP	6.64
MSP430F67781	512	16	90	4	5, 3,	3 7	•	•	4	2	6 ch	32×32 <sup>‡</sup>	•	•	8 ch ADC10/ 7 ch SD24	_	LCD	100LQFP, 128LQFP	6.37
MSP430F6779	512	32	90	4	5, 3,	3 7	•	•	4	2	6 ch	32×32 <sup>‡</sup>	•	•	8 ch ADC10/ 7 ch SD24	-	LCD	100LQFP, 128LQFP	6.76
MSP430F67791	512	32	90	4	5, 3,	3 7	•	•	4	2	6 ch	32×32 <sup>‡</sup>	•	•	8 ch ADC10/ 7 ch SD24	_	LCD	128LQFP, 100LQFP	6.48

<sup>&</sup>lt;sup>1</sup>Prices are quoted in U.S. dollars and represent year 2013 suggested resale price. \*Represents number of capture/compare registers per timer. †Additional 2K of SRAM available if USB is disabled. †Supports 64-bit result length. New products are listed in **bold red**.

Differentiated Performance: If you need low-voltage operation, industrial temperature range, or embedded RF, then you have come to the right place!

	<b>Application-Sp</b>	ecific MS	P430	) Devi	ces							
1					16-B	it Tim	ers					1 ku
1	Application	Part Number	Flash (KB)	SRAM (KB)	Total	A.	B*	ADC	Additional Features	Related Devices	Package(s)	Price <sup>1</sup> (U.S. \$)
1	NFC	RF430CL330H			_	_	-	_	Dynamic NFC interface transponder, NFC tag type 4	_	14PW	0.85
	Low Voltage (0.9V)	MSP430L092	_	2	2	2	_	ADC8	DAC8, COMP, SVS, temp. sensor, 11 I/Os, ROM-version available	_	14PW	1.00
	Bluetooth®	MSP430BT5190	256	16	3	5, 3	7	16 ch ADC12_A	MindTree's EtherMind <i>Bluetooth</i> stack and serial port profile available for download. Not preloaded by default.	CC2560	100PZ, 113ZQW	4.95
	Contactless Power	MSP430BQ1010	_	_	_	-	-	_	Fixed-function, Qi-certified software for contactless power applications (receiver). Compliant with the Wireless Power Consortium. Comes pre-loaded by default.	BQ25046, BQ500110	_	1.80

<sup>&</sup>lt;sup>1</sup>Prices are quoted in U.S. dollars and represent year 2013 suggested resale price.

<sup>\*</sup>Represents number of capture/compare registers per timer.

Enhanced	Perforr	nance	е														
Part Number	Frquency (MHz)	Flash (KB)	SRAM (B)	GP10	16-bit Timers	Watchdog	Brown Out Reset	USI (I <sup>2</sup> C/SPI)	DMA	MPY	Comp	Temp Sensor	ADC	Temp Range (°C)	Additional Features	Pin/Package	1 ku Price <sup>1</sup> (U.S. \$)
Auto and EP																	
MSP430F2619S-HT	16	120	4096	48	2	•	•	2	_	_	•	•	8 ch ADC12	-55 to 150	DAC12	64PM	68.75/100 u
MSP430F2013-EP	16	2	128	10	1	•	•	1	_	_	•	•	4 ch SD16	-40 to 125	_	16RSA	1.75/100 u
MSP430F2274-EP	16	32	1024	32	2	•	•	2	_	_	•	•	12 ch ADC12	-55 to 125	(2) Op Amp	38DA, 40RHA	6.75
MSP430F249-EP	16	60	2048	48	2	•	•	2	_	16×16	•	•	8 ch ADC12	-55 to 125	_	64PM	11.88/100 u
MSP430F2618-EP	16	116	8192	48	2	•	•	2	•	16×16	•	•	8 ch ADC12	-40 to 105	DAC12	113ZQW	26.00/100 u
MSP430G2230-EP	16	2	128	4	1	•	•	1	_	_	_	•	4 ch SAR10	-40 to 125	_	8SOIC	0.52
MSP430G2231-EP	16	2	128	10	1	•	•	1	_	_	_	•	8 ch SAR10	-40 to 125	_	14PW	1.05
MSP430G2302-EP	16	_	128	16	1	•	•	1	_	_	_	•	_	-40 to 85	_	14PW	0.85

<sup>&</sup>lt;sup>1</sup>Prices are quoted in U.S. dollars and represent year 2013 suggested resale price. Note: Additional 105°C devices available on page 14.

#### **CC430 Family**

#### Sub-1GHz RF + MSP430 MCU

The CC430 16-bit RF microcontroller family provides tight integration between the microcontroller core, peripherals,

software, and RF transceiver, creating true system-on-chip solutions that are easy to use. Features <1GHz RF transceiver, with 1.8V-3.6V operation.

				16-B	it Tin	ners			USC	1								1ku	
Part Number F51xx	Flash (KB)	SRAM (KB)	I/O (max)			B*	Watchdog and RTC	PMM: BOR, SVS, SVM, LDO		Ch B:	DMA	MPY	Comp B	Temp Sensor	ADC Ch/Res	Additional Features	Package(s)	Price <sup>1</sup> (U.S. \$)	Common Features
C430F5123	8	2	30	2	5	3		•	1	1	3 ch	32×32‡	_		6 ch	_	48RGZ	2.50	
C430F5125	16	2	30	2	5	3			1	1	3 ch	32×32 <sup>‡</sup>	_		6 ch	_	48RGZ	2.60	
C430F5133	8	2	30	2	5	3	•	•	1	1	3 ch	32×32‡	•	•	6 ch ADC12	_	48RGZ	4.15	
C430F5135	16	2	30	2	5	3	•	•	1	1	3 ch	32×32 <sup>‡</sup>	•	•	6 ch ADC12	_	48RGZ	4.30	
C430F5137	32	4	30	2	5	3	•	•	1	1	3 ch	32×32‡	•	•	6 ch ADC12	_	48RGZ	5.00	AES HW encryption
C430F5143	8	2	30	2	5	3	•	•	1	1	3 ch	32×32 <sup>‡</sup>	_	•	6 ch	_	48RGZ	2.60	Max RF data rate
C430F5145	16	2	30	2	5	3	•	•	1	1	3 ch	32×32‡	_	•	6 ch	_	48RGZ	2.70	500 kbps • Best sensitivity:
C430F5147	32	4	30	2	5	3	•	•	1	1	3 ch	32×32 <sup>‡</sup>	_	•	6 ch	_	48RGZ	2.80	110 dBm <sup>†</sup>
61xx																			Frequency ranges
C430F6125	16	2	44	2	5	3	•	•	1	1	3 ch	32×32‡	•	_	_	96 seg LCD	64RGC	4.35	300-348 MHz
430F6126	32	2	44	2	5	3	•	•	1	1	3 ch	32×32 <sup>‡</sup>	•	_	_	96 seg LCD	64RGC	4.60	389–464 MHz
430F6127	32	4	44	2	5	3	•	•	1	1	3 ch	32×32‡	•	_	_	96 seg LCD	64RGC	5.05	779–929 MHz
430F6135	16	2	44	2	5	3	•	•	1	1	3 ch	32×32‡	•	•	8 ch ADC12	96 seg LCD	64RGC	4.65	
430F6137	32	4	44	2	5	3	•	•	1	1	3 ch	32×32‡	•		8 ch ADC12	96 seg LCD	64RGC	5.35	
430F6143	8	2	44	2	5	3	•	•	1	1	3 ch	32×32 <sup>‡</sup>	_	•	8 ch	_	64RGC	3.00	
430F6145	16	2	44	2	5	3	•	•	1	1	3 ch	32×32 <sup>‡</sup>	_	•	8 ch	_	64RGC	3.10	
C430F6147	32	4	44	2	5	3		•	1	1	3 ch	32×32 <sup>‡</sup>	_		8 ch	_	64RGC	3.20	

<sup>&</sup>lt;sup>1</sup>Prices are quoted in U.S. dollars and represent year 2013 suggested resale price.

<sup>\*</sup>Represents number of capture/compare register per timer. †At 1.2 kBaud, 868 MHz, 1% packet error rate.

<sup>‡</sup>Supports 64-bit result length.

# **Package Options**

#### Ultra-Low-Power MSP430 MCU Selected Package Options 14-pin PW (TSSOP) 14-pin N (DIP) 16-pin RSA (QFN) 20-pin DVG (TVSOP) 20-pin PW (TSSOP) 20-pin DW (SOIC) — (19,18) — → (12,80) → (5,00) ← → (5,00) ← → (6,50) ← (10,58) (4,00) Įį, (10,30) 24-pin RGE (QFN) 28-pin DW (SOIC) 28-pin PW (TSSOP) 32-pin RTV (QFN) 20-pin N (PDIP) 32-pin RHB (QFN) (26,92) −(9,70)<del>----</del> → (5,15) ← → (5,00) ← (6,40) **4**(5,15) (5,00) Įį. (10,50) Įį, (10,30) 40-pin RHA (QFN) 40-pin RSB (QFN) 48-pin RGZ (QFN) 48-pin DL (SSOP) 48-pin PT (LQFP) 64-pin RGC, RTD (QFN) 38-pin DA (TSSOP) (9,00)→ **←** (12,50) − (9,00) † (6,0) (7,00) (8,10) (10,35) (5,15) (9,00) 100-pin PZ (LQFP) 64-pin PM, PAG (LQFP/TQFP) 80-pin ZQE (BGA) 80-pin PN (LQFP) 113-pin ZQW (BGA) 128-pin PEU (LQFP) - (16,00) -\_\_\_(14,00)\_ ← (12,00) → **←**(7,00)→ **(5,00)** (5,00) (7,00) (16,20) ij (16,00) (12,00) (14,00)

#### **Die-Size BGA Packages**



More information on package dimensions available in the datasheets or at ti.com/packaging.

#### Microcontroller Part Number Decoder



Processor Family	CC = Embedded RF Radio MSP = Mixed Signal Processor XMS = Experimental Silicon	
430 MCU Platform	TI's Low Power Microcontroller Platfo	orm
Device Type	Memory Type C = ROM F = FLASH FR = FRAM G = Flash (Value Line) L = No nonvolatile memory	Specialized Application AFE = Analog front end BT = Pre-programmed with Bluetooth® BQ = Contactless power CG = ROM medical FE = Flash energy meter FG = Flash medical FW = Flash electronic flow meter
Series	1 Series = Up to 8 MHz 2 Series = Up to 16 MHz 3 Series = Legacy OTP 4 Series = Up to 16 MHz w/ LCD	5 Series = Up to 25 MHz 6 Series = Up to 25 MHz w/ LCD 0 = Low-voltage series
Feature Set	Various levels of integration within a	series
Optional: A = Revision	N/A	
Optional: Temperature Range	S = 0°C to 50°C C = 0°C to 70°C I = -40°C to 85°C T = -40°C to 105°C	
Packaging	ti.com/packaging	
Optional: Tape and Reel	T = Small reel (7 in) R = Large reel (11 in) No markings = Tube or tray	
Optional: Additional Features	EP = Enhanced product (-40°C to 10 HT = Extreme temperature parts (-55	



#### **Embedded Processing Portfolio**

#### **TI Embedded Processors**

#### Code Composer Studio™ IDE

#### **Microcontroller (MCU)**

#### **ARM®-Based Processors**

#### **Digital Signal Processor (DSP)**

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