# Optical Power Sensor Issue

A diagram of a circuit

Description automatically generated

1. At input J3 with a 12V pulse, only seeing 6V at testpoint OPT\_RAW. Verified on oscilloscope that signal, when unconnected is 12V.  
     
   Removed 2k potentiometer, and signal measured 12V  
     
   Put in a static voltage divider of 100k/390k and with a 12V signal was expecting 3V at non-inverting input to U8, but saw around 2V. When input was 4V, we saw 1V, but continuing to increase we did not see the output voltage follow as expected (ie, 1:4 ratio)