SDSoC™
Environment

Xilinx SDSoC Development Environment Technical Seminar

Time	Session
8:30 a.m.	Continental Breakfast
9:00 a.m.	Welcome & Introductions
9:15 a.m.	*Zynq® All Programmable SoC and MPSoC Architecture*
9:30 a.m.	*SDSoC Development Environment Overview*
10:30 a.m.	Networking Break
10:55 a.m.	*Real-World Application Examples*
11:10 a.m.	*Optimizing C/C++ Code for System Performance*
12:30 p.m.	Lunch
1:30 p.m.	*How to Target a Custom Production Platform*
2:10 p.m.	*Roadmap and Q&A*
3:10 p.m.	Next Steps and Closing Remarks
3:30 p.m.	Networking Q&A with the Experts

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