

Daniel Delgado Acosta
Andrew Goldbaum
Eric Duong

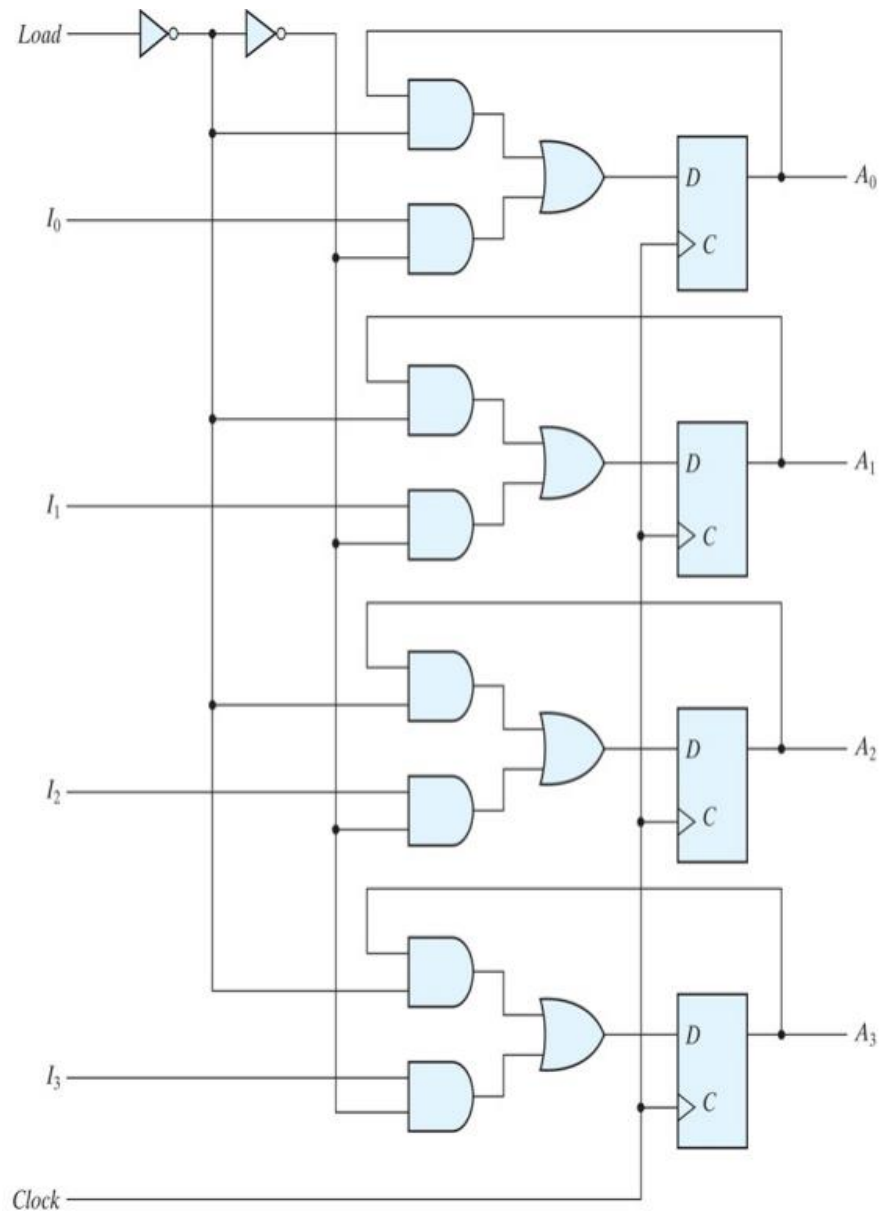
CSE 3100 Digital Logic
5/12/2022

Lab 10: Four-bit register with parallel load

Objective: To build a four-bit register with parallel load with D-type flip flops.

Provided: 74X374 (D flip-flops), 7408 (AND gates), 7404 (Inverters), 7432 (OR gates), breadboard.

Circuit Diagram:



Observation/Conclusion: After constructing the circuit on the breadboard we found that each bit A_0 , A_1 , A_2 , A_3 wouldn't be able to switch when the *Load* was 0 or "off." However, when the load was "on" or 1 each bit A_0 , A_1 , A_2 , A_3 would be able to switch.