

**OpenCL (Open Computing Language)** is a multi-vendor open standard for general-purpose parallel programming of heterogeneous systems that include CPUs, GPUs, and other processors. OpenCL provides a uniform programming environment for software developers to write efficient, portable code for high-performance compute servers, desktop computer systems, and handheld devices.

[n.n.n] refers to the section in the OpenCL Specification.

[n.n.n] refers to the section in the OpenCL Extension Specification

Text shown in purple is as per the OpenCL Extension Specification.

Specifications are available at [www.khronos.org/opencvl](http://www.khronos.org/opencvl).

## The OpenCL Platform Layer

The OpenCL platform layer implements platform-specific features that allow applications to query OpenCL devices, device configuration information, and to create OpenCL contexts using one or more devices.

### Querying Platform Info and Devices [4.1, 4.2]

```
cl_int clGetPlatformIDs (cl_uint num_entries,
                        cl_platform_id *platforms, cl_uint *num_platforms)
```

```
cl_int clGetPlatformInfo (cl_platform_id platform,
                          cl_platform_info param_name,
                          size_t param_value_size, void *param_value,
                          size_t *param_value_size_ret)
```

*param\_name:* CL\_PLATFORM\_PROFILE, CL\_PLATFORM\_VERSION, CL\_PLATFORM\_NAME, CL\_PLATFORM\_VENDOR, CL\_PLATFORM\_EXTENSIONS

```
cl_int clGetDeviceIDs (cl_platform_id platform,
                       cl_device_type device_type, cl_uint num_entries,
                       cl_device_id *devices, cl_uint *num_devices)
```

*device\_type:* CL\_DEVICE\_TYPE\_ACCELERATOR, CL\_DEVICE\_TYPE\_CPU, CL\_DEVICE\_TYPE\_CUSTOM, CL\_DEVICE\_TYPE\_DEFAULT, CL\_DEVICE\_TYPE\_GPU

```
cl_int clGetDeviceInfo (cl_device_id device,
                        cl_device_info param_name, size_t param_value_size,
                        void *param_value, size_t *param_value_size_ret)
```

*param\_name:*

- CL\_DEVICE\_NAME, CL\_DEVICE\_VENDOR, CL\_DEVICE\_PROFILE, CL\_DEVICE\_TYPE, CL\_DEVICE\_NATIVE\_VECTOR\_WIDTH\_CHAR, CL\_DEVICE\_NATIVE\_VECTOR\_WIDTH\_INT, CL\_DEVICE\_NATIVE\_VECTOR\_WIDTH\_LONG, CL\_DEVICE\_NATIVE\_VECTOR\_WIDTH\_SHORT, CL\_DEVICE\_NATIVE\_VECTOR\_WIDTH\_DOUBLE, CL\_DEVICE\_NATIVE\_VECTOR\_WIDTH\_HALF, CL\_DEVICE\_NATIVE\_VECTOR\_WIDTH\_FLOAT, CL\_DEVICE\_PREFERRED\_VECTOR\_WIDTH\_CHAR, CL\_DEVICE\_PREFERRED\_VECTOR\_WIDTH\_INT, CL\_DEVICE\_PREFERRED\_VECTOR\_WIDTH\_LONG, CL\_DEVICE\_PREFERRED\_VECTOR\_WIDTH\_SHORT, CL\_DEVICE\_PREFERRED\_VECTOR\_WIDTH\_DOUBLE, CL\_DEVICE\_PREFERRED\_VECTOR\_WIDTH\_HALF, CL\_DEVICE\_PREFERRED\_VECTOR\_WIDTH\_FLOAT, CL\_DEVICE\_PREFERRED\_INTEROP\_USER\_SYNC, CL\_DEVICE\_ADDRESS\_BITS, CL\_DEVICE\_AVAILABLE, CL\_DEVICE\_BUILT\_IN\_KERNELS, CL\_DEVICE\_COMPILER\_AVAILABLE, CL\_DEVICE\_DOUBLE\_SINGLE\_FP\_CONFIG, CL\_DEVICE\_ENDIAN\_LITTLE, CL\_DEVICE\_EXTENSIONS, CL\_DEVICE\_ERROR\_CORRECTION\_SUPPORT, CL\_DEVICE\_EXECUTION\_CAPABILITIES, CL\_DEVICE\_GLOBAL\_MEM\_CACHE\_SIZE, CL\_DEVICE\_GLOBAL\_MEM\_CACHELINE\_SIZE, CL\_DEVICE\_HOST\_UNIFIED\_MEMORY, CL\_DEVICE\_IMAGE\_MAX\_ARRAY\_BUFFER\_SIZE, CL\_DEVICE\_IMAGE\_SUPPORT, CL\_DEVICE\_IMAGE2D\_MAX\_WIDTH\_HEIGHT, CL\_DEVICE\_IMAGE3D\_MAX\_WIDTH\_HEIGHT\_DEPTH, CL\_DEVICE\_LOCAL\_MEM\_TYPE\_SIZE, CL\_DEVICE\_MAX\_READ\_WRITE\_IMAGE\_ARGS, CL\_DEVICE\_MAX\_CLOCK\_FREQUENCY, CL\_DEVICE\_MAX\_COMPUTE\_UNITS, CL\_DEVICE\_MAX\_CONSTANT\_ARGS\_BUFFER\_SIZE, CL\_DEVICE\_MAX\_MEM\_ALLOC\_PARAMETER\_SIZE, CL\_DEVICE\_MAX\_SAMPLERS, CL\_DEVICE\_MAX\_WORK\_GROUP\_SIZE, CL\_DEVICE\_MAX\_WORK\_ITEM\_DIMENSIONS\_SIZES, CL\_DEVICE\_MEM\_BASE\_ADDR\_ALIGN, CL\_DEVICE\_OPENCL\_C\_VERSION, CL\_DEVICE\_PARENT\_DEVICE, CL\_DEVICE\_PARTITION\_AFFINITY\_DOMAIN, CL\_DEVICE\_PARTITION\_MAX\_SUB\_DEVICES, CL\_DEVICE\_PARTITION\_PROPERTIES\_TYPE, CL\_DEVICE\_PLATFORM, CL\_DEVICE\_PRINTF\_BUFFER\_SIZE, CL\_DEVICE\_PROFILING\_TIMER\_RESOLUTION, CL\_DEVICE\_QUEUE\_PROPERTIES, CL\_DEVICE\_REFERENCE\_COUNT, CL\_DEVICE\_VENDOR\_ID, CL\_DEVICE\_DRIVER\_VERSION

### Partitioning a Device [4.3]

```
cl_int clCreateSubDevices (cl_device_id in_device,
                           const cl_device_partition_property *properties,
                           cl_uint num_devices, cl_device_id *out_devices,
                           cl_uint *num_devices_ret)
```

*properties:* CL\_DEVICE\_PARTITION\_EQUALLY, CL\_DEVICE\_PARTITION\_BY\_COUNTS, CL\_DEVICE\_PARTITION\_BY\_AFFINITY\_DOMAIN (Affinity domains may be: CL\_DEVICE\_AFFINITY\_DOMAIN\_NUMA, CL\_DEVICE\_AFFINITY\_DOMAIN\_L4\_L3\_L2\_L1\_CACHE, CL\_DEVICE\_AFFINITY\_DOMAIN\_NEXT\_PARTITIONABLE)

```
cl_int clRetainDevice (cl_device_id device)
```

## The OpenCL Runtime

### Command Queues [5.1]

```
cl_command_queue clCreateCommandQueue (
    cl_context context, cl_device_id device,
    cl_command_queue_properties properties,
    cl_int *errcode_ret)
```

*properties:* CL\_QUEUE\_PROFILING\_ENABLE, CL\_QUEUE\_OUT\_OF\_ORDER\_EXEC\_MODE\_ENABLE

```
cl_int clRetainCommandQueue (
    cl_command_queue command_queue)
```

```
cl_int clReleaseCommandQueue (
    cl_command_queue command_queue)
```

```
cl_int clGetCommandQueueInfo (
    cl_command_queue command_queue,
    cl_command_queue_info param_name,
    size_t param_value_size,
    void *param_value,
    size_t *param_value_size_ret)
```

*param\_name:* CL\_QUEUE\_CONTEXT, CL\_QUEUE\_DEVICE, CL\_QUEUE\_REFERENCE\_COUNT, CL\_QUEUE\_PROPERTIES

## Buffer Objects

Elements of a buffer object are stored sequentially and accessed using a pointer by a kernel executing on a device. Data is stored in the same format as it is accessed by the kernel.

### Create Buffer Objects [5.2.1]

```
cl_mem clCreateBuffer (cl_context context,
                       cl_mem_flags flags, size_t size, void *host_ptr,
                       cl_int *errcode_ret)
```

*flags:* CL\_MEM\_READ\_WRITE, CL\_MEM\_WRITE\_READ\_ONLY, CL\_MEM\_HOST\_NO\_ACCESS, CL\_MEM\_HOST\_READ\_WRITE\_ONLY, CL\_MEM\_USE\_ALLOC\_COPY\_HOST\_PTR

```
cl_mem clCreateSubBuffer (cl_mem buffer,
                           cl_mem_flags flags,
                           cl_buffer_create_type buffer_create_type,
                           const void *buffer_create_info, cl_int *errcode_ret)
```

*flags:* same as for clCreateBuffer

*buffer\_create\_type:* CL\_BUFFER\_CREATE\_TYPE\_REGION

### Read, Write, Copy Buffer Objects [5.2.2]

```
cl_int clEnqueueReadBuffer (
    cl_command_queue command_queue, cl_mem buffer,
    cl_bool blocking_read, size_t offset, size_t size,
    void *ptr, cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

```
cl_int clEnqueueReadBufferRect (
    cl_command_queue command_queue, cl_mem buffer,
    cl_bool blocking_read, const size_t *buffer_origin,
    const size_t *host_origin, const size_t *region,
    size_t buffer_row_pitch, size_t buffer_slice_pitch,
    size_t host_row_pitch, size_t host_slice_pitch,
    void *ptr, cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

```
cl_int clReleaseDevice (cl_device_id device)
```

### Contexts [4.4]

```
cl_context clCreateContext (
    const cl_context_properties *properties,
    cl_uint num_devices, const cl_device_id *devices,
    void (CL_CALLBACK *pfn_notify)
    (const char *errinfo, const void *private_info,
    size_t cb, void *user_data),
    void *user_data, cl_int *errcode_ret)
```

*properties:* NULL or CL\_CONTEXT\_PLATFORM, CL\_CONTEXT\_INTEROP\_USER\_SYNC, CL\_CONTEXT\_D3D10\_D3D11\_DEVICE\_KHR, CL\_CONTEXT\_ADAPTER\_D3D9\_D3D9EX\_DXVA\_KHR, CL\_GL\_CONTEXT\_KHR, CL\_CGL\_SHAREGROUP\_KHR, CL\_EGL\_KHR, CL\_WGL\_HDC\_KHR

```
cl_context clCreateContextFromType (
    const cl_context_properties *properties,
    cl_device_type device_type,
    void (CL_CALLBACK *pfn_notify)
    (const char *errinfo, const void *private_info,
    size_t cb, void *user_data),
    void *user_data, cl_int *errcode_ret)
```

*properties:* See clCreateContext

```
cl_int clRetainContext (cl_context context)
```

```
cl_int clReleaseContext (cl_context context)
```

```
cl_int clGetContextInfo (cl_context context,
                          cl_context_info param_name, size_t param_value_size,
                          void *param_value, size_t *param_value_size_ret)
```

*param\_name:* CL\_CONTEXT\_REFERENCE\_COUNT, CL\_CONTEXT\_DEVICES\_NUM\_DEVICES\_PROPERTIES, CL\_CONTEXT\_D3D10\_PREFER\_SHARED\_RESOURCES\_KHR, CL\_CONTEXT\_D3D11\_PREFER\_SHARED\_RESOURCES\_KHR

### Get CL Extension Function Pointers [9.2]

```
void* clGetExtensionFunctionAddressForPlatform (
    cl_platform_id platform, const char *funcname)
```

```
cl_int clEnqueueWriteBuffer (
    cl_command_queue command_queue, cl_mem buffer,
    cl_bool blocking_write, size_t offset, size_t size,
    const void *ptr, cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

```
cl_int clEnqueueWriteBufferRect (
    cl_command_queue command_queue,
    cl_mem buffer, cl_bool blocking_write,
    const size_t *buffer_origin, const size_t *host_origin,
    const size_t *region, size_t buffer_row_pitch,
    size_t buffer_slice_pitch, size_t host_row_pitch,
    size_t host_slice_pitch, const void *ptr,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

```
cl_int clEnqueueFillBuffer (
    cl_command_queue command_queue,
    cl_mem buffer, const void *pattern,
    size_t pattern_size, size_t offset, size_t size,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

```
cl_int clEnqueueCopyBuffer (
    cl_command_queue command_queue,
    cl_mem src_buffer, cl_mem dst_buffer,
    size_t src_offset, size_t dst_offset, size_t size,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

```
cl_int clEnqueueCopyBufferRect (
    cl_command_queue command_queue,
    cl_mem src_buffer, cl_mem dst_buffer,
    const size_t *src_origin, const size_t *dst_origin,
    const size_t *region, size_t src_row_pitch,
    size_t src_slice_pitch, size_t dst_row_pitch,
    size_t dst_slice_pitch, cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

### Map Buffer Objects [5.2.3]

```
void* clEnqueueMapBuffer (
    cl_command_queue command_queue, cl_mem buffer,
    cl_bool blocking_map, cl_map_flags map_flags,
    size_t offset, size_t size, cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event,
    cl_int *errcode_ret)
```

*map\_flags:* CL\_MAP\_READ, CL\_MAP\_WRITE, CL\_MAP\_WRITE\_INVALIDATE\_REGION

### Memory Objects [5.4.1, 5.4.2]

```
cl_int clRetainMemObject (cl_mem memobj)
```

```
cl_int clReleaseMemObject (cl_mem memobj)
```

```
cl_int clSetMemObjectDestructorCallback (
    cl_mem memobj, void (CL_CALLBACK *pfn_notify)
    (cl_mem memobj, void *user_data),
    void *user_data)
```

```
cl_int clEnqueueUnmapMemObject (
    cl_command_queue command_queue,
    cl_mem memobj, void *mapped_ptr,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

### Migrate Memory Objects [5.4.4]

```
cl_int clEnqueueMigrateMemObjects (
    cl_command_queue command_queue,
    cl_uint num_mem_objects,
    const cl_mem *mem_objects,
    cl_mem_migration_flags flags,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

*flags:* CL\_MIGRATE\_MEM\_OBJECT\_HOST, CL\_MIGRATE\_MEM\_OBJECT\_CONTENT\_UNDEFINED

### Query Memory Object [5.4.5]

```
cl_int clGetMemObjectInfo (cl_mem memobj,
                            cl_mem_info param_name, size_t param_value_size,
                            void *param_value, size_t *param_value_size_ret)
```

*param\_name:* CL\_MEM\_TYPE, CL\_MEM\_FLAGS, CL\_MEM\_SIZE, CL\_MEM\_HOST\_PTR, CL\_MEM\_MAP\_REFERENCE\_COUNT, CL\_MEM\_OFFSET, CL\_MEM\_CONTEXT, CL\_MEM\_ASSOCIATED\_MEMOBJECT, CL\_MEM\_D3D10\_D3D11\_RESOURCE\_KHR, CL\_MEM\_DX9\_MEDIA\_ADAPTER\_TYPE\_KHR, CL\_MEM\_DX9\_MEDIA\_SURFACE\_INFO\_KHR

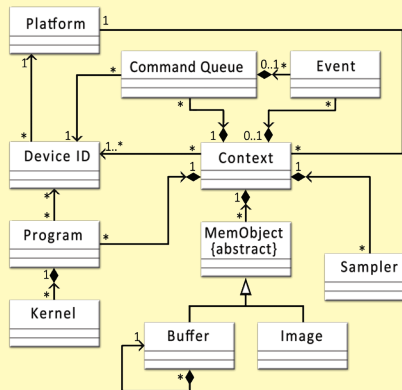
## OpenCL Class Diagram [2.1]

The figure below describes the OpenCL specification as a class diagram using the Unified Modeling Language<sup>1</sup> (UML) notation. The diagram shows both nodes and edges which are classes and their relationships. As a simplification it shows only classes, and no attributes or operations.

## Annotations

Relationships	
abstract classes	{abstract}
aggregations	◆
inheritance	△
relationship navigability	^

Cardinality	
many	*
one and only one	1
optionally one	0..1
one or more	1..*



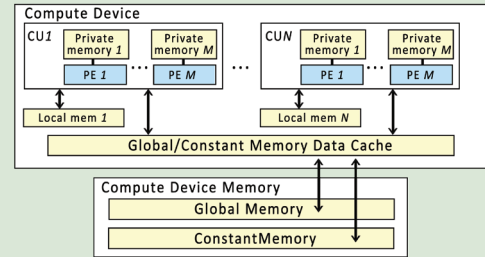
<sup>1</sup> Unified Modeling Language (<http://www.uml.org/>) is a trademark of Object Management Group (OMG).

## OpenCL Device Architecture Diagram [3.3]

The table below shows memory regions with allocation and memory access capabilities.

	Global	Constant	Local	Private
Host	Dynamic allocation Read/Write access	Dynamic allocation Read/Write access	Dynamic allocation No access	No allocation No access
Kernel	No allocation Read/Write access	Static allocation Read-only access	Static allocation Read/Write access	Static allocation Read/Write access

This conceptual OpenCL device architecture diagram shows processing elements (PE), compute units (CU), and devices. The host is not shown.



## Program Objects

## Create Program Objects [5.6.1]

```
cl_program clCreateProgramWithSource (
    cl_context context, cl_uint num_devices, const char **strings,
    const size_t *lengths, cl_int *errcode_ret)
```

```
cl_program clCreateProgramWithBinary (
    cl_context context, cl_uint num_devices,
    const cl_device_id *device_list, const size_t *lengths,
    const unsigned char **binaries, cl_int *binary_status,
    cl_int *errcode_ret)
```

```
cl_program clCreateProgramWithBuiltInKernels (
    cl_context context, cl_uint num_devices,
    const cl_device_id *device_list,
    const char *kernel_names, cl_int *errcode_ret)
```

```
cl_int clRetainProgram (cl_program program)
```

```
cl_int clReleaseProgram (cl_program program)
```

## Building Program Executables [5.6.2]

```
cl_int clBuildProgram (cl_program program,
    cl_uint num_devices, const cl_device_id *device_list,
    const char *options, void (CL_CALLBACK *pfn_notify)
    (cl_program program, void *user_data),
    void *user_data)
```

## Separate Compilation and Linking [5.6.3]

```
cl_int clCompileProgram (cl_program program,
    cl_uint num_devices, const cl_device_id *device_list,
    const char *options, cl_uint num_input_headers,
    const cl_program *input_headers,
    const char **header_include_names,
    void (CL_CALLBACK *pfn_notify)
    (cl_program program, void *user_data),
    void *user_data)
```

```
cl_program clLinkProgram (cl_context context,
    cl_uint num_devices, const cl_device_id *device_list,
    const char *options, cl_uint num_input_programs,
    const cl_program *input_programs,
    void (CL_CALLBACK *pfn_notify)
    (cl_program program, void *user_data),
    void *user_data, cl_int *errcode_ret)
```

## Unload the OpenCL Compiler [5.6.6]

```
cl_int clUnloadPlatformCompiler (
    cl_platform_id platform)
```

## Query Program Objects [5.6.7]

```
cl_int clGetProgramInfo (cl_program program,
    cl_program_info param_name, size_t param_value_size,
    void *param_value, size_t *param_value_size_ret)
```

param\_name: CL\_PROGRAM\_REFERENCE\_COUNT,  
CL\_PROGRAM\_CONTEXT, CL\_NUM\_DEVICES, CL\_DEVICES,  
CL\_PROGRAM\_SOURCE, CL\_BINARY\_LIST, CL\_BINARIES,  
CL\_PROGRAM\_NUM\_KERNELS, CL\_KERNEL\_NAMES

```
cl_int clGetProgramBuildInfo (
    cl_program program, cl_device_id device,
    cl_program_build_info param_name,
    size_t param_value_size, void *param_value,
    size_t *param_value_size_ret)
```

param\_name: CL\_PROGRAM\_BINARY\_TYPE,  
CL\_PROGRAM\_BUILD\_STATUS, CL\_OPTIONS, CL\_LOG

## Compiler Options [5.6.4]

**Preprocessor:** (-D processed in order listed in  
clBuildProgram or clCompileProgram)

-D name -D name=definition -I dir

## Math intrinsics:

-cl-single-precision-constant -cl-denorms-are-zero  
-cl-fp32-correctly-rounded-divide-sqrt

## Optimization options:

-cl-opt-disable -cl-mad-enable  
-cl-no-signed-zeros -cl-finite-math-only  
-cl-unsafe-math-optimizations -cl-fast-relaxed-math

## Warning request/suppress:

-w -Werror

## Control OpenCL C language version:

-cl-std=CL1.1 // OpenCL 1.1 specification.  
-cl-std=CL1.2 // OpenCL 1.2 specification.

## Query kernel argument information:

-cl-kernel-arg-info

## Linker Options [5.6.5]

**Library linking options:**  
-create-library  
-enable-link-options

**Program linking options:**  
-cl-denorms-are-zero  
-cl-no-signed-zeros  
-cl-unsafe-math-optimizations  
-cl-finite-math-only  
-cl-fast-relaxed-math

## Kernel and Event Objects

## Create Kernel Objects [5.7.1]

```
cl_kernel clCreateKernel (cl_program program,
    const char *kernel_name, cl_int *errcode_ret)
```

```
cl_int clCreateKernelsInProgram (cl_program program,
    cl_uint num_kernels, cl_kernel *kernels,
    cl_uint *num_kernels_ret)
```

```
cl_int clRetainKernel (cl_kernel kernel)
```

```
cl_int clReleaseKernel (cl_kernel kernel)
```

## Kernel Arguments and Queries [5.7.2, 5.7.3]

```
cl_int clSetKernelArg (cl_kernel kernel, cl_uint arg_index,
    size_t arg_size, const void *arg_value)
```

```
cl_int clGetKernelInfo (cl_kernel kernel,
    cl_kernel_info param_name, size_t param_value_size,
    void *param_value, size_t *param_value_size_ret)
```

param\_name: CL\_KERNEL\_FUNCTION\_NAME,  
CL\_KERNEL\_NUM\_ARGS, CL\_KERNEL\_REFERENCE\_COUNT,  
CL\_KERNEL\_ATTRIBUTES, CL\_CONTEXT, CL\_PROGRAM

```
cl_int clGetKernelWorkGroupInfo (
    cl_kernel kernel, cl_device_id device,
    cl_kernel_work_group_info param_name,
    size_t param_value_size, void *param_value,
    size_t *param_value_size_ret)
```

param\_name: CL\_KERNEL\_GLOBAL\_WORK\_SIZE,  
CL\_KERNEL\_COMPILE\_WORK\_GROUP\_SIZE,  
CL\_KERNEL\_LOCAL\_PRIVATE\_MEM\_SIZE,  
CL\_KERNEL\_PREFERRED\_WORK\_GROUP\_SIZE\_MULTIPLE

```
cl_int clGetKernelArgInfo (cl_kernel kernel,
    cl_uint arg_indx, cl_kernel_arg_info param_name,
    size_t param_value_size, void *param_value,
    size_t *param_value_size_ret)
```

param\_name:  
CL\_KERNEL\_ARG\_ACCESS, CL\_KERNEL\_ARG\_ADDRESS\_TYPE, CL\_KERNEL\_ARG\_QUALIFIER,  
CL\_KERNEL\_ARG\_NAME, CL\_KERNEL\_ARG\_TYPE\_NAME

## Execute Kernels [5.8]

```
cl_int clEnqueueNDRangeKernel (
    cl_command_queue command_queue,
    cl_kernel kernel, cl_uint work_dim,
    const size_t *global_work_offset,
    const size_t *global_work_size,
    const size_t *local_work_size,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

```
cl_int clEnqueueTask (
    cl_command_queue command_queue,
    cl_kernel kernel, cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

```
cl_int clEnqueueNativeKernel (cl_command_queue
    command_queue, void (*user_func)(void *),
    void *args, size_t cb_args, cl_uint num_mem_objects,
    const cl_mem *mem_list, const void **args_mem_loc,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

## Event Objects [5.9]

```
cl_event clCreateUserEvent (cl_context context,
    cl_int *errcode_ret)
```

```
cl_int clSetUserEventStatus (cl_event event,
    cl_int execution_status)
```

```
cl_int clWaitForEvents (cl_uint num_events,
    const cl_event *event_list)
```

```
cl_int clGetEventInfo (cl_event event,
    cl_event_info param_name, size_t param_value_size,
    void *param_value, size_t *param_value_size_ret)
```

param\_name: CL\_EVENT\_COMMAND\_QUEUE, CL\_EVENT\_TYPE,  
CL\_EVENT\_CONTEXT, CL\_EVENT\_REFERENCE\_COUNT,  
CL\_EVENT\_COMMAND\_EXECUTION\_STATUS

```
cl_int clSetEventCallback (cl_event event,
    cl_int command_exec_callback_type,
    void (CL_CALLBACK *pfn_notify)
    (cl_event event, cl_int event_command_exec_status,
    void *user_data),
    void *user_data)
```

```
cl_int clRetainEvent (cl_event event)
```

```
cl_int clReleaseEvent (cl_event event)
```

## Markers, Barriers, and Waiting for Events [5.10]

```
cl_int clEnqueueMarkerWithWaitList (
    cl_command_queue command_queue,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

```
cl_int clEnqueueBarrierWithWaitList (
    cl_command_queue command_queue,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

## Profiling Operations [5.12]

```
cl_int clGetEventProfilingInfo (cl_event event,
    cl_profiling_info param_name,
    size_t param_value_size, void *param_value,
    size_t *param_value_size_ret)
```

param\_name: CL\_PROFILING\_COMMAND\_QUEUED,  
CL\_PROFILING\_COMMAND\_SUBMIT, CL\_START, CL\_END

## Flush and Finish [5.13]

```
cl_int clFlush (cl_command_queue command_queue)
```

```
cl_int clFinish (cl_command_queue command_queue)
```

Supported Data Types

The optional double scalar and vector types are supported if CL\_DEVICE\_DOUBLE\_FP\_CONFIG is not zero.

Built-in Scalar Data Types [6.1.1]

OpenCL Type	API Type	Description
bool	--	true (1) or false (0)
char	cl_char	8-bit signed
unsigned char, uchar	cl_uchar	8-bit unsigned
short	cl_short	16-bit signed
unsigned short, ushort	cl_ushort	16-bit unsigned
int	cl_int	32-bit signed
unsigned int, uint	cl_uint	32-bit unsigned
long	cl_long	64-bit signed
unsigned long, ulong	cl_ulong	64-bit unsigned
float	cl_float	32-bit float
double	cl_double	64-bit. IEEE 754
half	cl_half	16-bit float (storage only)
size_t	--	32- or 64-bit unsigned integer
ptrdiff_t	--	32- or 64-bit signed integer
intptr_t	--	32- or 64-bit signed integer
uintptr_t	--	32- or 64-bit unsigned integer
void	void	void

Built-in Vector Data Types [6.1.2]

OpenCL Type	API Type	Description
char <sub>n</sub>	cl_char <sub>n</sub>	8-bit signed
uchar <sub>n</sub>	cl_uchar <sub>n</sub>	8-bit unsigned
short <sub>n</sub>	cl_short <sub>n</sub>	16-bit signed
ushort <sub>n</sub>	cl_ushort <sub>n</sub>	16-bit unsigned
int <sub>n</sub>	cl_int <sub>n</sub>	32-bit signed
uint <sub>n</sub>	cl_uint <sub>n</sub>	32-bit unsigned
long <sub>n</sub>	cl_long <sub>n</sub>	64-bit signed
ulong <sub>n</sub>	cl_ulong <sub>n</sub>	64-bit unsigned
float <sub>n</sub>	cl_float <sub>n</sub>	32-bit float
double <sub>n</sub>	cl_double <sub>n</sub>	64-bit float

Other Built-in Data Types [6.1.3]

The optional types listed here other than event\_t are only defined if CL\_DEVICE\_IMAGE\_SUPPORT is CL\_TRUE.

OpenCL Type	Description
image2d_t	2D image handle
image3d_t	3D image handle
image2d_array_t	2D image array
image1d_t	1D image handle
image1d_buffer_t	1D image buffer
image1d_array_t	1D image array
sampler_t	sampler handle
event_t	event handle

Reserved Data Types [6.1.4]

OpenCL Type	Description
bool <sub>n</sub>	boolean vector
half <sub>n</sub>	16-bit, vector
quad, quad <sub>n</sub>	128-bit float, vector
complex half, complex half <sub>n</sub> imaginary half, imaginary half <sub>n</sub>	16-bit complex, vector
complex float, complex float <sub>n</sub> imaginary float, imaginary float <sub>n</sub>	32-bit complex, vector
complex double, complex double <sub>n</sub> imaginary double, imaginary double <sub>n</sub>	64-bit complex, vector
complex quad, complex quad <sub>n</sub> imaginary quad, imaginary quad <sub>n</sub>	128-bit complex, vector
float <sub>n</sub> x <sub>m</sub>	<i>n</i> * <i>m</i> matrix of 32-bit floats
double <sub>n</sub> x <sub>m</sub>	<i>n</i> * <i>m</i> matrix of 64-bit floats

Preprocessor Directives & Macros [6.10]

#pragma OPENCL FP_CONTRACT <i>on-off-switch</i> <i>on-off-switch</i> : ON, OFF, DEFAULT	
#pragma OPENCL EXTENSION <i>extensionname</i> : <i>behavior</i>	
#pragma OPENCL EXTENSION all : <i>behavior</i>	
__FILE__	Current source file
__func__	Current function name

Vector Component Addressing [6.1.7]

Vector Components

	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
float2 v;	v.x, v.s0	v.y, v.s1														
float3 v;	v.x, v.s0	v.y, v.s1	v.z, v.s2													
float4 v;	v.x, v.s0	v.y, v.s1	v.z, v.s2	v.w, v.s3												
float8 v;	v.s0	v.s1	v.s2	v.s3	v.s4	v.s5	v.s6	v.s7								
float16 v;	v.s0	v.s1	v.s2	v.s3	v.s4	v.s5	v.s6	v.s7	v.s8	v.s9	v.sa, v.sA	v.sb, v.sB	v.sc, v.sC	v.sd, v.sD	v.se, v.sE	v.sF, v.sF

Vector Addressing Equivalences

Numeric indices are preceded by the letter s or S, e.g.: s1. Swizzling, duplication, and nesting are allowed, e.g.: v.yx, v.xx, v.lo.x

	v.lo	v.hi	v.odd	v.even
float2	v.x, v.s0	v.y, v.s1	v.y, v.s1	v.x, v.s0
float3 *	v.s01, v.xy	v.s23, v.zw	v.s13, v.yw	v.s02, v.xz
float4	v.s01, v.xy	v.s23, v.zw	v.s13, v.yw	v.s02, v.xz

	v.lo	v.hi	v.odd	v.even
float8	v.s0123	v.s4567	v.s1357	v.s0246
float16	v.s01234567	v.s89abcdef	v.s13579bdf	v.s02468ace

\* When using .lo or .hi with a 3-component vector, the .w component is undefined.

Operators and Qualifiers

Operators [6.3]

These operators behave similarly as in C99 except that operands may include vector types when possible:

+	-	*	%	/	--	++	==	!=	&
~	^	>	<	>=	<=		!	&&	
?:	>>	<<	=	,	op=	sizeof			

Address Space Qualifiers [6.5]

__global, global	__local, local
__constant, constant	__private, private

Function Qualifiers [6.7]

__kernel, kernel
__attribute__((vec_type_hint(type))) //type defaults to int
__attribute__((work_group_size_hint(X, Y, Z)))
__attribute__((reqd_work_group_size(X, Y, Z)))

Specify Type Attributes [6.11.1]

Use to specify special attributes of enum, struct and union types.

__attribute__((aligned( <i>n</i> )))	__attribute__((endian(host)))
__attribute__((aligned))	__attribute__((endian(device)))
__attribute__((packed))	__attribute__((endian))

Math Constants [6.12.2] [9.5.2]

The values of the following symbolic constants are type float, accurate within the precision of a single precision floating-point number.

MAXFLOAT	Value of maximum non-infinite single-precision floating-point number.
HUGE_VALF	Positive float expression, evaluates to +infinity.
HUGE_VAL	Positive double expression, evals. to +infinity. <i>OPTIONAL</i>

INFINITY	Constant float expression, positive or unsigned infinity.
NAN	Constant float expression, quiet NaN.

When double is supported, macros ending in _F are available in type double by removing _F from the macro name, and in type half when the half extension is enabled by replacing _F with _H.	
M_E_F	Value of e
M_LOG2E_F	Value of log <sub>2</sub> e
M_LOG10E_F	Value of log <sub>10</sub> e

M_LN2_F	Value of log <sub>e</sub> 2
M_LN10_F	Value of log <sub>e</sub> 10
M_PI_F	Value of π
M_PI_2_F	Value of π / 2
M_PI_4_F	Value of π / 4
M_1_PI_F	Value of 1 / π
M_2_PI_F	Value of 2 / π
M_2_SQRTPI_F	Value of 2 / √π
M_SQRT2_F	Value of √2
M_SQRT1_2_F	Value of 1 / √2

Integer Built-in Functions [6.12.3]

*T* is type char, char<sub>n</sub>, uchar, uchar<sub>n</sub>, short, short<sub>n</sub>, ushort, ushort<sub>n</sub>, int, int<sub>n</sub>, uint, uint<sub>n</sub>, long, long<sub>n</sub>, ulong, or ulong<sub>n</sub>, where *n* is 2, 3, 4, 8, or 16. *Tu* is the unsigned version of *T*. *Tsc* is the scalar version of *T*.

<i>Tu</i> abs ( <i>Tx</i> )	<i>x</i>
<i>Tu</i> abs_diff ( <i>Tx</i> , <i>Ty</i> )	<i>x</i> − <i>y</i>   without modulo overflow
<i>T</i> add_sat ( <i>Tx</i> , <i>Ty</i> )	<i>x</i> + <i>y</i> and saturates the result
<i>T</i> hadd ( <i>Tx</i> , <i>Ty</i> )	( <i>x</i> + <i>y</i> ) >> 1 without mod. overflow
<i>T</i> rhadd ( <i>Tx</i> , <i>Ty</i> )	( <i>x</i> + <i>y</i> + 1) >> 1
<i>T</i> clamp ( <i>Tx</i> , <i>T</i> min, <i>T</i> max) <i>T</i> clamp ( <i>Tx</i> , <i>Tsc</i> min, <i>Tsc</i> max)	min(max( <i>x</i> , <i>minval</i> ), <i>maxval</i> )
<i>T</i> clz ( <i>Tx</i> )	number of leading 0-bits in <i>x</i>
<i>T</i> mad_hi ( <i>Ta</i> , <i>Tb</i> , <i>Tc</i> )	mul_hi( <i>a</i> , <i>b</i> ) + <i>c</i>
<i>T</i> mad_sat ( <i>Ta</i> , <i>Tb</i> , <i>Tc</i> )	<i>a</i> * <i>b</i> + <i>c</i> and saturates the result
<i>T</i> max ( <i>Tx</i> , <i>Ty</i> )	<i>y</i> if <i>x</i> < <i>y</i> , otherwise it returns <i>x</i>
<i>T</i> max ( <i>Tx</i> , <i>Tsc</i> <i>y</i> )	
<i>T</i> min ( <i>Tx</i> , <i>Ty</i> )	<i>y</i> if <i>y</i> < <i>x</i> , otherwise it returns <i>x</i>
<i>T</i> min ( <i>Tx</i> , <i>Tsc</i> <i>y</i> )	<i>y</i> if <i>y</i> < <i>x</i> , otherwise it returns <i>x</i>
<i>T</i> mul_hi ( <i>Tx</i> , <i>Ty</i> )	high half of the product of <i>x</i> and <i>y</i>
<i>T</i> rotate ( <i>Tv</i> , <i>Ti</i> )	result[ <i>indx</i> ] = <i>v</i> [ <i>indx</i> ] << <i>i</i> [ <i>indx</i> ]

<i>T</i> sub_sat ( <i>Tx</i> , <i>Ty</i> )	<i>x</i> − <i>y</i> and saturates the result
<i>T</i> popcount ( <i>Tx</i> )	Number of non-zero bits in <i>x</i>

For *upsample*, return type is scalar when the parameters are scalar.

short[ <i>n</i> ] upsample ( char[ <i>n</i> ] <i>hi</i> , uchar[ <i>n</i> ] <i>lo</i> )	result[ <i>i</i> ] = ((short)hi[ <i>i</i> ] << 8)   lo[ <i>i</i> ]
ushort[ <i>n</i> ] upsample ( uchar[ <i>n</i> ] <i>hi</i> , uchar[ <i>n</i> ] <i>lo</i> )	result[ <i>i</i> ] = ((ushort)hi[ <i>i</i> ] << 8)   lo[ <i>i</i> ]
int[ <i>n</i> ] upsample ( short[ <i>n</i> ] <i>hi</i> , ushort[ <i>n</i> ] <i>lo</i> )	result[ <i>i</i> ] = ((int)hi[ <i>i</i> ] << 16)   lo[ <i>i</i> ]
uint[ <i>n</i> ] upsample ( ushort[ <i>n</i> ] <i>hi</i> , ushort[ <i>n</i> ] <i>lo</i> )	result[ <i>i</i> ] = ((uint)hi[ <i>i</i> ] << 16)   lo[ <i>i</i> ]
long[ <i>n</i> ] upsample ( int[ <i>n</i> ] <i>hi</i> , uint[ <i>n</i> ] <i>lo</i> )	result[ <i>i</i> ] = ((long)hi[ <i>i</i> ] << 32)   lo[ <i>i</i> ]
ulong[ <i>n</i> ] upsample ( uint[ <i>n</i> ] <i>hi</i> , uint[ <i>n</i> ] <i>lo</i> )	result[ <i>i</i> ] = ((ulong)hi[ <i>i</i> ] << 32)   lo[ <i>i</i> ]

The following fast integer functions optimize the performance of kernels. In these functions, *T* is type int, uint, int<sub>n</sub> or int<sub>n</sub>, where *n* is 2, 3, 4, 8, or 16.

<i>T</i> mad24 ( <i>Tx</i> , <i>Ty</i> , <i>Tz</i> )	Multiply 24-bit integer values <i>x</i> , <i>y</i> , add 32-bit int. result to 32-bit int. <i>z</i>
<i>T</i> mul24 ( <i>Tx</i> , <i>Ty</i> )	Multiply 24-bit integer values <i>x</i> and <i>y</i>

__LINE__	Integer line number
__OPENCL_VERSION__	Integer version number, e.g: 120
CL_VERSION_1_0	Substitutes integer 100 for 1.0
CL_VERSION_1_1	Substitutes integer 110 for 1.1
CL_VERSION_1_2	Substitutes integer 120 for 1.2
__OPENCL_C_VERSION__	Sub. integer for OpenCL C version.
__ENDIAN_LITTLE__	1 if device is little endian
__IMAGE_SUPPORT__	1 if images are supported

__FAST_RELAXED_MATH__	1 if -cl-fast-relaxed-math optimization option is specified
FP_FAST_FMA	Defined if double <b>fma</b> is fast
FP_FAST_FMAF	Defined if float <b>fma</b> is fast
FP_FAST_FMA_HALF	Defined if half <b>fma</b> is fast
__kernel_exec(X, <i>ty</i> en)	Same as:
__kernel__attribute__((work_group_size_hint(X, 1, 1)))	
__attribute__((vec_type_hint( <i>ty</i> en)))	



## Math Built-in Functions [6.12.2] [9.5.2]

$Ts$  is type float, optionally double, or half if the **half extension** is enabled.  $Tn$  is the vector form of  $Ts$ , where  $n$  is 2, 3, 4, 8, or 16.  $T$  is  $Ts$  and  $Tn$ .  $Q$  is qualifier `_global`, `_local`, or `_private`. **HN** indicates that half and native variants are available using only the float or floatn types by prepending "half\_" or "native\_" to the function name. Prototypes shown in brown text are available in `half_` and `native_` forms only using the float or floatn types.

$T \text{ acos}$ ( $T$ )	Arc cosine
$T \text{ acosh}$ ( $T$ )	Inverse hyperbolic cosine
$T \text{ acospi}$ ( $Tx$ )	$\text{acos}(x) / \pi$
$T \text{ asin}$ ( $T$ )	Arc sine
$T \text{ asinh}$ ( $T$ )	Inverse hyperbolic sine
$T \text{ asinpi}$ ( $Tx$ )	$\text{asin}(x) / \pi$
$T \text{ atan}$ ( $Ty\_over\_x$ )	Arc tangent
$T \text{ atan2}$ ( $Ty, Tx$ )	Arc tangent of $y/x$
$T \text{ atanh}$ ( $T$ )	Hyperbolic arc tangent
$T \text{ atanpi}$ ( $Tx$ )	$\text{atan}(x) / \pi$
$T \text{ atan2pi}$ ( $Tx, Ty$ )	$\text{atan2}(y, x) / \pi$
$T \text{ cbrt}$ ( $T$ )	Cube root
$T \text{ ceil}$ ( $T$ )	Round to integer toward + infinity
$T \text{ copysign}$ ( $Tx, Ty$ )	$x$ with sign changed to sign of $y$
$T \text{ cos}$ ( $T$ ) <b>HN</b>	Cosine
$T \text{ cosh}$ ( $T$ )	Hyperbolic cosine
$T \text{ cospi}$ ( $Tx$ )	$\cos(\pi x)$
$T \text{ half\_divide}$ ( $Tx, Ty$ )	$x/y$ ( $T$ may only be float or floatn)
$T \text{ native\_divide}$ ( $Tx, Ty$ )	
$T \text{ erfc}$ ( $T$ )	Complementary error function
$T \text{ erf}$ ( $T$ )	Calculates error function of $T$
$T \text{ exp}$ ( $Tx$ ) <b>HN</b>	Exponential base $e$
$T \text{ exp2}$ ( $T$ ) <b>HN</b>	Exponential base 2

$T \text{ exp10}$ ( $T$ ) <b>HN</b>	Exponential base 10
$T \text{ expm1}$ ( $Tx$ )	$e^x - 1.0$
$T \text{ fabs}$ ( $T$ )	Absolute value
$T \text{ fdim}$ ( $Tx, Ty$ )	Positive difference between $x$ and $y$
$T \text{ floor}$ ( $T$ )	Round to integer toward - infinity
$T \text{ fma}$ ( $Ta, Tb, Tc$ )	Multiply and add, then round
$T \text{ fmax}$ ( $Tx, Ty$ ) $Tn \text{ fmax}$ ( $Tn\ x, Ts\ y$ )	Return $y$ if $x < y$ , otherwise it returns $x$
$T \text{ fmin}$ ( $Tx, Ty$ ) $Tn \text{ fmin}$ ( $Tn\ x, Ts\ y$ )	Return $y$ if $y < x$ , otherwise it returns $x$
$T \text{ fmod}$ ( $Tx, Ty$ )	Modulus. Returns $x - y * \text{trunc}(x/y)$
$T \text{ fract}$ ( $Tx, Q\ T * iptr$ )	Fractional value in $x$
$Ts \text{ frexp}$ ( $Tx, Q\ \text{int} * exp$ ) $Tn \text{ frexp}$ ( $Tx, Q\ \text{intrn} * exp$ )	Extract mantissa and exponent
$T \text{ hypot}$ ( $Tx, Ty$ )	Square root of $x^2 + y^2$
$\text{int}[n] \text{ logb}$ ( $Tx$ )	Return exponent as an integer value
$Ts \text{ ldexp}$ ( $Tx, \text{int } n$ ) $Tn \text{ ldexp}$ ( $Tx, \text{intrn } n$ )	$x * 2^n$
$T \text{ lgamma}$ ( $Tx$ ) $Ts \text{ lgamma}_r$ ( $Tx, Q\ \text{int} * signp$ ) $Tn \text{ lgamma}_r$ ( $Tx, Q\ \text{intrn} * signp$ )	Log gamma function
$T \text{ log}$ ( $T$ ) <b>HN</b>	Natural logarithm
$T \text{ log2}$ ( $T$ ) <b>HN</b>	Base 2 logarithm
$T \text{ log10}$ ( $T$ ) <b>HN</b>	Base 10 logarithm
$T \text{ log1p}$ ( $Tx$ )	$\ln(1.0 + x)$
$T \text{ logb}$ ( $Tx$ )	Exponent of $x$
$T \text{ mad}$ ( $Ta, Tb, Tc$ )	Approximates $a * b + c$
$T \text{ maxmag}$ ( $Tx, Ty$ )	Maximum magnitude of $x$ and $y$
$T \text{ minmag}$ ( $Tx, Ty$ )	Minimum magnitude of $x$ and $y$

$T \text{ modf}$ ( $Tx, Q\ T * iptr$ )	Decompose floating-point number
$\text{float}[n] \text{ nan}$ ( $\text{uint}[n] \text{ nancode}$ ) $\text{half}[n] \text{ nan}$ ( $\text{ushort}[n] \text{ nancode}$ ) $\text{double}[n] \text{ nan}$ ( $\text{ulong}[n] \text{ nancode}$ )	Quiet NaN (Return is scalar when <i>nancode</i> is scalar)
$T \text{ nextafter}$ ( $Tx, Ty$ )	Next representable floating-point value after $x$ in the direction of $y$
$T \text{ pow}$ ( $Tx, Ty$ )	Compute $x$ to the power of $y$
$Ts \text{ pown}$ ( $Tx, \text{int } y$ ) $Tn \text{ pown}$ ( $Tx, \text{intrn } y$ )	Compute $x^y$ , where $y$ is an integer
$T \text{ powr}$ ( $Tx, Ty$ ) <b>HN</b>	Compute $x^y$ , where $x$ is $\geq 0$
$T \text{ half\_recip}$ ( $Tx$ ) $T \text{ native\_recip}$ ( $Tx$ )	$1/x$ ( $T$ may only be float or floatn)
$T \text{ remainder}$ ( $Tx, Ty$ )	Floating point remainder
$Ts \text{ remquo}$ ( $Tx, Ty, Q\ \text{int} * quo$ ) $Tn \text{ remquo}$ ( $Tx, Ty, Q\ \text{intrn} * quo$ )	Remainder and quotient
$T \text{ rint}$ ( $T$ )	Round to nearest even integer
$Ts \text{ rootn}$ ( $Tx, \text{int } y$ ) $Tn \text{ rootn}$ ( $Tx, \text{intrn } y$ )	Compute $x$ to the power of $1/y$
$T \text{ round}$ ( $Tx$ )	Integral value nearest to $x$ rounding
$T \text{ rsqrt}$ ( $T$ ) <b>HN</b>	Inverse square root
$T \text{ sin}$ ( $T$ ) <b>HN</b>	Sine
$T \text{ sincos}$ ( $Tx, Q\ T * cosval$ )	Sine and cosine of $x$
$T \text{ sinh}$ ( $T$ )	Hyperbolic sine
$T \text{ sinpi}$ ( $Tx$ )	$\sin(\pi x)$
$T \text{ sqrt}$ ( $T$ ) <b>HN</b>	Square root
$T \text{ tan}$ ( $T$ ) <b>HN</b>	Tangent
$T \text{ tanh}$ ( $T$ )	Hyperbolic tangent
$T \text{ tanpi}$ ( $Tx$ )	$\tan(\pi x)$
$T \text{ tgamma}$ ( $T$ )	Gamma function
$T \text{ trunc}$ ( $T$ )	Round to integer toward zero

## Geometric Built-in Functions [6.12.5] [9.5.4]

$Ts$  is scalar type float, optionally double, or half if the **half extension** is enabled.  $T$  is  $Ts$  and the 2-, 3-, or 4-component vector forms of  $Ts$ .

$\text{float}\{3,4\} \text{ cross}$ ( $\text{float}\{3,4\} p0, \text{float}\{3,4\} p1$ ) $\text{double}\{3,4\} \text{ cross}$ ( $\text{double}\{3,4\} p0, \text{double}\{3,4\} p1$ ) $\text{half}\{3,4\} \text{ cross}$ ( $\text{half}\{3,4\} p0, \text{half}\{3,4\} p1$ )	Cross product
$Ts \text{ distance}$ ( $Tp0, Tp1$ )	Vector distance
$Ts \text{ dot}$ ( $Tp0, Tp1$ )	Dot product
$Ts \text{ length}$ ( $Tp$ )	Vector length
$T \text{ normalize}$ ( $Tp$ )	Normal vector length 1
$\text{float fast\_distance}$ ( $\text{float } p0, \text{float } p1$ ) $\text{float fast\_distance}$ ( $\text{floatn } p0, \text{floatn } p1$ )	Vector distance
$\text{float fast\_length}$ ( $\text{float } p$ ) $\text{float fast\_length}$ ( $\text{floatn } p$ )	Vector length
$\text{float fast\_normalize}$ ( $\text{float } p$ ) $\text{floatn fast\_normalize}$ ( $\text{floatn } p$ )	Normal vector length 1

## Vector Data Load/Store [6.12.7] [9.5.6]

$T$  is type char, uchar, short, ushort, int, uint, long, ulong, or float, optionally double, or half if the **half extension** is enabled.  $Tn$  refers to the vector form of type  $T$ , where  $n$  is 2, 3, 4, 8, or 16.  $Q$  is an Address Space Qualifier listed in 6.5 unless otherwise noted. When red,  $Q$  cannot be `_constant`.  $R$  defaults to the current rounding mode, or is one of the Rounding Modes listed in 6.2.3.2.

$Tn \text{ vloadn}$ ( $\text{size\_t offset}$ , $\text{const } Q\ T * p$ )	Read vector data from address ( $p + (\text{offset} * n)$ )
$\text{void vstoren}$ ( $Tn \text{ data}$ , $\text{size\_t offset}$ , $Q\ T * p$ )	Write vector data to address ( $p + (\text{offset} * n)$ )
$\text{float vload\_half}$ ( $\text{size\_t offset}$ , $\text{const } Q\ \text{half} * p$ )	Read a half from address ( $p + \text{offset}$ )
$\text{floatn vload\_halfn}$ ( $\text{size\_t offset}$ , $\text{const } Q\ \text{half} * p$ )	Read a halfn from address ( $p + (\text{offset} * n)$ )
$\text{void vstore\_half}$ ( $\text{float data}$ , $\text{size\_t offset}$ , $Q\ \text{half} * p$ ) $\text{void vstore\_half}_R$ ( $\text{float data}$ , $\text{size\_t offset}$ , $Q\ \text{half} * p$ ) $\text{void vstore\_half}$ ( $\text{double data}$ , $\text{size\_t offset}$ , $Q\ \text{half} * p$ )	Write a half to address ( $p + \text{offset}$ )

$\text{void vstore\_half}_R$ ( $\text{double data}$ , $\text{size\_t offset}$ , $Q\ \text{half} * p$ )	Write a half to address ( $p + \text{offset}$ )
$\text{void vstore\_halfn}$ ( $\text{floatn data}$ , $\text{size\_t offset}$ , $Q\ \text{half} * p$ ) $\text{void vstore\_halfn}_R$ ( $\text{floatn data}$ , $\text{size\_t offset}$ , $Q\ \text{half} * p$ ) $\text{void vstore\_halfn}$ ( $\text{doublen data}$ , $\text{size\_t offset}$ , $Q\ \text{half} * p$ ) $\text{void vstore\_halfn}_R$ ( $\text{doublen data}$ , $\text{size\_t offset}$ , $Q\ \text{half} * p$ )	Write a half vector to address ( $p + (\text{offset} * n)$ )
$\text{floatn vloada\_halfn}$ ( $\text{size\_t offset}$ , $\text{const } Q\ \text{half} * p$ )	Read half vector data from ( $p + (\text{offset} * n)$ ). For half3, read from ( $p + (\text{offset} * 4)$ ).
$\text{void vstorea\_halfn}$ ( $\text{floatn data}$ , $\text{size\_t offset}$ , $Q\ \text{half} * p$ ) $\text{void vstorea\_halfn}_R$ ( $\text{floatn data}$ , $\text{size\_t offset}$ , $Q\ \text{half} * p$ ) $\text{void vstorea\_halfn}$ ( $\text{doublen data}$ , $\text{size\_t offset}$ , $Q\ \text{half} * p$ ) $\text{void vstorea\_halfn}_R$ ( $\text{doublen data}$ , $\text{size\_t offset}$ , $Q\ \text{half} * p$ )	Write half vector data to ( $p + (\text{offset} * n)$ ). For half3, write to ( $p + (\text{offset} * 4)$ ).

## Async Copies and Prefetch Functions [6.12.10] [9.5.7]

$T$  is type char, charn, uchar, uchar\_n, short, shortn, ushort, ushortn, int, intrn, uint, uintn, long, longn, ulong, ulongn, float, floatn, optionally double or double\_n, or half or halfn if the **half extension** is enabled.

$\text{event\_t async\_work\_group\_copy}$ ( $\_\text{local } T * dst$ , $\text{const } \_\text{global } T * src$ , $\text{size\_t num\_gentypes}$ , $\text{event\_t event}$ )	Copies $\text{num\_gentypes}$ $T$ elements from $src$ to $dst$
$\text{event\_t async\_work\_group\_copy}$ ( $\_\text{global } T * dst$ , $\text{const } \_\text{local } T * src$ , $\text{size\_t num\_gentypes}$ , $\text{event\_t event}$ )	
$\text{event\_t async\_work\_group\_strided\_copy}$ ( $\_\text{local } T * dst$ , $\text{const } \_\text{global } T * src$ , $\text{size\_t num\_gentypes}$ , $\text{size\_t src\_stride}$ , $\text{event\_t event}$ )	Copies $\text{num\_gentypes}$ $T$ elements from $src$ to $dst$
$\text{event\_t async\_work\_group\_strided\_copy}$ ( $\_\text{global } T * dst$ , $\text{const } \_\text{local } T * src$ , $\text{size\_t num\_gentypes}$ , $\text{size\_t dst\_stride}$ , $\text{event\_t event}$ )	
$\text{void wait\_group\_events}$ ( $\text{int num\_events}$ , $\text{event\_t} * \text{event\_list}$ )	Wait for events that identify the <b>async\_work\_group\_copy</b> operations to complete
$\text{void prefetch}$ ( $\text{const } \_\text{global } T * p$ , $\text{size\_t num\_gentypes}$ )	Prefetch $\text{num\_gentypes} * \text{sizeof}(T)$ bytes into the global cache

## Work-Item Built-in Functions [6.12.1]

These functions query the number of dimensions, the global and local work size specified to `clEnqueueNDRangeKernel`, and the global and local identifier of each work-item when this kernel is executed on a device.  $D$  is the dimension index.

$\text{uint get\_work\_dim}$ ( $D$ )	Number of dimensions in use
$\text{size\_t get\_global\_size}$ ( $\text{uint } D$ )	Number of global work-items
$\text{size\_t get\_global\_id}$ ( $\text{uint } D$ )	Global work-item ID value
$\text{size\_t get\_local\_size}$ ( $\text{uint } D$ )	Number of local work-items
$\text{size\_t get\_local\_id}$ ( $\text{uint } D$ )	Local work-item ID
$\text{size\_t get\_num\_groups}$ ( $\text{uint } D$ )	Number of work-groups
$\text{size\_t get\_group\_id}$ ( $\text{uint } D$ )	Returns the work-group ID
$\text{size\_t get\_global\_offset}$ ( $\text{uint } D$ )	Returns global offset

**Common Built-in Functions** [6.12.4] [9.5.3]

These functions operate component-wise and use round to nearest even rounding mode. *Ts* is type float, optionally double, or half if the **half extension** is enabled. *Tn* is the vector form of *Ts*, where *n* is 2, 3, 4, 8, or 16. *T* is *Ts* and *Tn*.

<i>T clamp</i> ( <i>T x</i> , <i>T min</i> , <i>T max</i> ) <i>Tn clamp</i> ( <i>Tn x</i> , <i>Ts min</i> , <i>Ts max</i> )	Clamp <i>x</i> to range given by <i>min</i> , <i>max</i>
<i>T degrees</i> ( <i>T radians</i> )	<i>radians</i> to <i>degrees</i>
<i>T max</i> ( <i>T x</i> , <i>T y</i> ) <i>Tn max</i> ( <i>Tn x</i> , <i>Ts y</i> )	Max of <i>x</i> and <i>y</i>

<i>T min</i> ( <i>T x</i> , <i>T y</i> ) <i>Tn min</i> ( <i>Tn x</i> , <i>Ts y</i> )	Min of <i>x</i> and <i>y</i>
<i>T mix</i> ( <i>T x</i> , <i>T y</i> , <i>T a</i> ) <i>Tn mix</i> ( <i>Tn x</i> , <i>Tn y</i> , <i>Ts a</i> )	Linear blend of <i>x</i> and <i>y</i>
<i>T radians</i> ( <i>T degrees</i> )	<i>degrees</i> to <i>radians</i>
<i>T step</i> ( <i>T edge</i> , <i>T x</i> ) <i>Tn step</i> ( <i>Ts edge</i> , <i>Tn x</i> )	0.0 if <i>x</i> < <i>edge</i> , else 1.0
<i>T smoothstep</i> ( <i>T edge0</i> , <i>T edge1</i> , <i>T x</i> ) <i>Tn smoothstep</i> ( <i>Ts edge0</i> , <i>Ts edge1</i> , <i>T x</i> )	Step and interpolate
<i>T sign</i> ( <i>T x</i> )	Sign of <i>x</i>

**Relational Built-in Functions** [6.12.6]

These functions can be used with built-in scalar or vector types as arguments and return a scalar or vector integer result. *T* is type float, float*n*, char, char*n*, uchar, uchar*n*, short, short*n*, ushort, ushort*n*, int, int*n*, uint, uint*n*, long, long*n*, ulong, ulong*n*, or optionally double or double*n*. *Ti* is type char, char*n*, short, short*n*, int, int*n*, long, or long*n*. *Tu* is type uchar, uchar*n*, ushort, ushort*n*, uint, uint*n*, ulong, or ulong*n*. *n* is 2, 3, 4, 8, or 16. **Optional extension** enables half and half*n* types.

int <i>isequal</i> (float <i>x</i> , float <i>y</i> ) int <i>n</i> <i>isequal</i> (float <i>n x</i> , float <i>n y</i> ) int <i>isequal</i> (double <i>x</i> , double <i>y</i> ) long <i>n</i> <i>isequal</i> (double <i>n x</i> , double <i>n y</i> ) int <i>isequal</i> (half <i>x</i> , half <i>y</i> ) short <i>n</i> <i>isequal</i> (half <i>n x</i> , half <i>n y</i> )	Compare of <i>x</i> == <i>y</i>
int <i>isnotequal</i> (float <i>x</i> , float <i>y</i> ) int <i>n</i> <i>isnotequal</i> (float <i>n x</i> , float <i>n y</i> ) int <i>isnotequal</i> (double <i>x</i> , double <i>y</i> ) long <i>n</i> <i>isnotequal</i> (double <i>n x</i> , double <i>n y</i> ) int <i>isnotequal</i> (half <i>x</i> , half <i>y</i> ) short <i>n</i> <i>isnotequal</i> (half <i>n x</i> , half <i>n y</i> )	Compare of <i>x</i> != <i>y</i>
int <i>isgreater</i> (float <i>x</i> , float <i>y</i> ) int <i>n</i> <i>isgreater</i> (float <i>n x</i> , float <i>n y</i> ) int <i>isgreater</i> (double <i>x</i> , double <i>y</i> ) long <i>n</i> <i>isgreater</i> (double <i>n x</i> , double <i>n y</i> ) int <i>isgreater</i> (half <i>x</i> , half <i>y</i> ) short <i>n</i> <i>isgreater</i> (half <i>n x</i> , half <i>n y</i> )	Compare of <i>x</i> > <i>y</i>
int <i>isgreaterequal</i> (float <i>x</i> , float <i>y</i> ) int <i>n</i> <i>isgreaterequal</i> (float <i>n x</i> , float <i>n y</i> ) int <i>isgreaterequal</i> (double <i>x</i> , double <i>y</i> ) long <i>n</i> <i>isgreaterequal</i> (double <i>n x</i> , double <i>n y</i> ) int <i>isgreaterequal</i> (half <i>x</i> , half <i>y</i> ) short <i>n</i> <i>isgreaterequal</i> (half <i>n x</i> , half <i>n y</i> )	Compare of <i>x</i> >= <i>y</i>
int <i>isless</i> (float <i>x</i> , float <i>y</i> ) int <i>n</i> <i>isless</i> (float <i>n x</i> , float <i>n y</i> ) int <i>isless</i> (double <i>x</i> , double <i>y</i> ) long <i>n</i> <i>isless</i> (double <i>n x</i> , double <i>n y</i> ) int <i>isless</i> (half <i>x</i> , half <i>y</i> ) short <i>n</i> <i>isless</i> (half <i>n x</i> , half <i>n y</i> )	Compare of <i>x</i> < <i>y</i>
int <i>islessequal</i> (float <i>x</i> , float <i>y</i> ) int <i>n</i> <i>islessequal</i> (float <i>n x</i> , float <i>n y</i> ) int <i>islessequal</i> (double <i>x</i> , double <i>y</i> ) long <i>n</i> <i>islessequal</i> (double <i>n x</i> , double <i>n y</i> ) int <i>islessequal</i> (half <i>x</i> , half <i>y</i> ) short <i>n</i> <i>islessequal</i> (half <i>n x</i> , half <i>n y</i> )	Compare of <i>x</i> <= <i>y</i>
int <i>islessgreater</i> (float <i>x</i> , float <i>y</i> ) int <i>n</i> <i>islessgreater</i> (float <i>n x</i> , float <i>n y</i> ) int <i>islessgreater</i> (double <i>x</i> , double <i>y</i> ) long <i>n</i> <i>islessgreater</i> (double <i>n x</i> , double <i>n y</i> ) int <i>islessgreater</i> (half <i>x</i> , half <i>y</i> ) short <i>n</i> <i>islessgreater</i> (half <i>n x</i> , half <i>n y</i> )	Compare of ( <i>x</i> < <i>y</i> )    ( <i>x</i> > <i>y</i> )
int <i>isfinite</i> (float) int <i>n</i> <i>isfinite</i> (float <i>n</i> ) int <i>isfinite</i> (double) long <i>n</i> <i>isfinite</i> (double <i>n</i> ) int <i>isfinite</i> (half) short <i>n</i> <i>isfinite</i> (half <i>n</i> )	Test for finite value

int <i>isinf</i> (float) int <i>n</i> <i>isinf</i> (float <i>n</i> ) int <i>isinf</i> (double) long <i>n</i> <i>isinf</i> (double <i>n</i> ) int <i>isinf</i> (half) short <i>n</i> <i>isinf</i> (half <i>n</i> )	Test for + or - infinity
int <i>isnan</i> (float) int <i>n</i> <i>isnan</i> (float <i>n</i> ) int <i>isnan</i> (double) long <i>n</i> <i>isnan</i> (double <i>n</i> ) int <i>isnan</i> (half) short <i>n</i> <i>isnan</i> (half <i>n</i> )	Test for a NaN
int <i>isnormal</i> (float) int <i>n</i> <i>isnormal</i> (float <i>n</i> ) int <i>isnormal</i> (double) long <i>n</i> <i>isnormal</i> (double <i>n</i> ) int <i>isnormal</i> (half) short <i>n</i> <i>isnormal</i> (half <i>n</i> )	Test for a normal value
long <i>n</i> <i>isnormal</i> (double <i>n</i> ) int <i>isnormal</i> (half) short <i>n</i> <i>isnormal</i> (half <i>n</i> )	Test for a normal value
int <i>isordered</i> (float <i>x</i> , float <i>y</i> ) int <i>n</i> <i>isordered</i> (float <i>n x</i> , float <i>n y</i> ) int <i>isordered</i> (double <i>x</i> , double <i>y</i> ) long <i>n</i> <i>isordered</i> (double <i>n x</i> , double <i>n y</i> ) int <i>isordered</i> (half <i>x</i> , half <i>y</i> ) short <i>n</i> <i>isordered</i> (half <i>n x</i> , half <i>n y</i> )	Test if arguments are ordered
int <i>isunordered</i> (float <i>x</i> , float <i>y</i> ) int <i>n</i> <i>isunordered</i> (float <i>n x</i> , float <i>n y</i> ) int <i>isunordered</i> (double <i>x</i> , double <i>y</i> ) long <i>n</i> <i>isunordered</i> (double <i>n x</i> , double <i>n y</i> ) int <i>isunordered</i> (half <i>x</i> , half <i>y</i> ) short <i>n</i> <i>isunordered</i> (half <i>n x</i> , half <i>n y</i> )	Test if arguments are unordered
int <i>signbit</i> (float) int <i>n</i> <i>signbit</i> (float <i>n</i> ) int <i>signbit</i> (double) long <i>n</i> <i>signbit</i> (double <i>n</i> ) int <i>signbit</i> (half) short <i>n</i> <i>signbit</i> (half <i>n</i> )	Test for sign bit
int <i>any</i> ( <i>Ti x</i> )	1 if MSB in component of <i>x</i> is set; else 0
int <i>all</i> ( <i>Ti x</i> )	1 if MSB in all components of <i>x</i> are set; else 0
<i>T bitselect</i> ( <i>T a</i> , <i>T b</i> , <i>T c</i> ) half <i>bitselect</i> (half <i>a</i> , half <i>b</i> , half <i>c</i> ) half <i>n</i> <i>bitselect</i> (half <i>n a</i> , half <i>n b</i> , half <i>n c</i> )	Each bit of result is corresponding bit of <i>a</i> if corresponding bit of <i>c</i> is 0
<i>T select</i> ( <i>T a</i> , <i>T b</i> , <i>Ti c</i> ) <i>T select</i> ( <i>T a</i> , <i>T b</i> , <i>Tu c</i> ) half <i>n</i> <i>select</i> (half <i>n a</i> , half <i>n b</i> , short <i>n c</i> ) half <i>select</i> (half <i>a</i> , half <i>b</i> , short <i>c</i> ) half <i>n</i> <i>select</i> (half <i>n a</i> , half <i>n b</i> , ushort <i>n c</i> ) half <i>select</i> (half <i>a</i> , half <i>b</i> , ushort <i>c</i> )	For each component of a vector type, result[ <i>i</i> ] = if MSB of <i>c</i> [ <i>i</i> ] is set ? <i>b</i> [ <i>i</i> ] : <i>a</i> [ <i>i</i> ] For scalar type, result = <i>c</i> ? <i>b</i> : <i>a</i>

**Atomic Functions** [6.12.11] [9.3]

These functions provide atomic operations on 32-bit signed and unsigned integers and single precision floating-point to locations in `__global` or `__local` memory. *T* is type int or unsigned int. *T* may also be type float for **atomic\_xchg**, and type long or ulong for extended 64-bit atomic functions. *Q* is volatile `__global` or volatile `__local`.

<i>T atomic_add</i> ( <i>Q T *p</i> , <i>T val</i> )	Read, add, and store
<i>T atomic_sub</i> ( <i>Q T *p</i> , <i>T val</i> )	Read, subtract, and store
<i>T atomic_xchg</i> ( <i>Q T *p</i> , <i>T val</i> )	Read, swap, and store
<i>T atomic_inc</i> ( <i>Q T *p</i> )	Read, increment, and store
<i>T atomic_dec</i> ( <i>Q T *p</i> )	Read, decrement, and store
<i>T atomic_cmpxchg</i> ( <i>Q T *p</i> , <i>T cmp</i> , <i>T val</i> )	Read, store (* <i>p</i> == <i>cmp</i> ) ? <i>val</i> : * <i>p</i>
<i>T atomic_min</i> ( <i>Q T *p</i> , <i>T val</i> )	Read, store min(* <i>p</i> , <i>val</i> )
<i>T atomic_max</i> ( <i>Q T *p</i> , <i>T val</i> )	Read, store max(* <i>p</i> , <i>val</i> )
<i>T atomic_and</i> ( <i>Q T *p</i> , <i>T val</i> )	Read, store (* <i>p</i> & <i>val</i> )
<i>T atomic_or</i> ( <i>Q T *p</i> , <i>T val</i> )	Read, store (* <i>p</i>   <i>val</i> )
<i>T atomic_xor</i> ( <i>Q T *p</i> , <i>T val</i> )	Read, store (* <i>p</i> ^ <i>val</i> )

Optional extensions enable forms of these functions using the `atom_` prefix that implement atomic operations on 64-bit signed and unsigned integers. To use any of these forms, include the following in the OpenCL program source:

```
#pragma OPENCL_EXTENSION extension-name : enable
```

Use `cl_khr_int64_base_atomics` for *extension-name* to enable 64-bit versions of the following functions:

atom_add	atom_sub	atom_inc
atom_dec	atom_xchg	atom_cmpxchg

Use `cl_khr_int64_extended_atomics` for *extension-name* to enable 64-bit versions of the following functions:

atom_min	atom_max	atom_and
atom_or	atom_xor	

**Conversions and Type Casting Examples** [6.2]

```
T a = (T)b; // Scalar to scalar, or scalar to vector
T a = convert_T(b);      T a = convert_T_R(b);
T a = as_T(b);           T a = convert_T_sat_R(b);
```

*R* can be one of the following rounding modes:

_rte	to nearest even	_rtp	toward + infinity
_rtz	toward zero	_rtn	toward - infinity

**Synchronization and Explicit Memory Fence Functions** [6.12.8, 6.12.9]

*flags* argument is the memory address space, set to a combination of `CLK_LOCAL_MEM_FENCE` and `CLK_GLOBAL_MEM_FENCE`. Explicit memory fence functions provide ordering between memory operations of a work-item.

void <i>barrier</i> ( <i>cl_mem_fence_flags flags</i> )	Work-items in a work-group must execute this before any can continue
void <i>mem_fence</i> ( <i>cl_mem_fence_flags flags</i> )	Orders loads and stores of a work-item executing a kernel
void <i>read_mem_fence</i> ( <i>cl_mem_fence_flags flags</i> )	Orders memory loads
void <i>write_mem_fence</i> ( <i>cl_mem_fence_flags flags</i> )	Orders memory stores

**Miscellaneous Vector Functions** [6.12.12]

*Tm* and *Tn* are type char*n*, uchar*n*, short*n*, ushort*n*, int*n*, uint*n*, long*n*, ulong*n*, float*n*, optionally double*n*, or half*n* if the **half extension** is enabled, where *n* is 2, 4, 8, or 16 except in **vec\_step** it may also be 3. *TUn* is uchar*n*, ushort*n*, uint*n*, or ulong*n*.

int <i>vec_step</i> ( <i>Tn a</i> ) int <i>vec_step</i> ( <i>typename</i> )	Takes a built-in scalar or vector data type argument, returns an integer showing number of elements in the scalar or vector. Returns 1 for scalar, 4 for 3-component vector, else number of elements in the specified type.
<i>Tn shuffle</i> ( <i>Tm x</i> , <i>TUn mask</i> )	Construct permutation of elements from one or two input vectors, return a vector with same element type as input and length that is the same as the shuffle mask.
<i>Tn shuffle2</i> ( <i>Tm x</i> , <i>Tm y</i> , <i>TUn mask</i> )	

**printf Function** [6.12.13]

Writes output to an implementation-defined stream.

```
int printf (constant char * restrict format, ...)
```

**printf output synchronization**

When the event associated with a particular kernel invocation completes, the output of applicable printf() calls is flushed to the implementation-defined output stream.

**printf format string**

The format string follows C99 conventions and supports an optional vector specifier:

```
%[flags][width][precision][vector][length]conversion
```

**Examples:**

The following examples show the use of the vector specifier in the **printf** format string.

```
float4 f = (float4)(1.0f, 2.0f, 3.0f, 4.0f);
printf("f4 = %2.2v4f\n", f);
```

Output: f4 = 1.00,2.00,3.00,4.00

```
uchar4 uc = (uchar4)(0xFA, 0xFB, 0xFC, 0xFD);
printf("uc = %#v4x\n", uc);
```

Output: uc = 0xfa,0xfb,0xfc,0xfd

```
uint2 ui = (uint2)(0x12345678, 0x87654321);
printf("unsigned short value = (%#v2hx)\n", ui);
```

Output: unsigned short value = (0x5678,0x4321)

**OpenCL Image Processing:** Following is a subset of the OpenCL specification that pertains to image processing and graphics.

## Image Objects

### Create Image Objects [5.3.1]

```
cl_mem clCreateImage (cl_context context,
    cl_mem_flags flags,
    const cl_image_format *image_format,
    const cl_image_desc *image_desc,
    void *host_ptr, cl_int *errcode_ret)
flags:
    CL_MEM_READ_WRITE,
    CL_MEM_{{WRITE, READ}}_ONLY,
    CL_MEM_HOST_{{WRITE, READ}}_ONLY,
    CL_MEM_HOST_NO_ACCESS,
    CL_MEM_{{USE, ALLOC, COPY}}_HOST_PTR
```

### Query List of Supported Image Formats [5.3.2]

```
cl_int clGetSupportedImageFormats (
    cl_context context, cl_mem_flags flags,
    cl_mem_object_type image_type,
    cl_uint num_entries, cl_image_format *image_formats,
    cl_uint *num_image_formats)
flags: See clCreateImage
image_type: CL_MEM_OBJECT_IMAGE1D, 2D, 3D),
    CL_MEM_OBJECT_IMAGE1D_BUFFER,
    CL_MEM_OBJECT_IMAGE1D_2D_ARRAY
```

### Read, Write, Copy Image Objects [5.3.3]

```
cl_int clEnqueueReadImage (
    cl_command_queue command_queue,
    cl_mem image, cl_bool blocking_read,
    const size_t *origin, const size_t *region,
    size_t row_pitch, size_t slice_pitch, void *ptr,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)

cl_int clEnqueueWriteImage (
    cl_command_queue command_queue,
    cl_mem image, cl_bool blocking_write,
    const size_t *origin, const size_t *region,
    size_t input_row_pitch, size_t input_slice_pitch,
    const void *ptr, cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)

void * clEnqueueCopyImage (
    cl_command_queue command_queue, cl_mem image,
    cl_bool blocking_map, cl_map_flags map_flags,
    const size_t *origin, const size_t *region,
    size_t *image_row_pitch, size_t *image_slice_pitch,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event,
    cl_int *errcode_ret)

Also see clGetMemObjectInfo [5.4.5]
```

## Image Read and Write Built-in Functions

[6.12.14] [9.4, 9.5.8]

The built-in functions defined in this section can only be used with image memory objects created with `clCreateImage`. `sampler` specifies the addressing and filtering mode to use. To enable the `read_imageh` and `write_imageh` forms, enable the extension `cl_khr_fp16`. To enable the type `image3d_t` in functions `write_imagef`, `i`, `ui`, `h`}, enable the extension `cl_khr_3d_image_writes`.

### Read and write functions for 1D images

Read an element from a 1D image, or write a color value to a location in a 1D image.

```
float4 read_imagef (image1d_t image, sampler_t sampler,
    {int, float} coord)
float4 read_imagef (image1d_t image, int coord)
float4 read_imagef (image1d_array_t image,
    sampler_t sampler, {int2, float4} coord)
float4 read_imagef (image1d_array_t image, int2 coord)
float4 read_imagef (image1d_buffer_t image, int coord)

int4 read_imagei (image1d_t image, sampler_t sampler,
    {int, float} coord)
int4 read_imagei (image1d_t image, int coord)
int4 read_imagei (image1d_array_t image, sampler_t sampler,
    {int2, float2} coord)
int4 read_imagei (image1d_array_t image, int2 coord)
int4 read_imagei (image1d_buffer_t image, int coord)

uint4 read_imageui (image1d_t image, sampler_t sampler,
    {int, float} coord)
uint4 read_imageui (image1d_t image, int coord)
uint4 read_imageui (image1d_array_t image,
    sampler_t sampler, {int2, float2} coord)
uint4 read_imageui (image1d_array_t image, int2 coord)
uint4 read_imageui (image1d_buffer_t image, int coord)

half4 read_imageh (image1d_t image, sampler_t sampler,
    {int, float} coord)
half4 read_imageh (image1d_t image, int coord)
half4 read_imageh (image1d_array_t image,
    sampler_t sampler, {int2, float4} coord)
half4 read_imageh (image1d_array_t image, int2 coord)
half4 read_imageh (image1d_buffer_t image, int coord)
```

```
void write_imagef (image1d_t image, int coord, float4 color)
void write_imagef (image1d_array_t image, int2 coord,
    float4 color)
void write_imagef (image1d_buffer_t image, int coord,
    float4 color)
```

```
cl_int clEnqueueFillImage (
    cl_command_queue command_queue,
    cl_mem image, const void *fill_color,
    const size_t *origin, const size_t *region,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list,
    cl_event *event)

cl_int clEnqueueCopyImage (
    cl_command_queue command_queue,
    cl_mem src_image, cl_mem dst_image,
    const size_t *src_origin, const size_t *dst_origin,
    const size_t *region, cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

### Copy Between Image, Buffer Objects [5.3.4]

```
cl_int clEnqueueCopyImageToBuffer (
    cl_command_queue command_queue,
    cl_mem src_image, cl_mem dst_buffer,
    const size_t *src_origin, const size_t *region,
    size_t dst_offset, cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)

cl_int clEnqueueCopyBufferToImage (
    cl_command_queue command_queue,
    cl_mem src_buffer, cl_mem dst_image,
    size_t src_offset,
    const size_t *dst_origin, const size_t *region,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

### Map and Unmap Image Objects [5.3.5]

```
void * clEnqueueMapImage (
    cl_command_queue command_queue, cl_mem image,
    cl_bool blocking_map, cl_map_flags map_flags,
    const size_t *origin, const size_t *region,
    size_t *image_row_pitch, size_t *image_slice_pitch,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event,
    cl_int *errcode_ret)
```

Also see `clGetMemObjectInfo` [5.4.5]

### Read and write functions for 1D images (continued)

```
void write_imagei (image1d_t image, int coord, int4 color)
void write_imagei (image1d_array_t image, int2 coord,
    int4 color)
void write_imagei (image1d_buffer_t image, int coord,
    int4 color)

void write_imageh (image1d_t image, int coord, half4 color)
void write_imageh (image1d_array_t image, int2 coord,
    half4 color)
void write_imageh (image1d_buffer_t image, int coord,
    half4 color)

void write_imageui (image1d_t image, int coord, uint4 color)
void write_imageui (image1d_array_t image, int2 coord,
    uint4 color)
void write_imageui (image1d_buffer_t image, int coord,
    uint4 color)
```

### Read and write functions for 2D images

Read an element from a 2D image, or write a color value to a location in a 2D image.

```
float4 read_imagef (image2d_t image, sampler_t sampler,
    {int2, float2} coord)
float4 read_imagef (image2d_t image, int2 coord)
float4 read_imagef (image2d_array_t image,
    sampler_t sampler, {int4, float4} coord)
float4 read_imagef (image2d_array_t image, int4 coord)

int4 read_imagei (image2d_t image, sampler_t sampler,
    {int2, float2} coord)
int4 read_imagei (image2d_t image, int2 coord)
int4 read_imagei (image2d_array_t image, sampler_t sampler,
    {int4, float4} coord)
int4 read_imagei (image2d_array_t image, int4 coord)

uint4 read_imageui (image2d_t image, sampler_t sampler,
    {int2, float2} coord)
uint4 read_imageui (image2d_t image, int2 coord)
uint4 read_imageui (image2d_array_t image,
    sampler_t sampler, {int4, float4} coord)
uint4 read_imageui (image2d_array_t image, int4 coord)

half4 read_imageh (image2d_t image, sampler_t sampler,
    {int2, float2} coord)
half4 read_imageh (image2d_t image, int2 coord)
half4 read_imageh (image2d_array_t image,
    sampler_t sampler, {int4, float4} coord)
half4 read_imageh (image2d_array_t image, int4 coord)
```

### Query Image Objects [5.3.6]

```
cl_int clGetImageInfo (cl_mem image,
    cl_image_info param_name, size_t param_value_size,
    void *param_value, size_t *param_value_size_ret)
param_name: CL_IMAGE_{{ARRAY, ELEMENT}}_SIZE,
    CL_IMAGE_{{ROW, SLICE}}_PITCH,
    CL_IMAGE_{{FORMAT, BUFFER, HEIGHT, WIDTH, DEPTH}},
    CL_IMAGE_NUM_{{SAMPLES, MIP_LEVELS}},
    CL_IMAGE_DX9_MEDIA_PLANE_KHR,
    CL_IMAGE_{{D3D10, D3D11}}_SUBRESOURCE_KHR
```

### Image Formats [5.3.1.1, 9.5]

Supported image formats: `image_channel_order` with `image_channel_data_type`.

Built-in support: [Table 5.8]

```
CL_RGBA: CL_HALF_FLOAT, CL_FLOAT, CL_UNORM_INT8,16},
    CL_SIGNED_INT{8,16,32}, CL_UNSIGNED_INT{8,16,32}
```

```
CL_BGRA: CL_UNORM_INT8
```

Optional support: [Table 5.6]

```
CL_R, CL_A: CL_HALF_FLOAT, CL_FLOAT, CL_UNORM_INT8,16},
    CL_SIGNED_INT{8,16,32}, CL_UNSIGNED_INT{8,16,32},
    CL_SNORM_INT8,16}
```

```
CL_INTENSITY: CL_HALF_FLOAT, CL_FLOAT, CL_UNORM_INT8,16},
    CL_SNORM_INT8,16}
```

```
CL_LUMINANCE: CL_UNORM_INT8,16}, CL_HALF_FLOAT,
    CL_FLOAT, CL_SNORM_INT8,16}
```

```
CL_RG, CL_RA: CL_HALF_FLOAT, CL_FLOAT, CL_UNORM_INT8,16},
    CL_SIGNED_INT{8,16,32}, CL_UNSIGNED_INT{8,16,32},
    CL_SNORM_INT8,16}
```

```
CL_RGB: CL_UNORM_SHORT_{{555,565}}, CL_UNORM_INT_101010
```

```
CL_ARGB: CL_UNORM_INT8, CL_SIGNED_INT8,
    CL_UNSIGNED_INT8, CL_SNORM_INT8
```

```
CL_BGRA: CL_{{SIGNED, UNSIGNED}}_INT8, CL_SNORM_INT8
```

### Read and write functions for 2D images (continued)

```
void write_imagef (image2d_t image, int2 coord, float4 color)
void write_imagef (image2d_array_t image, int4 coord,
    float4 color)

void write_imagei (image2d_t image, int2 coord, int4 color)
void write_imagei (image2d_array_t image, int4 coord,
    int4 color)

void write_imageui (image2d_t image, int2 coord,
    uint4 color)
void write_imageui (image2d_array_t image, int4 coord,
    uint4 color)

void write_imageh (image2d_t image, int2 coord, half4 color)
void write_imageh (image2d_array_t image, int4 coord,
    half4 color)
```

### Read and write functions for 3D images

Read an element from a 3D image, or write a color value to a location in a 3D image.

```
float4 read_imagef (image3d_t image, sampler_t sampler,
    {int4, float4} coord)
float4 read_imagef (image3d_t image, int4 coord)

int4 read_imagei (image3d_t image, sampler_t sampler,
    {int4, float4} coord)
int4 read_imagei (image3d_t image, int4 coord)

uint4 read_imageui (image3d_t image, sampler_t sampler,
    {int4, float4} coord)
uint4 read_imageui (image3d_t image, int4 coord)

half4 read_imageh (image3d_t image, sampler_t sampler,
    {int4, float4} coord)
half4 read_imageh (image3d_t image, int4 coord)
```

Use this pragma to enable writes to type `image3d_t`:

```
#pragma OPENCL EXTENSION cl_khr_3d_image_writes : enable
```

```
void write_imagef (image3d_t image, int4 coord,
    float4 color)
```

```
void write_imagei (image3d_t image, int4 coord, int4 color)
```

```
void write_imageui (image3d_t image, int4 coord, uint4 color)
```

```
void write_imageh (image3d_t image, int4 coord, half4 color)
```

### Access Qualifiers [6.6]

Apply to 2D and 3D image types to declare if the image memory object is being read or written by a kernel.

```
__read_only, read_only
__write_only, write_only
```



**OpenCL Image Processing (continued):** Following is a subset of the OpenCL specification that pertains to image processing and graphics.

## Sampler Objects [5.5]

```
cl_sampler_t clCreateSampler (
    cl_context context, cl_bool normalized_coords,
    cl_addressing_mode addressing_mode,
    cl_filter_mode filter_mode, cl_int *errcode_ret)
    addressing_mode: CL_ADDRESS_MIRRORED_REPEAT,
    CL_ADDRESS_CLAMP_TO_EDGE, CL_ADDRESS_NONE
    filter_mode: CL_FILTER_NEAREST, CL_FILTER_LINEAR
cl_int_t clRetainSampler (cl_sampler_t sampler)
cl_int_t clReleaseSampler (cl_sampler_t sampler)
cl_int_t clGetSamplerInfo (cl_sampler_t sampler,
    cl_sampler_info param_name,
    size_t param_value_size, void *param_value,
    size_t *param_value_size_ret)
    param_name: CL_SAMPLER_REFERENCE_COUNT,
    CL_SAMPLER_CONTEXT, CL_FILTER_MODE,
    CL_SAMPLER_ADDRESSING_MODE,
    CL_SAMPLER_NORMALIZED_COORDS
```

## Sampler Declaration Fields [6.12.14.1]

The sampler can be passed as an argument to the kernel using `clSetKernelArg`, or can be declared in the outermost scope of kernel functions, or it can be a constant variable of type `sampler_t` declared in the program source.

```
const sampler_t <sampler-name> =
    <normalized-mode> | <address-mode> | <filter-mode>
    normalized-mode:
    CLK_NORMALIZED_COORDS_{TRUE, FALSE}
    address-mode:
    CLK_ADDRESS_{REPEAT, CLAMP, NONE},
    CLK_ADDRESS_{CLAMP_TO_EDGE, MIRRORING_REPEAT}
    filter-mode: CLK_FILTER_NEAREST, CLK_FILTER_LINEAR
```

## Direct3D 10 Sharing [9.9]

Provide interoperability between OpenCL and Direct3D 10. If supported, `cl_khr_d3d10_sharing` will be present in `CL_PLATFORM_EXTENSIONS` or `CL_DEVICE_EXTENSIONS`.

```
cl_int_t clGetDeviceIDsFromD3D10KHR (
    cl_platform_id platform,
    cl_device_source_khr d3d_device_source,
    void *d3d_object,
    cl_device_source_khr d3d_device_set,
    cl_uint num_entries, cl_device_id *devices,
    cl_uint *num_devices)
    d3d_device_source:
    CL_D3D10_DEVICE, DXGI_ADAPTER_KHR
    d3d_device_set:
    CL_{ALL, PREFERRED}_DEVICES_FOR_D3D10_KHR
cl_mem_t clCreateFromD3D10BufferKHR (
    cl_context context, cl_mem_flags flags,
    ID3D10Buffer *resource, cl_int *errcode_ret)
    flags: CL_MEM_{READ, WRITE}_ONLY, CL_MEM_READ_WRITE
cl_mem_t clCreateFromD3D10Texture2DKHR (
    cl_context context, cl_mem_flags flags,
    ID3D10Texture2D *resource, cl_int *errcode_ret)
    flags: See clCreateFromD3D10BufferKHR
cl_mem_t clCreateFromD3D10Texture3DKHR (
    cl_context context, cl_mem_flags flags,
    ID3D10Texture3D *resource, cl_int *errcode_ret)
    flags: See clCreateFromD3D10BufferKHR
cl_int_t clEnqueueAcquireD3D10ObjectsKHR (
    cl_command_queue command_queue,
    cl_uint num_objects, const cl_mem *mem_objects,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
cl_int_t clEnqueueReleaseD3D10ObjectsKHR (
    cl_command_queue command_queue,
    cl_uint num_objects, const cl_mem *mem_objects,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

## Direct3D 11 Sharing [9.11]

Provide interoperability between OpenCL and Direct3D 11. If supported, `cl_khr_d3d11_sharing` will be present in `CL_PLATFORM_EXTENSIONS` or `CL_DEVICE_EXTENSIONS`.

```
cl_mem_t clCreateFromD3D11Texture2DKHR (
    cl_context context, cl_mem_flags flags,
    ID3D11Texture2D *resource,
    UINT subresource, cl_int *errcode_ret)
    flags: CL_MEM_{READ, WRITE}_ONLY, CL_MEM_READ_WRITE
```

## Image Query Functions [6.12.14.5]

### Query image width, height, and depth in pixels

```
int get_image_width (image1d_t image)
int get_image_width (image1d_buffer_t image)
int get_image_width (image1d_array_t image)
int get_image_height (image2d_t image)
int get_image_height (image2d_array_t image)
int get_image_depth (image3d_t image)
```

### Query image array size

```
size_t get_image_array_size (image1d_array_t image)
size_t get_image_array_size (image2d_array_t image)
```

### Query image dimensions

```
int2 get_image_dim (image2d_t image)
int2 get_image_dim (image2d_array_t image)
int4 get_image_dim (image3d_t image)
```

### Query image Channel data type and order

```
int get_image_channel_data_type (image1d_t image)
int get_image_channel_data_type (image1d_buffer_t image)
int get_image_channel_data_type (image1d_array_t image)
int get_image_channel_order (image1d_t image)
int get_image_channel_order (image1d_buffer_t image)
int get_image_channel_order (image1d_array_t image)
```

## OpenGL Sharing

Functions available if `cl_khr_gl_sharing` or `cl_apple_gl_sharing` is supported. Creating OpenCL memory objects from OpenGL objects using `clCreateFromGLBuffer`, `clCreateFromGLTexture`, and `clCreateFromGLRenderbuffer` ensure the OpenGL object will not be deleted while the corresponding OpenCL memory object exists.

### CL Buffer Objects > GL Buffer Objects [9.7.2]

```
cl_mem_t clCreateFromGLBuffer (cl_context context,
    cl_mem_flags flags, GLuint bufobj, cl_int *errcode_ret)
    flags: CL_MEM_{READ, WRITE}_ONLY, CL_MEM_READ_WRITE
```

### CL Image Objects > GL Textures [9.7.3]

```
cl_mem_t clCreateFromGLTexture (cl_context context,
    cl_mem_flags flags, GLenum texture_target,
    GLint miplevel, GLuint texture, cl_int *errcode_ret)
    flags: See clCreateFromGLBuffer
```

```
texture_target: GL_TEXTURE_1D, 2D, 3D, GL_ARRAY,
GL_TEXTURE_3D, BUFFER, RECTANGLE,
GL_TEXTURE_CUBE_MAP_POSITIVE_X, Y, Z,
GL_TEXTURE_CUBE_MAP_NEGATIVE_X, Y, Z
```

### CL Image Objects > GL Renderbuffers [9.7.4]

```
cl_mem_t clCreateFromGLRenderbuffer (
    cl_context context, cl_mem_flags flags,
    GLuint renderbuffer, cl_int *errcode_ret)
    flags: CL_MEM_{READ, WRITE}_ONLY, CL_MEM_READ_WRITE
```

### Query Information [9.7.5]

```
cl_int_t clGetGLObjectInfo (cl_mem memobj,
    cl_gl_object_type *gl_object_type,
    GLuint *gl_object_name)
```

\*gl\_object\_type returns:

```
CL_GL_OBJECT_TEXTURE_BUFFER,
CL_GL_OBJECT_TEXTURE_1D, 2D, 3D,
CL_GL_OBJECT_TEXTURE_1D_ARRAY,
CL_GL_OBJECT_{BUFFER, RENDERBUFFER}
```

```
cl_int_t clGetGLTextureInfo (cl_mem memobj,
    cl_gl_texture_info param_name,
    size_t param_value_size, void *param_value,
    size_t *param_value_size_ret)
    param_name:
    CL_GL_{TEXTURE_TARGET, MIPMAP_LEVEL}
```

### Share Objects [9.7.6]

```
cl_int_t clEnqueueAcquireGLObjects (
    cl_command_queue command_queue,
    cl_uint num_objects, const cl_mem *mem_objects,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

```
cl_int_t clEnqueueReleaseGLObjects (
    cl_command_queue command_queue,
    cl_uint num_objects, const cl_mem *mem_objects,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

### CL Event Objects > GL Sync Objects [9.8.2]

```
cl_event_t clCreateEventFromGLsyncKHR (
    cl_context context, GLsync sync, cl_int *errcode_ret)
```

### CL Context > GL Context, Sharegroup [9.6.5]

```
cl_int_t clGetGLContextInfoKHR (
    const cl_context_properties *properties,
    cl_gl_context_info param_name, size_t param_value_size,
    void *param_value, size_t *param_value_size_ret)
    param_name: CL_DEVICES_FOR_GL_CONTEXT_KHR,
    CL_CURRENT_DEVICE_FOR_GL_CONTEXT_KHR
```

## DX9 Media Surface Sharing [9.10]

These functions allow applications to use media surfaces as OpenCL memory objects. If this extension is supported, `cl_khr_dx9_media_sharing` will be present in `CL_PLATFORM_EXTENSIONS` or `CL_DEVICE_EXTENSIONS`.

```
cl_int_t clGetDeviceIDsFromDX9MediaAdapterKHR (
    cl_platform_id platform, cl_uint num_media_adapters,
    cl_dx9_media_adapter_type_khr *media_adapters_type,
    void *media_adapters,
    cl_dx9_media_adapter_set_khr media_adapter_set,
    cl_uint num_entries, cl_device_id *devices,
    cl_int *num_devices)
    media_adapter_type: CL_ADAPTER_{D3D9, D3D9EX, DXVA}_KHR
    media_adapter_set: CL_ALL_DEVICES_FOR_DX9_MEDIA_ADAPTER_KHR,
    CL_PREFERRED_DEVICES_FOR_DX9_MEDIA_ADAPTER_KHR
```

```
cl_mem_t clCreateFromDX9MediaSurfaceKHR (
    cl_context context, cl_mem_flags flags,
    cl_dx9_media_adapter_type_khr adapter_type,
    void *surface_info, cl_uint plane, cl_int *errcode_ret)
    flags: CL_MEM_{READ, WRITE}_ONLY, CL_MEM_READ_WRITE
    adapter_type: CL_ADAPTER_{D3D9, D3D9EX, DXVA}_KHR
```

```
cl_int_t clEnqueueAcquireDX9MediaSurfacesKHR (
    cl_command_queue command_queue,
    cl_uint num_objects, const cl_mem *mem_objects,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

```
cl_int_t clEnqueueReleaseDX9MediaSurfacesKHR (
    cl_command_queue command_queue,
    cl_uint num_objects, const cl_mem *mem_objects,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

```
cl_int_t clGetDeviceIDsFromD3D11KHR (
    cl_platform_id platform,
    cl_device_source_khr d3d_device_source,
    void *d3d_object,
    cl_device_source_khr d3d_device_set,
    cl_uint num_entries, cl_device_id *devices,
    cl_uint *num_devices)
```

```
d3d_device_source: CL_D3D11_DEVICE_KHR,
CL_D3D11_DXGI_ADAPTER_KHR
d3d_device_set: CL_PREFERRED_DEVICES_FOR_D3D11_KHR,
CL_ALL_DEVICES_FOR_D3D11_KHR
```

```
cl_mem_t clCreateFromD3D11BufferKHR (
    cl_context context, cl_mem_flags flags,
    ID3D11Buffer *resource, cl_int *errcode_ret)
    flags: CL_MEM_{READ, WRITE}_ONLY, CL_MEM_READ_WRITE
```

```
cl_mem_t clCreateFromD3D11Texture3DKHR (
    cl_context context, cl_mem_flags flags,
    ID3D11Texture3D *resource, cl_int *errcode_ret)
    flags: CL_MEM_{READ, WRITE}_ONLY, CL_MEM_READ_WRITE
```

```
cl_int_t clEnqueueAcquireD3D11ObjectsKHR (
    cl_command_queue command_queue,
    cl_uint num_objects, const cl_mem *mem_objects,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

```
cl_int_t clEnqueueReleaseD3D11ObjectsKHR (
    cl_command_queue command_queue,
    cl_uint num_objects, const cl_mem *mem_objects,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

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		clGetDeviceIDs	1	dot	4	mad	4	round	4
		clGetDeviceIDsFromD3D10KHR	7	DX9 Media Surface Sharing	7	mad_hi, mad_sat, mad24	3	Rounding modes	5
<b>C</b>		clGetDeviceIDsFromD3D11KHR	7	<b>E</b>		Map and Unmap Image Objects	6	rsqrt	4
cbrt	4	clGetDeviceIDsFromDX9Media...	7	erf, erfc	4	Map Buffer Objects	1	Runtime	1
ceil	4	clGetDeviceInfo	1	Event Objects	2	Markers, Barriers, Wait for Events	2	<b>S</b>	
clamp (Common)	5	clGetEventInfo	2	Execute Kernels	2	Math Constants	3	Sampler Declaration Fields	7
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Class Diagram	7	clGetExtensionFunctionAddress ForPlatform	1	EXTENSION	3	Math intrinsics options	2	sampler_t	7
cl_apple_gl_sharing	7	clGetGLContextInfoKHR	7	<b>F</b>		max (common)	5	Scalar Data Types (Built-in)	3
cl_khr_3d_image_writes	6	clGetGLObjectInfo	7	fabs	4	max (integer)	3	select	5
cl_khr_d3d10_sharing	7	clGetGLTextureInfo	7	fast_{distance, length, normalize}	4	MAXFLOAT	3	Separate Compilation, Linking	2
cl_khr_d3d11_sharing	7	clGetImageInfo	6	fdim	4	min (common)	5	shuffle, shuffle2	5
cl_khr_dx9_media_sharing	7	clGetKernelArgInfo	2	floor	4	min (integer)	3	sign	5
cl_khr_fp16	6	clGetKernelInfo	2	Flush and Finish	2	minmag	4	signbit	5
cl_khr_gl_sharing	7	clGetKernelWorkGroupInfo	2	fma	4	mix	5	sin, sincos, sinh, sinpi	4
cl_khr_int64_base_atomics	5	clGetMemObjectInfo	1	fmin, fmax	4	modf	4	smoothstep	5
cl_khr_int64_extended_atomics	5	clGetPlatformIDs	1	fmod	4	mul_hi, mul24	3	sqrt	4
clBuildProgram	2	clGetPlatformInfo	1	FP_CONTRACT	3	<b>N</b>		step	5
clCompileProgram	2	clGetProgramBuildInfo	2	FP_FAST_FMA*	3	NAN	3	sub_sat	3
clCreateBuffer	1	clGetProgramInfo	2	fract	4	nan	4	Synchronization, Explicit Memory Fence Functions	5
clCreateCommandQueue	1	clGetSamplerInfo	7	frexp	4	nextafter	4	<b>T</b>	
clCreateContext	1	clGetSupportedImageFormats	6	Function Qualifiers	3	normalize	4	tan, tanh, tanpi	4
clCreateContextFromType	1	clLinkProgram	2	<b>G</b>		<b>O</b>		tgamma	4
clCreateEventFromGLsyncKHR	7	clReleaseCommandQueue	1	Geometric Functions	4	OpenGL Sharing	7	trunc	4
clCreateFromD3D10*	7	clReleaseContext	1	get_array_size	7	Operators	3	Type Attributes	3
clCreateFromD3D11*	7	clReleaseDevice	1	get_global_{id, offset, size}	4	Optimization options	2	<b>U</b>	
clCreateFromDX9MediaSurface...	7	clReleaseEvent	2	get_group_id	4	Partitioning a Device	1	Unload OpenCL Compiler	2
clCreateFromGL*	7	clReleaseKernel	2	get_image_{width, height, depth}	7	Platform layer	1	upsample	3
clCreateImage	6	clReleaseMemObject	1	get_image_channel_data_type	7	popcount	3	<b>V</b>	
clCreateKernel	2	clReleaseProgram	2	get_image_channel_order	7	pow, pown, powr	4	vec_step	5
clCreateKernelsInProgram	2	clReleaseSampler	7	get_image_dim	7	prefetch	4	Vector Components	3
clCreateProgramWith {Binary, BuiltInKernels, Source}	2	clRetainCommandQueue	1	get_local_{id, size}	4	Preprocessor Directives, Macros	3	Vector Data Types (Built-in)	3
clCreateSampler	7	clRetainContext	1	get_num_groups	4	Preprocessor options	2	Vector Data Load/Store Functions	4
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clCreateSubDevices	1	clRetainEvent	2	GL Sharing	7	Program Objects	2	vload*	4
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clEnqueue(Acquire, Release) D3D11ObjectsKHR	7	clRetainProgram	2	HUGE_VAL, HUGE_VALF	3			wait_group_events	4
clEnqueue(Acquire, Release) DX9MediaSurfaceKHR	7	clRetainSampler	7	hypot	4			Warning request/suppress options	2
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clEnqueueCopyBuffer	1	clSetMemObjectDestructor Callback	1	Image Formats	6			write_mem_fence	5
clEnqueueCopyBufferRect	1	clSetUserEventStatus	2	Image Objects	6			__write_only	6
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