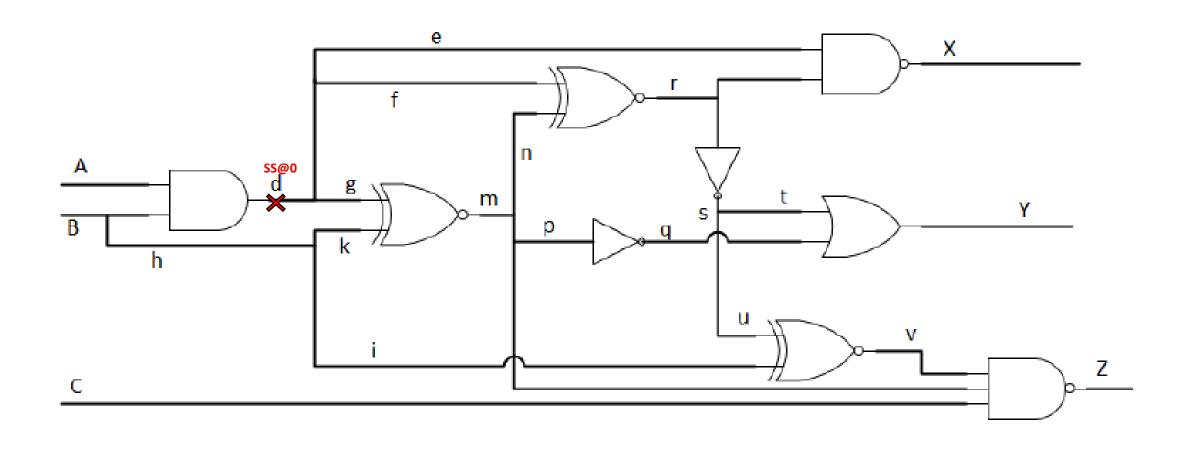


DALG d – SS@0

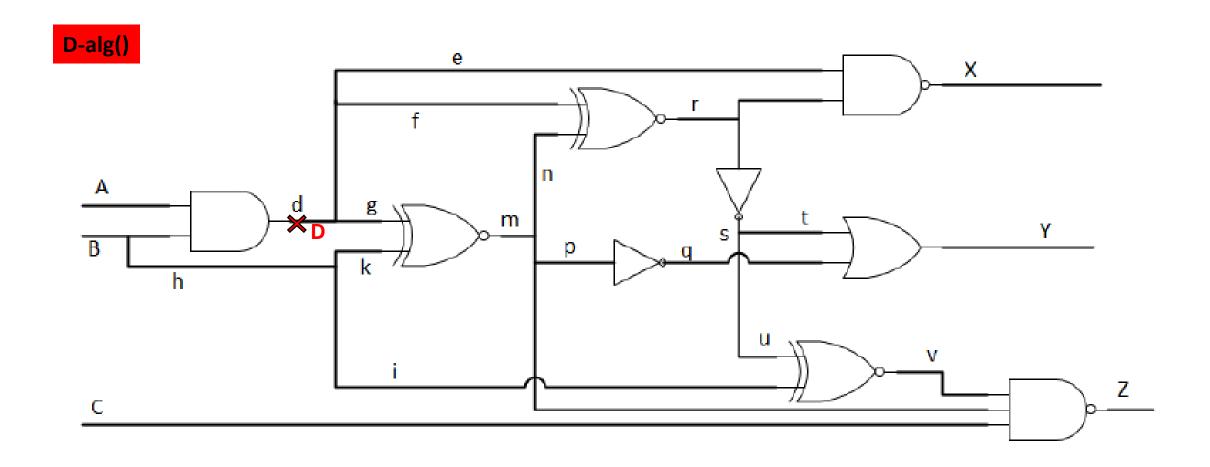
EE658 – Diagnosis and Design of Reliable Digital Systems Professor Tabar Fei Wu





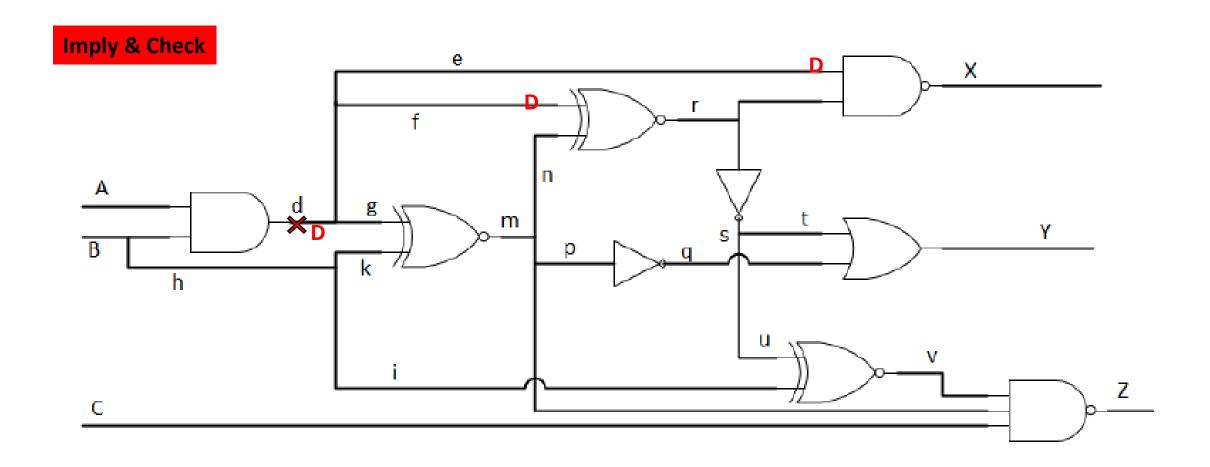
Note: Gate are shown using the letter representing their output. The content of D-frontier and J-frontier are gates, and not nodes.





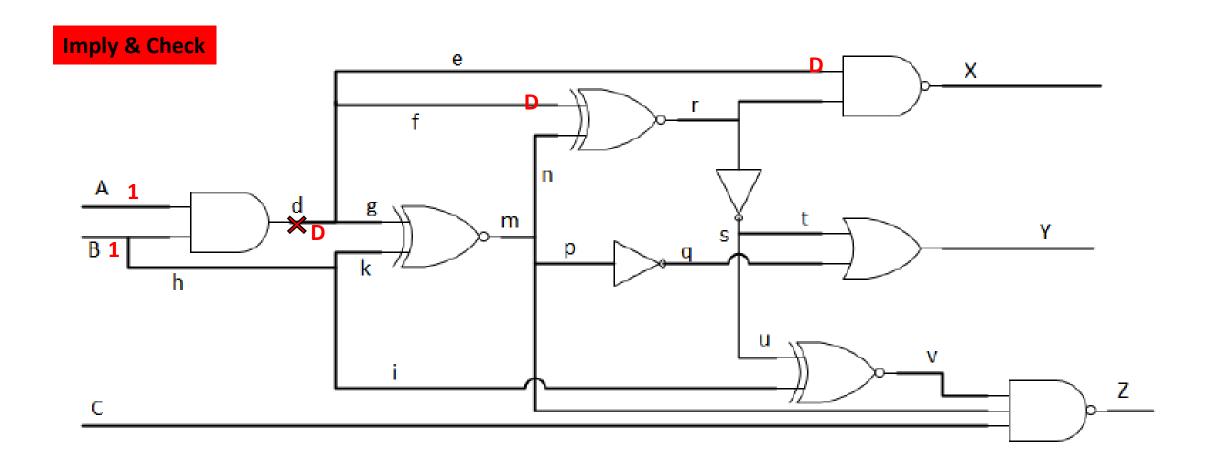
D-frontier: {}





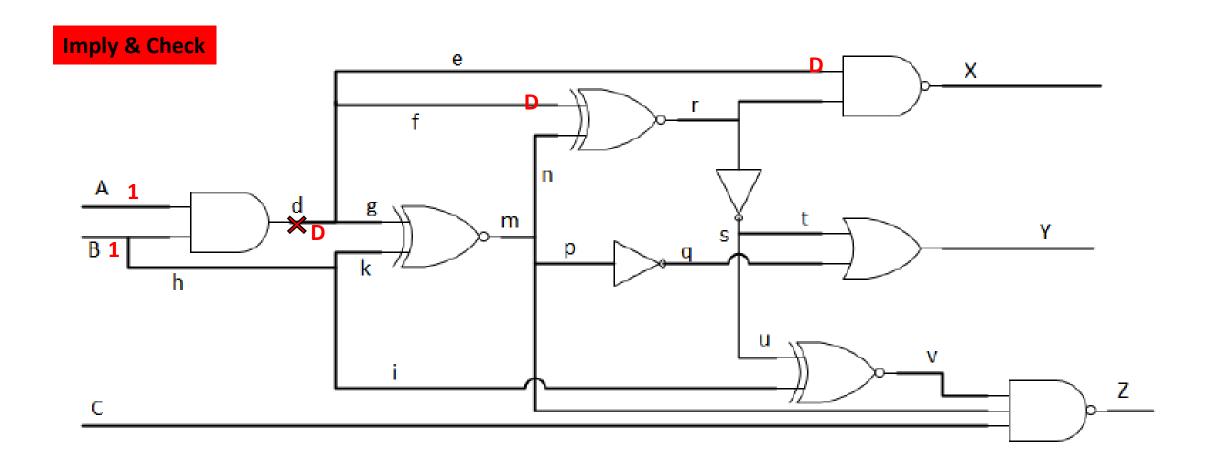
D-frontier: {m,r,x}





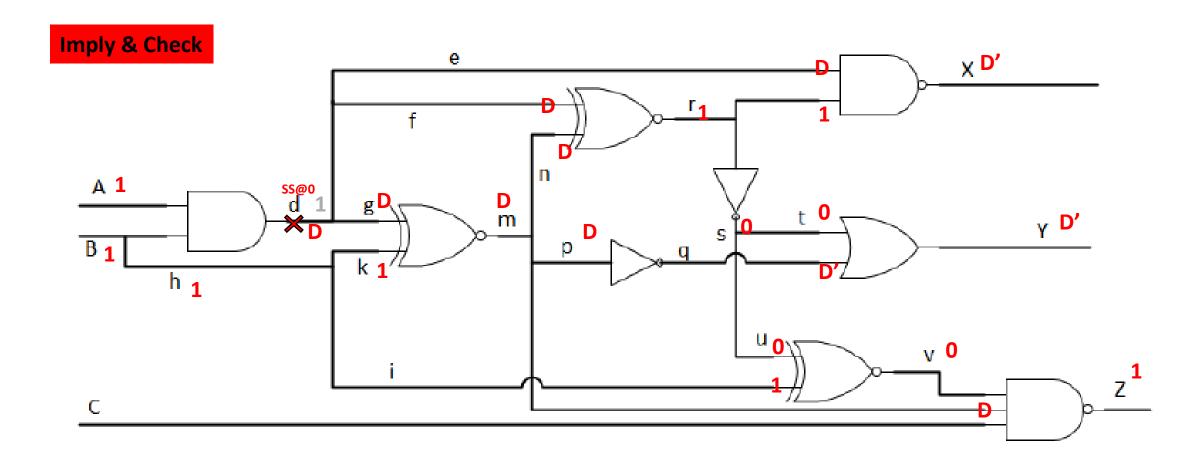
D-frontier: {m,r,x}





D-frontier: {m,r,x}





Note: error is at the output

SUCCESS

$$T = \{1, 1, X\}$$

