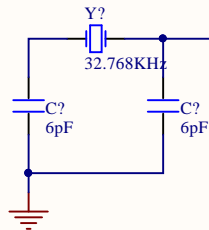


# MCU\_ABC



U?A		
WK_UP	PA0	PA0/WK_UP/TIM2_CH1/TIM2_ETR/TIM5_CH1/TIM8_ETR/U2_CTS/U4_TX/ETH_MII CRS/ADC123_IN0
RMII_REF_CLK	PA1	PA1/TIM2_CH2/TIM5_CH2/U2_RTS/U4_RX/ETH_MII_RX_CLK/ETH_RMII_REF_CLK/ADC123_IN1
USART2_TX	ETH_MDIO	PA2/TIM2_CH3/TIM5_CH3/TIM9_CH1/U2_TX/ETH_MDIO/ADC123_IN2
USART2_RX	PWM_DAC	PA3/TIM2_CH4/TIM5_CH4/TIM9_CH2/U2_RX/OTG_HS_ULPI_D0/ETH_MII_COL/LCD_B5/ADC123_IN3
GBC_LED	STM_DAC	PA4/SPI1_NSS/SPI3_NSS/I2S3_WS/U2_CK/OTG_HS_SOF/DCMI_HSYNC/LCD_VSYNC/ADC12_IN4/DAC_OUT1
	STM_ADC	PA5/TIM2_CH1/TIM2_ETR/TIM8_CH1N/SPI1_SCK/OTG_HS_ULPI_CK/ADC12_IN5/DAC_OUT2
	DCMI_PCLK	PA6/TIM1_BKIN/TIM3_CH1/TIM8_BKIN/SPI1_MISO/TIM13_CH1/DCMI_PIXCLK/LCD_G2/ADC12_IN6
	RMII_CRS_DV	PA7/TIM1_CH1N/TIM3_CH2/TIM8_CH1N/SPI1_MOSI/TIM14_CH1/ETH_MII_RX_DV/ETH_RMII_CRS_DV/ADC12_IN7
REMOTE_IN	DCMI_XCLK	PA8/MCO1/TIM1_CH1/I2C3_SCL/U1_CK/OTG_FS_SOF/LCD_R6
USART1_TX	PA9	PA9/TIM1_CH2/I2C3_SMBA/U1_TX/DCMI_D0/OTG_FS_VBUS
USART1_RX	PA10	PA10/TIM1_CH3/U1_RX/OTG_FS_ID/DCMI_D1
USB_D-	PA11	PA11/TIM1_CH4/U1_CTS/CAN1_RX/LCD_R4/OTG_FS_DM
USB_D+	PA12	PA12/TIM1_ETR/U1_RTS/CAN1_TX/LCD_R5/OTG_FS_DP
JTMS	PA13	PA13/JTMS/SWDIO
JTCK	PA14	PA14/JTCK/SWDCLK
DCMI_RESET	JTDI	PA15/JTDI/TIM2_CH1/TIM2_ETR/SPI1_NSS/SPI3_NSS/I2S3_WS
LED1	PB0	PB0/TIM1_CH2N/TIM3_CH3/TIM8_CH2N/LCD_R3/OTG_HS_ULPI_D1/ETH_MII_RXD2/ADC12_IN8
LED0	PB1	PB1/TIM1_CH3N/TIM3_CH4/TIM8_CH3N/LCD_R6/OTG_HS_ULPI_D2/ETH_MII_RXD3/ADC12_IN9
BOOT1	PB2	PB2/BOOT1
DCMI_SDA	JTDO	PB3/JTDO/TIM2_CH2/SPI1_SCK/SPI3_SCK/I2S3_CK
DCMI_SCL	JTRST	PB4/NJTRST/TIM3_CH1/SPI1_MISO/SPI3_MISO/I2S3ext_SD
	LCD_BL	PB5/TIM3_CH2/I2C1_SMBA/SPI1_MOSI/SPI3_MOSI/I2S3_SD/CAN2_RX/OTG_HS_ULPI_D7/ETH_PPS_OUT/FMC_SDCKE1/DCMI_D10
		PB6/TIM4_CH1/I2C1_SCL/U1_TX/CAN2_TX/FMC_SDNE1/DCMI_D5
		PB7/TIM4_CH2/I2C1_SDA/U1_RX/FMC_NL/DCMI_VSYNC
		PB8/TIM4_CH3/TIM10_CH1/I2C1_SCL/CAN1_RX/ETH_MII_TXD3/SDIO_D4/DCMI_D6/LCD_B6
		PB9/TIM4_CH4/TIM11_CH1/I2C1_SDA/SPI2_NSS/I2S2_WS/CAN1_TX/SDIO_D5/DCMI_D7/LCD_B7
USART3_TX		PB10/TIM2_CH3/I2C2_SCL/SPI2_SCK/I2S2_CK/U3_TX/OTG_HS_ULPI_D3/ETH_MII_RX_ER/LCD_G4
USART3_RX	RMII_TX_EN	PB11/TIM2_CH4/I2C2_SDA/U3_RX/OTG_HS_ULPI_D4/ETH_MII_TX_EN/ETH_RMII_TX_EN/LCD_G5
IWIRE_DQ	IIC_INT	PB12/TIM1_BKIN/I2C2_SMBA/SPI2_NSS/I2S2_WS/U3_CK/CAN2_RX/OTG_HS_ULPI_D5/ETH_MII_TXD0/ETH_RMII_TXD0/OTG_HS_ID
	SPI2_SCK	PB13/TIM1_CH1N/SPI2_SCK/I2S2_CK/U3_CTS/CAN2_TX/OTG_HS_ULPI_D6/ETH_MII_TXD1/ETH_RMII_TXD1/OTG_HS_VBUS
	SPI2_MISO	PB14/TIM1_CH2N/TIM8_CH2N/SPI2_MISO/I2S2ext_SD/U3_RTS/TIM12_CH1/OTG_HS_DM
	SPI2_MOSI	PB15/RTC_REFIN/TIM1_CH3N/TIM8_CH3N/SPI2_MOSI/I2S2_SD/TIM12_CH2/OTG_HS_DP
FMC_SDNWE	PC0	PC0/OTG_HS_ULPI_STP/FMC_SDNWE/ADC123_IN10
ETH_MDC	PC1	PC1/ETH_MDC/ADC123_IN11
FMC_SDNE0	PC2	PC2/SPI2_MISO/I2S2ext_SD/OTG_HS_ULPI_DIR/ETH_MII_TXD2/FMC_SDNE0/ADC123_IN12
FMC_SDCKE0	PC3	PC3/SPI2_MOSI/I2S2_SD/OTG_HS_ULPI_NXT/ETH_MII_TX_CLK/FMC_SDCKE0/ADC123_IN13
RMII_RXD0	PC4	PC4/ETH_MII_RXD0/ETH_RMII_RXD0/ADC12_IN14
RMII_RXD1	PC5	PC5/ETH_MII_RXD1/ETH_RMII_RXD1/ADC12_IN15
DCMI_D0	PC6	PC6/TIM3_CH1/TIM8_CH1/I2S2_MCK/U6_TX/SDIO_D6/DCMI_D0/LCD_HSYNC
DCMI_D1	PC7	PC7/TIM3_CH2/TIM8_CH2/I2S3_MCK/U6_RX/SDIO_D7/DCMI_D1/LCD_G6
SDIO_D0	DCMI_D2	PC8/TIM3_CH3/TIM8_CH3/U6_CK/SDIO_D0/DCMI_D2
SDIO_D1	DCMI_D3	PC9/MCO2/TIM3_CH4/TIM8_CH4/I2C3_SDA/I2S_CKIN/SDIO_D1/DCMI_D3
SDIO_D2		PC10/SPI3_SCK/I2S3_CK/U3_TX/U4_TX/SDIO_D2/DCMI_D8/LCD_R2
SDIO_D3	DCMI_D4	PC11/I2S3ext_SD/SPI3_MISO/U3_RX/U4_RX/SDIO_D3/DCMI_D4
SDIO_SCK		PC12/SPI3_MOSI/I2S3_SD/U3_CK/U5_TX/SDIO_CK/DCMI_D9
	KEY2	PC13/TAMP_1
		PC14/OSC32_IN
		PC15/OSC32_OUT

STM32F429IGT6

Title		
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