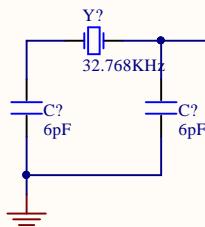


# MCU\_ABC

WK_UP	PA0	U?A
RMIIDREF CLR PA1		PA0/WK_UP/TIM2_CH1/TIM2_ETR/TIM5_CH1/TIM8_ETR/U2_CTS/U4_TX/ETH_MII_CRS/ADC123_IN0
USART2_TX	ETH_MDIÖ PA2	PA1/TIM2_CH2/TIM5_CH2/U2_RTS/U4_RX/ETH_MII_RX_CLK/ETH_RMII_REF_CLK/ADC123_IN1
USART2_RX	PWM_DAC PA3	PA2/TIM2_CH3/TIM5_CH3/TIM9_CH1/U2_TX/ETH_MDIÖ/ADC123_IN2
GBC_LED	STM_DAC PA4	PA3/TIM2_CH4/TIM5_CH4/TIM9_CH2/U2_RX/OTG_HS_ULPI_D0/ETH_MII_COL/LCD_B5/ADC123_IN3
	STM_ADC PA5	PA4/SPI1_NSS/SPI3_NSS/I2S3_WS/U2_CK/OTG_HS_SOF/DCMI_HSYNC/LCD_VSYNC/ADC12_IN4/DAC_OUT1
	DCMI_PCLK PA6	PA5/TIM2_CH1/TIM2_ETR/TIM8_CH1/SPI1_SCK/OTG_HS_ULPI_CLK/ADC12_IN5/DAC_OUT2
	RMIICRS DV PA7	PA6/TIM1_BKIN/TIM3_CH1/TIM8_BKIN/SPI1_MISO/TIM13_CH1/DCMI_PIXCLK/LCD_G2/ADC12_IN6
REMOTE_IN	DCMI_XCLK PA8	PA7/TIM1_CH1N/TIM3_CH2/TIM8_CH1N/SPI1_MOSI/TIM14_CH1/ETH_MII_RX_DV/ETH_RMII_CRS_DV/ADC12_IN7
	USART1_TX PA9	PA8/MCO1/TIM1_CH1/I2C3_SCL/U1_CK/OTG_FS_SOF/LCD_R6
	USART1_RX PA10	PA9/TIM1_CH2/I2C3_SMBA/U1_TX/DCMI_D0/OTG_FS_VBUS
	USB_D- PA11	PA10/TIM1_CH3/U1_RX/OTG_FS_ID/DCMI_D1
	USB_D+ PA12	PA11/TIM1_CH4/U1_CTS/CAN1_RX/LCD_R4/OTG_FS_DM
	JTMS PA13	PA12/TIM1_ETR/U1_RTS/CAN1_TX/LCD_R5/OTG_FS_DP
	JTCK PA14	PA13/JTMS/SWDIO
	DCMI_RESET JTDX PA15	PA14/JTCK/SWDCLK
		PA15/JTDI/TIM2_CH1/TIM2_ETR/SPI1_NSS/SPI3_NSS/I2S3_WS
	LED1 PB0	
	LED0 PB1	
	BOOT1 PB2	
DCMI_SDA	JTDO PB3	PB0/TIM1_CH2N/TIM3_CH3/TIM8_CH2N/LCD_R3/OTG_HS_ULPI_D1/ETH_MII_RXD2/ADC12_IN8
DCMI_SCL	JTRST PB4	PB1/TIM1_CH3N/TIM3_CH4/TIM8_CH3N/LCD_R6/OTG_HS_ULPI_D2/ETH_MII_RXD3/ADC12_IN9
	LCD_BL PB5	PB2/BOOT1
	PB6	PB3/JTDO/TIM2_CH2/SPI1_SCK/SPI3_SCK/I2S3_CK
	DCMI_VSYNC PB7	PB4/JTRST/TIM3_CH1/SPI1_MISO/SPI3_MISO/I2S3ext_SD
	DCMI_D6 PB8	PB5/TIM3_CH2/I2C1_SMBA/SPI1_MOSI/SPI3_MOSI/I2S3_SD/CAN2_RX/OTG_HS_ULPI_D7/ETH_PPS_OUT/FMC_SDCKE1/DCMI_D10
	DCMI_D7 PB9	PB6/TIM4_CH1/I2C1_SCL/U1_TX/CAN2_TX/FMC_SDNE1/DCMI_D5
USART3_TX	PB10	PB7/TIM4_CH2/I2C1_SDA/U1_RX/FMC_NL/DCMI_VSYNC
USART3_RX	RMIIDTX_EN PB11	PB8/TIM4_CH3/TIM10_CH1/I2C1_SCL/CAN1_RX/ETH_MII_TXD3/SDIO_D4/DCMI_D6/LCD_B6
IWIRE_DQ	IIC_INT PB12	PB9/TIM4_CH4/TIM11_CH1/I2C1_SDA/SPI2_NSS/I2S2_WS/CAN1_TX/SDIO_D5/DCMI_D7/LCD_B7
	SPDIF_SCK PB13	PB10/TIM2_CH3/I2C2_SCL/SPI2_SCK/I2S2_CK/U3_TX/OTG_HS_ULPI_D3/ETH_MII_RX_ER/LCD_G4
	SPDIF_MISO PB14	PB11/TIM2_CH4/I2C2_SDA/U3_RX/OTG_HS_ULPI_D4/ETH_MII_TX_EN/ETH_RMII_TX_EN/LCD_G5
	SPDIF_MOSI PB15	PB12/TIM1_BKIN/I2C2_SMBA/SPI2_NSS/I2S2_WS/U3_CK/CAN2_RX/OTG_HS_ULPI_D5/ETH_MII_TXD0/ETH_RMII_RXD0/OTG_HS_ID
	FMC_SDNWE PC0	PB13/TIM1_CH1N/SPI2_SCK/I2S2_CK/U3_CTS/CAN2_TX/OTG_HS_ULPI_D6/ETH_MII_TXD1/ETH_RMII_RXD1/OTG_HS_VBUS
	ETH_MDC PC1	PB14/TIM1_CH2N/TIM8_CH2N/SPI2_MISO/I2S2ext_SD/U3_RTS/TIM12_CH1/OTG_HS_DM
	FMC_SDNE0 PC2	PB15/RTC_REFIN/TIM1_CH3N/TIM8_CH3N/SPI2_MOSI/I2S2_SD/TIM12_CH2/OTG_HS_DP
	FMC_SDCKE0 PC3	
	RMIIDRXD0 PC4	
	RMIIDRXD1 PC5	
	DCMI_D0 PC6	
	DCMI_D1 PC7	
	SDIO_D0 DCMID2 PC8	PC0/OTG_HS_HS_ULPI_STP/FMC_SDNWE/ADC123_IN10
	SDIO_D1 DCMID3 PC9	PC1/ETH_MDC/ADC123_IN11
	SDIO_D2 PC10	PC2/SPI2_MISO/I2S2ext_SD/OTG_HS_ULPI_DIR/ETH_MII_TXD2/FMC_SDNE0/ADC123_IN12
	SDIO_D3 DCMID4 PC11	PC3/SPI2_MOSI/I2S2_SD/OTG_HS_ULPI_NXT/ETH_MII_TX_CLK/FMC_SDCKE0/ADC123_IN13
	SDIO_SCK PC12	PC4/ETH_MII_RXD0/ETH_RMII_RXD0/ADC12_IN14
	KEY2 PC13	PC5/ETH_MII_RXD1/ETH_RMII_RXD1/ADC12_IN15
	PC14	PC6/TIM3_CH1/TIM8_CH1/I2S2_MCK/U6_TX/SDIO_D6/DCMI_D0/LCD_HSYNC
	PC15	PC7/TIM3_CH2/TIM8_CH2/I2S3_MCK/U6_RX/SDIO_D7/DCMI_D1/LCD_G6
		PC8/TIM3_CH3/TIM8_CH4/I2C3_SDA/I2S_CKIN/SDIO_D1/DCMI_D3
		PC9/MCO2/TIM3_CH4/TIM8_CH4/I2C3_SDA/I2S_CKIN/SDIO_D1/DCMI_D3
		PC10/SPI3_SCK/I2S3_CK/U3_TX/U4_RX/SDIO_D2/DCMI_D8/LCD_R2
		PC11/I2S3ext_SD/SPI3_MISO/U3_RX/U4_RX/SDIO_D3/DCMI_D4
		PC12/SPI3_MOSI/I2S3_SD/U3_CK/U5_TX/SDIO_CLK/DCMI_D9
		PC13/TAMP_1
		PC14/OSC32_IN
		PC15/OSC32_OUT

STM32F429IGT6



Title

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