TOSHIBA 2SK30ATM

TOSHIBA FIELD EFFECT TRANSISTOR SILICON N CHANNEL JUNCTION TYPE

2 S K 3 0 A T M

LOW NOISE PRE-AMPLIFIER, TONE CONTROL AMPLIFIER AND DC-AC HIGH INPUT IMPEDANCE AMPLIFIER CIRCUIT APPLICATIONS

High Breakdown Voltage : V_{GDS} = −50V

• High Input Impedance : $I_{GSS} = -1nA \text{ (Max.) (V}_{GS} = -30V)$

• Low Noise : NF=0.5dB (Typ.)

 $(V_{DS}=15V, V_{GS}=0, R_{G}=100k\Omega,$

f = 120Hz

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Gate-Drain Voltage	v_{GDS}	-50	V
Gate Current	$I_{\mathbf{G}}$	10	mA
Drain Power Dissipation	P_{D}	100	mW
Junction Temperature	T_{j}	125	°C
Storage Temperature Range	$T_{ m stg}$	-55~125	°C

1. SOURCE 2. GATE 3. DRAIN JEDEC TO-92 EIAJ SC-43 TOSHIBA 2-5F1C

Unit in mm

Weight: 0.21g (Typ.)

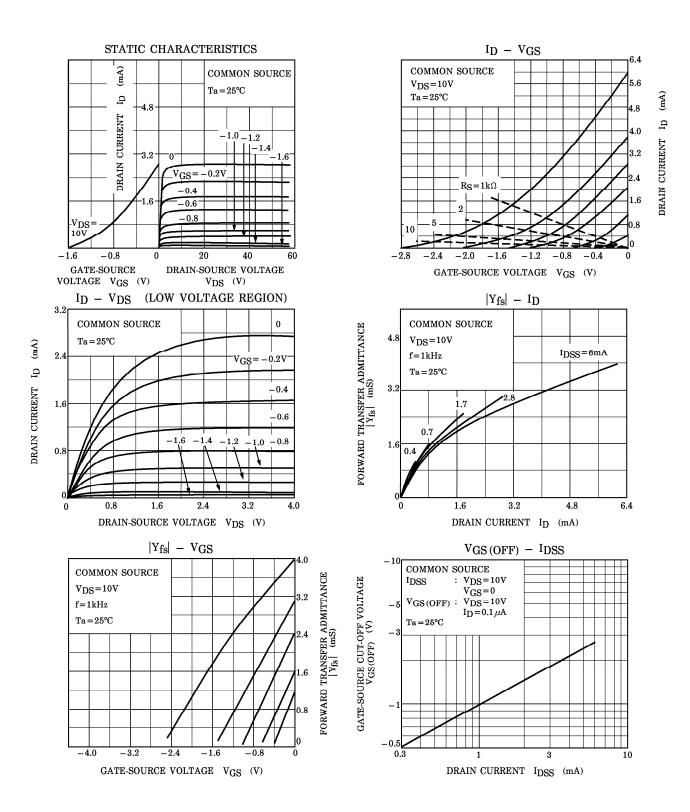
ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Gate Cut-off Current	IGSS	$V_{GS} = -30V, V_{DS} = 0$			-1.0	nA
Gate-Drain Breakdown Voltage	V _(BR) GDS	$V_{DS} = 0, I_G = -100 \mu A$	-50	_	_	V
Drain Current	I _{DSS} (Note)	$V_{DS} = 10V, V_{GS} = 0$	0.3	_	6.5	mA
Gate-Source Cut-off Voltage	V _{GS} (OFF)	$V_{DS} = 10V, I_D = 0.1 \mu A$	-0.4	_	-5.0	V
Forward Transfer Admittance	Y _{fs}	$V_{DS} = 10V, V_{GS} = 0, f = 1kHz$	1.2	_	_	mS
Input Capacitance	$\mathrm{c}_{\mathrm{iss}}$	$V_{GS} = 0, V_{DS} = 0, f = 1MHz$	ı	8.2	_	pF
Reverse Transfer Capacitance	$\mathrm{C}_{\mathrm{rss}}$	$V_{\rm GD} = -10 \text{V}, \ V_{ m DS} = 0, \ { m f} = 1 { m MHz}$	_	2.6		pF
Noise Figure	NF	$V_{DS} = 15V, V_{GS} = 0$ $R_G = 100 \text{k}\Omega, f = 120 \text{Hz}$		0.5	5.0	dB

Note: I_{DSS} Classification R: 0.30~0.75, 0: 0.60~1.40, Y: 1.20~3.00, GR: 2.60~6.50

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