

Value Mutation Testing for SMT Solvers

DYLAN WOLFF*, ETH Zürich, Switzerland

We propose *Value Mutation Testing*, a novel approach for testing SMT solvers. The core idea behind this technique is to generate test cases by mutating the constant values within a given seed SMT formula. We further develop a strategy, *Boolean Expression Coercion* (BEC), for systematically choosing value mutations. BEC extracts value mutations from the model of a separate SMT solver call that leverages constraints derived from the original seed formula. We realized Value Mutation Testing via BEC in a prototype tool: ValFuzz. In a preliminary evaluation with ValFuzz, we have already uncovered and confirmed 11 new bugs in the comprehensively tested, state-of-the-art SMT solvers, Z3 and CVC4. Developers of these solvers greatly appreciated our bug reports. We further demonstrate with ValFuzz, using a set of known bugs, that BEC is an effective method for picking value mutations. In the future, we aim at a more thorough evaluation of Value Mutation Testing to understand its full potential.

1 INTRODUCTION

Satisfiability Modulo Theory (SMT) solvers evaluate the satisfiability of first-order logic formulas augmented with functions from various additional theories. They are key components in tools with critical security implications such as program verifiers [2, 8, 13]. Thus errors in the underlying solvers for commercial [1] and open source [12] projects dependent on these tools can have serious potential consequences. This is true especially for hard-to-find *soundness* bugs, where the SMT solver silently outputs an incorrect answer. Recent work has also demonstrated that SMT solvers are less reliable than previously thought [9, 15, 16]. Furthermore, because SMT formulas are a highly structured input format, it is difficult to test these solvers via classic automated testing techniques [5]. Consequently, finding and fixing bugs in SMT solvers is both a challenging open research question and of vital importance.

We observed that many SMT solver bugs are triggered only by a specific set of constant values in the triggering formula. For example, consider Figure 1 which shows a soundness bug in Z3:

<pre>(declare-funs a b () String) (assert (and (= (str.++ (str.substr "1" 0 (str.len a)) "0") b) (< (str.to.int b) 11))) (check-sat)</pre>	<pre>(declare-funs a b () String) (assert (and (= (str.++ (str.substr "1" 0 (str.len a)) "0") b)) (< (str.to.int b) 0))) (check-sat)</pre>
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Fig. 1. Left: An SMT-LIB formula. Right: Mutating the highlighted constant triggers a bug in Z3 (#4153).¹

When we mutate a constant value in the correctly handled formula in the left of Figure 1 to produce the formula on the right, Z3 outputs “sat” for this new formula, rather than the correct answer “unsat”, indicating a critical *soundness* bug. Thus we propose that this transformation, a *Value Mutation*, can be used to generate many syntactically valid test cases that may trigger bugs in SMT solvers from a corpus of non-buggy seed files. By performing only type preserving mutations, we circumvent the challenge posed by the structured nature of the input language.

In practice, however, finding a value mutation of the formula in the left of Figure 1 that triggers this bug from the search space of possible value mutations is non-trivial. Using randomized value mutations, we were unable to reproduce this bug in practice from the structure of the non-triggering seed. While utilizing branch or code coverage as feedback for choosing new inputs in automated testing has been an effective and popular approach [7, 11, 14], in the context of SMT solvers, Winterer

*Advised by Zhendong Su

¹We extend the SMT-LIB syntax here with the **declare-funs** command to abbreviate multiple function declarations

et. al. uncovered many critical bugs in Z3 and CVC4 without any significant corresponding increase in code coverage [15]. This observation lead us to develop a novel approach for *choosing* value mutations: *Boolean Expression Coercion* (BEC), in which we use constraints derived from the original formula and an additional call to an SMT solver to pick value mutations that are more likely to trigger divergent, buggy behavior in the solver under test. Indeed, our refined implementation of value mutation via BEC, was able to reproduce the bug shown in Figure 1 in practice.

To evaluate our approach of Value Mutation Testing using Boolean Expression Coercion, we implemented ValFuzz, a differential value mutation fuzzer for SMT-LIB formulas and their solvers [10].² Using ValFuzz, we identified a new soundness issue in Z3 and 11 other bugs between Z3 and CVC4 in three months of preliminary testing [3, 6].

2 APPROACH

Our approach consists of Value Mutation Testing via Boolean Expression Coercion.

Definition 2.1 describes our core test-case generation technique, *Value Mutation*, more precisely:

DEFINITION 2.1. We define a **Value Mutation** for a given seed formula ϕ with n constant values $c_1 \dots c_n$ as a transformation replacing each constant c_k in ϕ with a constant value c'_k of the same sort such that $\phi \neq \phi[c'_1 \dots c'_n / c_1 \dots c_n]$.

In Value Mutation Testing, we find bugs by performing value mutations on a corpus of seed files and then differentially testing SMT Solvers using the resulting mutant formulas as inputs.

2.1 Picking Value Mutations With Boolean Expression Coercion

For Boolean Expression Coercion, we leverage constraints derived from the boolean sub-expressions of the seed formula, along with an additional call to an SMT solver to obtain our value mutations. By coercing the individual sub-expressions to be *true* or *false*, and allowing the model from this intermediate SMT solver call to dictate which constant values will be used for a given formula, we explore values that we will later show are empirically more likely trigger buggy behavior in the solvers under test.

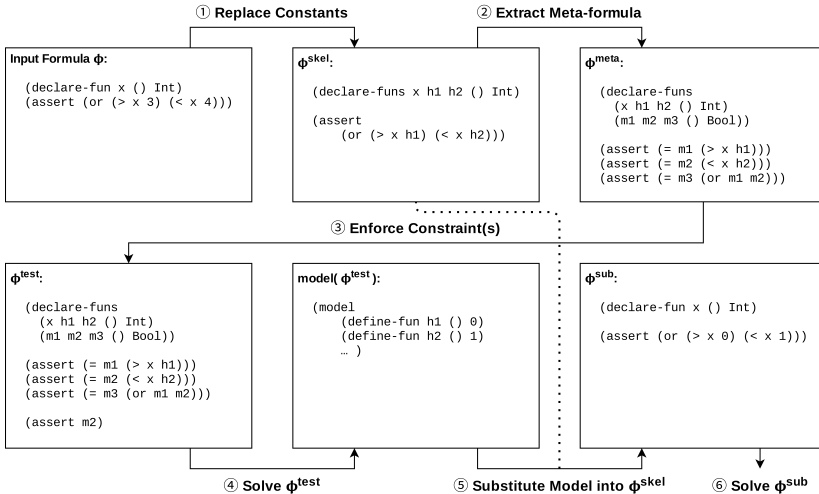


Fig. 2. A Single Iteration of Boolean Expression Coercion for an Input Formula ϕ

²Differential testing involves testing different versions or configurations of solvers against each other to find discrepancies

We demonstrate this approach through Figure 2 in steps ① through ⑥. Starting with a seed formula ϕ , we first replace each constant value with fresh variables, in this case h_1 and h_2 , representing holes in the formula giving us the *skeleton* of ϕ (step ①). We then create a *meta-formula*, by inserting expressions that monitor the truth values of the boolean expressions with set of fresh variables, $m_1 \dots m_3$ (②). Next, we enforce one or more such boolean expressions, here $(x < h_2)$, to be either *true* or *false* (③). We pass the resulting formula, ϕ^{test} , to an SMT solver (④). If the formula is satisfiable, we take the values given for the variables representing constant holes in the model, and substitute them back into the skeleton of the original formula (⑤). This gives us a new formula, ϕ^{sub} , which we can use to exercise the SMT solvers under test (⑥). If the generated ϕ^{test} is unsatisfiable, we enforce a different constraint and attempt to generate a new value mutation. We repeat this process until each boolean sub-expression has been enforced to be both *true* and *false*.

Additional Refinements. We also have begun investigating refinements of BEC for choosing Value Mutations. One refinement that has shown promise is adding monitor expressions for variables and sub-expressions of non-boolean types, dictating whether or not they fall within a given abstract domain. For example, we might include `(assert (= m4 (< x 0)))` in step ③ of Figure 2 and later enforce that constraint to ensure that the integer variable x is negative.

3 EVALUATION AND CONCLUSIONS

RQ1 - Is value mutation an effective technique for testing SMT solvers? We exercised ValFuzz for three months in parallel with its development on the trunk versions of state-of-the-art SMT solvers Z3 and CVC4. We utilized the SMT-LIB Benchmarks [4], in addition to the test suites for these solvers as seed files. ValFuzz uncovered four bugs in CVC4 and seven bugs in Z3, including one critical soundness bug, all of which were fixed by developers. Project developers responded with positive feedback for our efforts, including the following:

“Nice catch, I was able to make some major improvements to the regex algorithm and model construction with this one. Thanks for the report!”

(Z3 #4271)

RQ2 - Does BEC improve the effectiveness of value mutation testing? We hand-picked bug triggering formulas from the issue trackers of CVC4 and Z3 that contained constant values and used these formulas as seed files for a secondary evaluation. We tracked the quantity and class of each bug found by ValFuzz and a baseline randomized value mutation fuzzer, as well as an efficiency measure.³ In this evaluation, we found that basic BEC was relatively highly efficient at triggering hard-to-find, critical soundness bugs, finding $2.02 \pm 0.20 (\mu \pm \sigma)$ bugs per 1000 solver calls compared to the baseline of only 0.61 ± 0.14 bugs per 1000 solver calls. This translated to a modest, but still statistically significant, increase in the absolute number of bugs found as well.⁴ For other kinds of bugs, BEC remained more efficient, but found slightly fewer bugs overall. With additional refinements, such as adding abstract domain constraints, BEC outperforms the baseline in all aspects, finding approximately twice as many soundness bugs while maintaining a high level of efficiency.

Conclusions: We have introduced Value Mutation Testing via Boolean Expression Coercion and shown it can be effective at finding real-world issues in state-of-the-art SMT Solvers. Looking forwards, we aim to further explore BEC and other value mutation strategies to fully realize the potential of this technique.

³We use $\text{EFFICIENCY} = \frac{\text{BUGS FOUND}}{1000 \text{ SMT SOLVER INVOCATIONS}}$ for our efficiency metric, as calls to the solvers dominate execution time

⁴Statistical significance determined by a single-tailed Welch’s t-test at $p < 0.05$

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