



f _s	10 MHz
T	100 ns
T _{sample_high}	16.7 ns
T _{sample_low}	83.3 ns
f _{clk}	120 MHz
T _{clk}	8.34 ns

feasible to read data at this rate

likely way to fast for GPIO

f _s	1 MHz
T	1000 ns
T _{sample_high}	167 ns
T _{sample_low}	833 ns
f _{clk}	12 MHz
T _{clk}	83.4 ns

feasible