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DOT MATRIX LIQUID CRYSTAL DISPLAY MODULE

HJ1602A Serial

USER' MANUAL

PROPO	SED BY	APPROVED
Design	Approved	

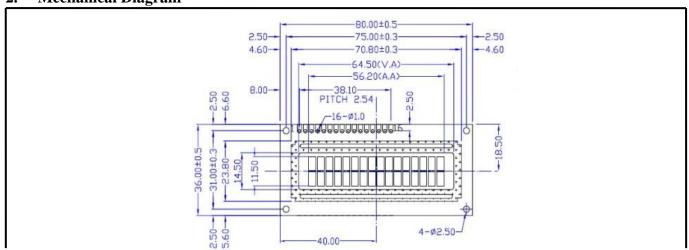
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1. Mechanical Specification

ITEM	STANDARD VA	LUE	UNIT
NUMBER OF CHARACTERS	16 CHARACTERS X		
CHARACTER FORMAT	5 X 8 DOTS		
MODULE DIMENSION	80.0 (W) X 36.0 (H) X 13.0 (T)		mm
VIEWING DISPLAY AREA	64.5 (W) X 14.5		mm
ACTIVE DISPLAY AREA	56.20 (W) X 11.50		mm
CHARACTER SIZE	2.96 (W) X 5.56		mm
CHARACTER PITCH	3.55 (W) X 5.94	(H)	mm
DOT SIZE	0.56 (W) X 0.60		mm
DOT PITCH	$0.60 (W) \times 0.70$	(H)	mm
EL Use Inverter Type			
Inverter Input			
Inverter Output			
Backlight Half-Lift Time	•		
LED Backlight Color	Yellov	v Green	'
Backlight Input	DC +4.2V V	100	mA
Backlight Half-Lift Time	50,000		HR.
E Mode LED Backlight Color	·		·
Backlight Input	V		mA
Backlight Half-Lift Time			HR.

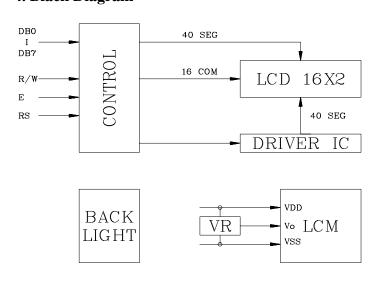
2. Mechanical Diagram



3. Interface Pin Connections

NO	SYMBOL	LEVEL	FUNCTION
1	VSS	-	GND (0V)
2	VDD	H/L	DC +5V
3	VO	H/L	Contrast Adjust
4	RS	H/L	Register select
5	R/W	H/L	Read/Write
6	E	H,H→L	Enable signal
7	DB0	H/L	Data Bit 0
8	DB1	H/L	Data Bit 1
9	DB2	H/L	Data Bit 2
10	DB3	H/L	Data Bit 3
11	DB4	H/L	Data Bit 4
12	DB5	H/L	Data Bit 5
13	DB6	H/L	Data Bit 6
14	DB7	H/L	Data Bit 7
15	A+ (EL1)		A (EL Backlight 1)
16	K- (EL2)		K (EL Backlight 2)

4. Black Diagram



5. Absolute Maximum Ratings

ITEM	SYMBOL	MIN.	TYPE	MAX.	UNIT
OPERATING TEMPERATURE	TOP	0/-20		+50/+70	$^{\circ}\!\mathbb{C}$
STORAGE TEMPERATURE	TST	TST -10/-30		+60/+80	$^{\circ}\!\mathbb{C}$
INPUT VOLAGE	VI	VSS		VDD	V
SUPPLY VOLTAGE FOR LOGIC	VDD-VSS		3.3		V
SUPPLY VOLTAGE FOR LCD	VDD-VO				V
STATIC ELECTRICITY	Be sure that you ar	e grounded when	n handing LCM		

6. Electrical Characteristics

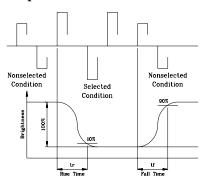
ITEM	SYN	CONDITION	MIN.	TYPE	MAX.	UNIT
SUPPLY VOLTAGE FOR LOGIC	VDD-VSS			3.3		V
		Ta= 0/-20 °C				V
SUPPLY VOLTAGE FOR LCD	VDD-VO	Ta= 25°C				V
		Ta= +50/+70 °C				V
INPUT HIGH VOLTAGE	VIH		2.2		VDD	V
INPUT LOW VOLTAGE	VIL		0		0.6	V
OUTPUT HIGH VOLTAGE	VOH		2.4			V
OUTPUT LOW VOLTAGE	VOL				0.4	V
SUPPLY CURRENT	IDD	VDD=+5V		3.0	4.5	mA

7. Optical Characteristics

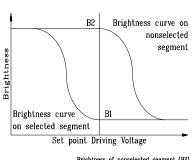
ITEM	SYM	CONDITION	MIN.	TYPE	MAX.	UNIT
VIEW ANGLE (V)	θ	CR≧2	-10		40	deg.
VIEW ANGLE (H)	φ	CR≧2	-30		30	deg.
CONTRAST RATIO	CR			5		
RESPONSE TIME	TON			180	230	mS
RESPONSE TIME	TOFF			100	150	mS

8. Optical Definitions

Response Time

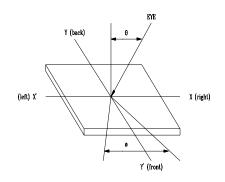


Contrast Ration



Contrast Ration (K) = $\frac{Brightness}{Brightness}$ of nonselected segment (B2)

View Angle



9. Display Address

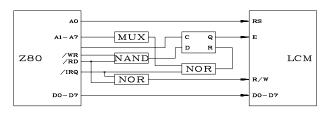
Line 4

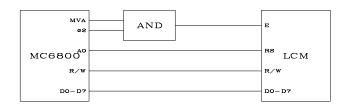
	•																			
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Line 1	00	01	02	03	04	05	06	07	08	09	0A	0B	0C	0D	0E	0F				
Line 2	40	41	42	43	44	45	46	47	48	49	4A	4B	4C	4D	4E	4F				
Line 3																				
Line 4																				
	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Line 1																				
Line 2																				
Line 3																				

10. Interface to MPU

10.1 Interface to Z-80 CPU

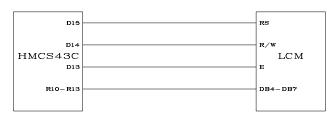
10.2 Interface to MC6800 CPU

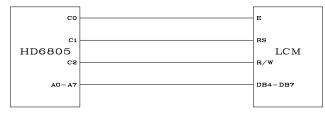




10.3 Interface to 4-bit CPU (HMCS43C)

10.4 Interface to HD6805 MP



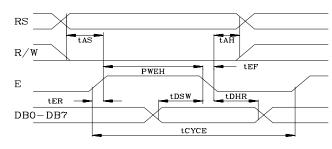


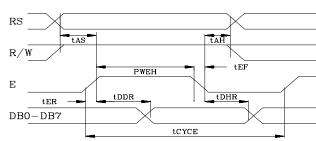
11. Timing Control

11.1 Write and Read Operation

Write Operation

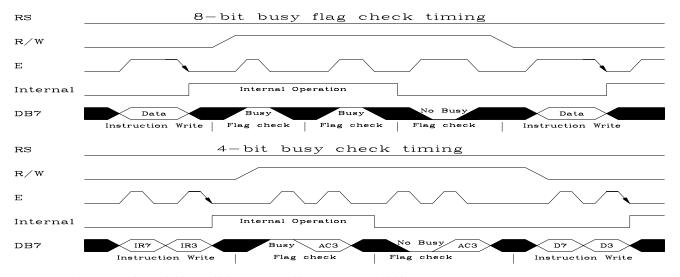
Read Operation





Item	Symbol	Limit (Min.)	Limit (Max.)	Unit
Enable Cycle Time	tCYCE	1000	-	ns
Enable Pules Width (High level)	PWEH	450	-	ns
Enable Rise/Fall Time	tER,tEF		25	ns
Address Set-Up Time (RS,R/W,E)	tAS	100	-	ns
Address Hole Time	tAH	10	-	ns
Data Set-Up Time	tDSW	100		ns
Data Delay Time	tDDR		190	ns
Data Hold Time	tDHR	20		ns

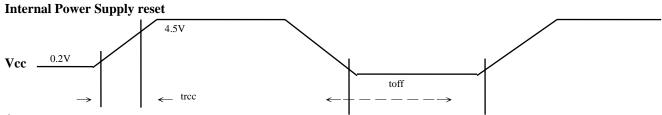
11.2 Busy flag check timing



Note: IR7, IR3: Instruction 7th bit, 3rd bit; AC3: Address Counter 3rd bit.

12. Initialization of LCM

The LCM automatically initializes (reset) when power is turned on using the internal reset circuit. If the power supply conditions for correctly operating of the internal reset circuit are not met, initialization by instruction is required. Use the procedure is next page for initialization.



(Note 1) 10 ms \geq trcc \geq 0.1 ms, toff \geq 1 ms.

(Note 2) toff stipulates the time of power OFF for momentary power supply dip or when power supply cycles ON and OFF.

Item	Symbol	Test condition	Limit (Min.)	Limit (Max.)		Unit							
Power supply rise time	tree		0.1	10		ms							
Power supply off time	toff		1			ms							
1) 8 Bit	Interface	2) 4	2) 4 Bit Interface										
Powe	er On	<u> </u>	Power On										
Wait 15 ms or more a	ifter V _{DD} reaches 4.5V	Wait 15 ms or 1	nore after V _{DD} reaches	4.5V	İ								
Busy flag can't be checked. Function set: 8 bits													
RS R/W DB7 DB6 DB5 DB4		0	RS R/W	DB7 DB6	DB5	DB4							
0 0 0 0 1 1	* * * *		0 0	0 0	1	1							
Wait 4.1 r	ms or more		Wai	it 4.1 ms or more									
	Busy flag can't be checked. Function set: 8 bits												
RS E/W DB7 DB6 DB5 DB4		0	RS R/W	DB7 DB6	DB5	DB4							
0 0 0 0 1 1	* * * *		0 0	0 0	1	1							
Wait 100	μs or more		Wai	t 100 μs or more									
	Bus	y flag can't be checked. Function set: 8 bits	before this instruc	etion									
RS R/W DB7 DB6 DB5 DB4		0	RS R/W	DB7 DB6	DB5	DB4							
	* * * *		0 0	0 0	1	1							
		Function set: 8 bits											
	Innal pnal pnal pn	<u> </u>	RS R/W	DB7 DB6	DB5	DB4							
RS R/W DB7 DB6 DB5 DB4 0 0 0 0 1 1	DB3 DB2 DB1 DB N F * *	Function Set	0 0	0 0	1 1	0							
		- Tunction Set		N F	*	*							
0 0 0 0 0 0	1 0 0 0	Display Off	0 0	0 0 1	0	0							
0 0 0 0 0 0	0 0 0 1	Display Clear		0 0	0	0							
			0 0	0 0	0	1							
	0 1 I/D S	Entry Mode Se	et 0 0	$\begin{bmatrix} 0 & 0 \\ 0 & 1 \end{bmatrix}$	0	0							

Busy flag is checked after instructions are completed. If busy flay isn't checked, the waiting time between
instructions should be longer than execution time of these instructions.

End of

Initialization

End of

Initialization

13. Instruction Set

FUNCTION	R S	R /W	D B	D B	D B	D B	D B	D B	D B	D B	DESCRIPTION	EXECU. TIME*
FUNCTION	3	/ ••	В 7	В 6	5	B 4	3	2	В 1	0	DESCRIPTION	(MAX.)
Clear Display	0	0	0	0	0	0	0	0	0	1	Clears entire display and returns the cursor to home	1.64ms
											position (address 0).	
Return Home	0	0	0	0	0	0	0	0	1	X	Return the cursor to the home position. Also returns the	1.64ms
											display being shifted to the original position. DD RAM	
											contents remain unchanged.	
Entry mode									_		Set cursor move direct and specifies display shift. These	$40 \mu\mathrm{s}$
set								١.	I		operations are performed during data rite/read. For	
	0	0	0	0	0	0	0	1	/	S	normal operation, set S to zero. I/D=1: increment;	
									D		0 :decrement ;S=1 : accompanies display shift when	
D: 1						_		_			data is written, for normal operation, set to zero.	40
Display ON/OFF			0	0	0	_	1	_			Set ON/OFF all display(D), cursor ON/OFF(C), and	$40\mu\mathrm{s}$
ON/OFF control	0	0	0	0	0	0	1	D	С	В	blink of cursor position character(B). D=1: ON display; 0:OFF display. C=1: ON cursor;0: OFF cursor. B=1:	
Control											ON blink cursor; 0: OFF blink cursor.	
Cursor or							S	R			Move the cursor and shift the display without changing	40 μ s
Display	0	0	0	0	0	1	<i>3</i>	K	x	v	DD RAM contents. S/C=1: Display shift; 0:Cursor	40 μ s
shift	0		"	"	"	1	Ć	Ĺ	Λ.	^	move. R/L=1: shift to right; 0: shift to left.	
Function							_	_			Set the interface data length (DL). Number of display	40 μ s
Set	0	0	0	0	1	D	N	F	X	X	lines (N) and character font (F). DL=1: 8 bits; 0:4 bits.	- 1
						L					N=1: 2 lines; 0: 1 lines. F=1: 5x10 dots; 0: 5x7 dots.	
Set CG RAM	0	0	0	1			A(CG			Set CG RAM address. CG RAM data is sent and	$40 \mu\mathrm{s}$
address											received after this setting.	
Set DD RAM	0	0	1			1	ADI)			Set DD RAM address. DD RAM data is sent and	$40 \mu\mathrm{s}$
address											received after this setting	
Read busy											Reads Busy Flag (BF) indicating internal operation is	$1 \mu s$
flag &	0	1	В				AC				being performed and reads address counter contents.	
address			F								BF=1: internally operating. 0: can accept instruction	
Write Data to	1	0			WF	RITE	DA	TA			Write data into DD RAM or CG RAM.	$40 \mu\mathrm{s}$
CG/DDRAM												
Read Data for					TA			Read data from DD RAM or CG RAM	$40 \mu\mathrm{s}$			
CG/DDRAM												

14. User Font Patterns (CG RAM Character)

Character Code (DD RAM data)	CG RAM Address	Character Pattern (CG RAM data)						
Hi 76543210 Lo	543 210	Hi 765 4 3 2 1 0 Lo						
	000	x x x 1 1 1 1 0						
	0 0 1	x x x 1 0 0 0 1						
	010	x x x 1 0 0 0 1						
$0\ 0\ 0\ 0\ x\ 0\ 0\ 0$	000 011	x x x 1 1 1 1 0						
	100	x x x 1 0 1 0 0						
	101	x x x 1 0 0 1 0						
	110	x x x 1 0 0 0 1						
	111	x x x 0 0 0 0 0						
	000	x x x 1 0 0 0 1						
	0 0 1	x x x 0 1 0 1 0						
	010	x x x 1 1 1 1 1						
0 0 0 0 x 0 0 1	001 011	x x x 0 0 1 0 0						
	100	x x x 1 1 1 1 1						
	1 0 1	x x x 0 0 1 1 0						
	110	x x x 0 0 1 0 0						
	111	xxx 0 0 0 0 0						
	000							
	0 0 1							
	010							
0 0 0 0 x 1 1 1	111 011							
	100							
	1 0 1							
	110							
	111							

15. Software Example

15.1 8-bit operation (8 bits 2 lines)

Function								D			Display	Description
	S	w	7	6	5	4	3	2	1	0		
Power on delay												Initialization. No display appears.
Function set	0	0	0	0	1	1	0	0	X	X		Sets to 8-bit operation and selects 2-line display and 5x7 dots character font. (Note: number of display lines and character fonts cannot be chang after this.)
Display OFF	0	0	0	0	0	0	1	0	0	0		Turn off display.
Display ON	0	0	0	0	0	0	1	1	1	0	_	Turn on display and cursor
Entry Mode Set	0	0	0	0	0	0	0	1	1	0	_	Set mode to increment the address by one and to shift the cursor to the right, at the time of write, to the DD/CG RAM Display is not shifted.
Write data to CG/DD RAM	1	0	0	1	0	1	0	0	1	1	S_	Write "S". Cursor incremented by one and shift to right.
Write data to CG/DD RAM	1	0	0	1	0	0 0 0	0	1	0 0 1	1	SDEC_	Write "D", "E", and "C".
Set DD RAM			_	_	0	0	0	0	0	0	SDEC	Set RAM address so that the cursor is propositioned at the head of the second line.
Write data to CG/DD RAM		•	•	•	*				•		SDEC CR_	Write "C", and "R".
Cursor or display shift	0	0	0	0	0	1	0	0	X	X	SDEC CR	Shift only the cursor position to the left.
Write data to CG/DD RAM					*						SDEC CO., LTD	Write "O., LTD." .
Entry Mode Set	0	0	0	0	0	0	0	1	1	1	SDEC CO., LTD	Set display mode shift at the time during writing operation.
Write data to CG/DD RAM	1	0	0	1	1	1	1	0	0	0	DEC O., LTD. x_	Write "x". Cursor incremented by one and shift to right. (The display move to left.)
Write data to CG/DD RAM					*							Write other characters.
Return Home	0	0	0	0	0	0	0	0	1	0	SDEC CO., LTD.	Return both display and cursor to the original position (Set address to zero).

15.2 4-bit operation (4-bit, 1 line)

Function	RS	R/ W	D7	D6	D5	D4	Display	Description
power on delay								initialization. No display appears.
Function set	0	0	0	0	1	0		Sets to 4-bit operation. In this case, operation is handled as 8-bits by initialization, and only this instruction completes with one write.
Function set	0	0	0	0	1 x	0 x		Sets 4-bit operation and selects 1-line display and 5x7 dot character font on and resetting is needed. (number of display lines and character fonts cannot be changed hence after).
Display ON/OFF Control	0 0	0	0 1	0 1	0 1	0	_	Turn on display and cursor.
Entry Mode Set	0	0	0	0 1	0 1	0	_	Set mode to incremented the address by one and to shift the cursor to the right, at the time of write. to the DD/CG RAM display is not shifted.
Write data to CG/DD RAM	1 1	0	0	1 0	0 1	1 1	S_	Write "S". Cursor incremented by one and shift to right.
		·	, and the second	•	, and the second	S	ame as 8-bit op	eration

16. Reliability Condition

			TN	Гуре	STN Type			
			Normal Temp.					
Viewing	Horizontal Φ)	±30°	±30°	±30°	±30°		
Angle	Vertical ⊖(m	1)	10° to 30°	10° to 30°	-10° to 40°	-10° to 40°		
Operating	g Temperature		-10 to 70°C	-25 to 80°C	*-20 to 70°C			
Storage	Temperature		-20 to 80°C	-35 to 90°C	-20 to 70°C	*-30 to 80°C		
High Temper	rature (Power Of	f)	240 Hours	240 Hours	240 Hours	240 Hours		
			@70°C	@90°C	@65°C	@75°C		
Low Temper	rature (Power Off	(240 Hours	240 Hours	240 Hours	240 Hours		
			@-20°C	@-35°C	@-15°C	@-25°C		
High Temper	rature (Power On	1)	240 Hours	240 Hours	240 Hours	240 Hours		
			@70°C	@80°C	@60°C	@70°C		
Low Temper	rature (Power On)	240 Hours	240 Hours	240 Hours	240 Hours		
			@-10°C	@-25°C	@-10°C	@-20°C		
High Temp	perature & High		55°C/90%RH	75℃/90%RH	45°C/90%RH	65°C/90%RH		
H	umidity		240 Hours	240 Hours 240 Hours 240 Hours		240 Hours		
Thermal Shock	<u>C</u>	A	60min@-20°C	60min@-35°C	60min@-20°C	60min@-30°C		
5 Cycle	В	В	5min@25°℃	5min@25°℃	5min@25°℃	5min@25°℃		
		C	60min@70°C	60min@90°℃	60min@70°C	60min@80°C		
Exp	ected Lift		50,000 Hours	50,000 Hours	50,000 Hours	50,000 Hours		

^{*}Wide temp. version may not available for some products, Please consult our sales engineer or respresentative.

17. Functional Test & Inspection Criteria

17.1 Sample plan

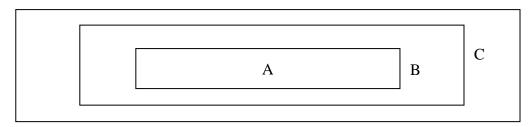
Sample plan according to MIL-STD-105D level 2, and acceptance/rejection criteria is.

Base on: Major defect: AQL 0.65 Minor defect: AQL 2.5

17.2 Inspection condition

Viewing distance for cosmetic inspection is 30cm with bare eyes, and under an environment of 800 lus (20W) light intensity. All direction for inspecting the sample should be within 45° against perpendicular line.

17.3 Definition of Inspection Zone in LCD



Zone A: Character / Digit area

Zone B: Viewing area except Zone A (Zone A + Zone B = minimum Viewing area)

Zone C: Outside viewing area (invisible area after assembly in customer's product)

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product.

17.4 Major Defect

All functional defects such as open (or missing segment), short, contrast differential, excess power consumption, smearing, leakage, etc. and overall outline dimension beyond the drawing. Are classified as major defects.

17.5 Minor Defect Except the Major defects above, all cosmetic defects are classified as minor defects.

Item No.	Item to be Inspected		Insp		Classification of		
							defects
1.	Spot defect	Zone siz	ze (mm)	A	cceptable (Q ty	Minor
	(Defects in spot			A	В	С	
	from)	$\Phi \leq$	0.15		ptable	Accepta-	
					ng of spot	ble	
		0.4 7			owed)		
		0.15≦ ₫		1	2		
		0.20≦ ₫		0	1		
		Φ>	0.25	0	0		
		Remarks:	for dark/w	hite spot, s	size Φ is	defined as	
			$\Phi = 1/2(X +$	$\cdot Y)$			
2.	Line defect		Size (mm)		Accepta	able Qty	Minor
	(Defects in line	L	V	V	Zo	ne	
	form)	Length	Wi	dth	A B	С	
		Accep-	$W \le$	0.02	Accep-	Accep-	
		table			table	table	
		L≦3.0		0.03	2		
		L>2.5	W≦	0.03	0		
		L≦3.0	0.03 <w< td=""><td><i>I</i>≦0.05</td><td>2</td><td></td><td></td></w<>	<i>I</i> ≦0.05	2		
		L>2.5	0.03 < W	$I \leq 0.05$	0		
			W>	0.05	Counted	d as spot	
					defect (
					item 1		
		l .		_	ct and line		
				ceed four.		. –	2.51
3.	Orientation defect	Not allov	ved inside	_	ea (Zone A	A or Zone	Minor
	(such as			B)			
	misalignment of L/C)						
4.	Polarizing	17.5.4.1 P	olarizer Po	sition			Minor
		l	_		d not excee	d the	
			outline dir				
					e viewing a	rea due to	
			ng is not a		(71	- /	
					ent on Glas		
		l		Reflector, Bu	ıbble betwe	CII	
		Size		ı	cceptable ()tv	
		JIZC ((11111)		Zone	(· y	
				A	B	С	
		⊕ <	0.20		ptable	Accep-	
		0.20< Φ		ļ	3	table	
		0.20< Φ			2		
			$\frac{7 = 1.00}{1.00}$		0		
		Ψ>	1.00	<u>'</u>	<u> </u>		

СНА	ARA	СТЕІ	R PA	TTE	RN C	CHAI	RT (5	×7 D	OTS	+CU	RSO	R)	
Higher 4 bit Lower 4 bit	0000	0010	0011	0100	0101	0110	0111	1010	1011	1100	1101	1110	1111
XXXX0000	CG RAM (1)		Ø	a	P	*	P			9	₹.	œ	9
XXXX0001	(2)	ļ	1	A	Q	a	9		7	チ	4	ä	ŋ
XXXX0010	(3)		2	В	R	Ь	r	Γ	1	ij	×	ß	Θ
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