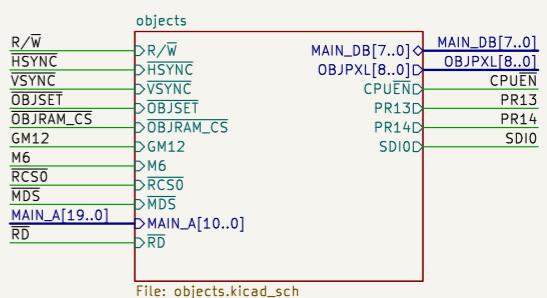
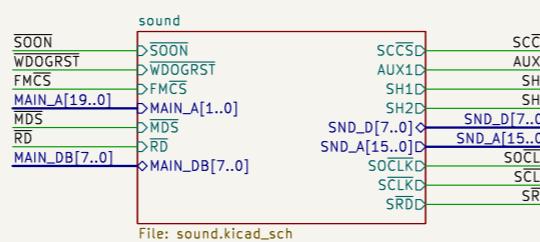
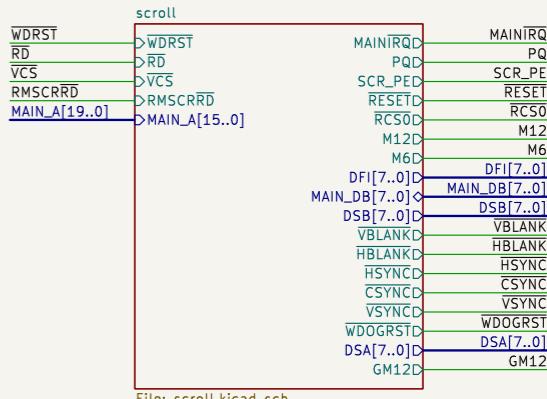
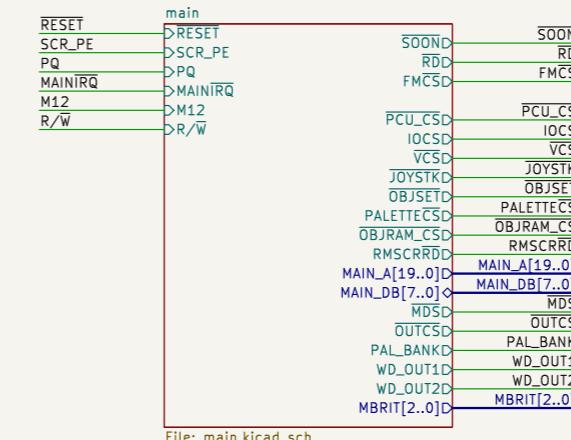
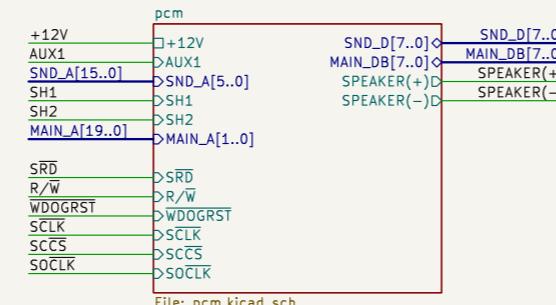
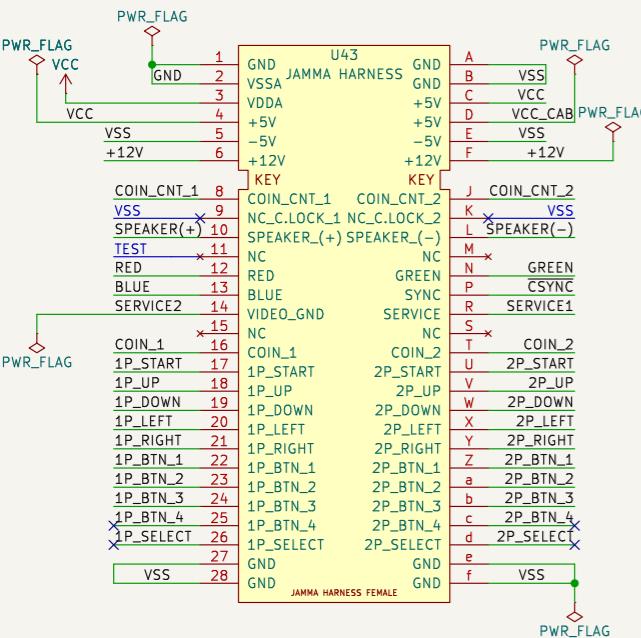


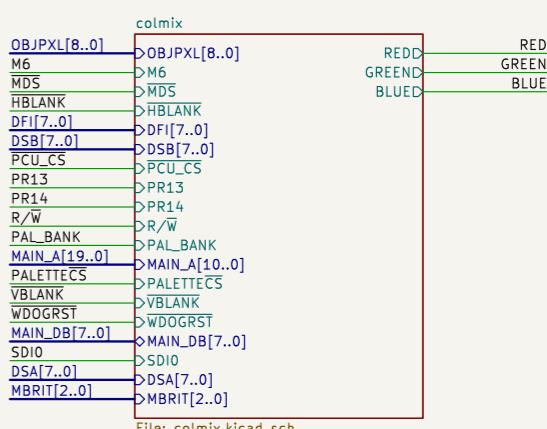
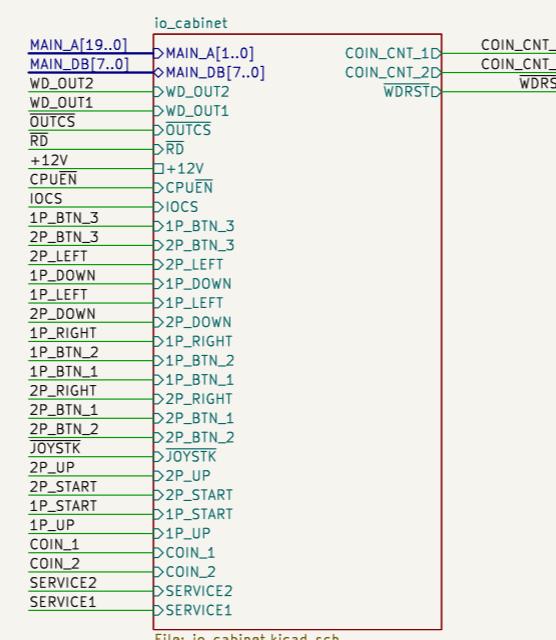
A



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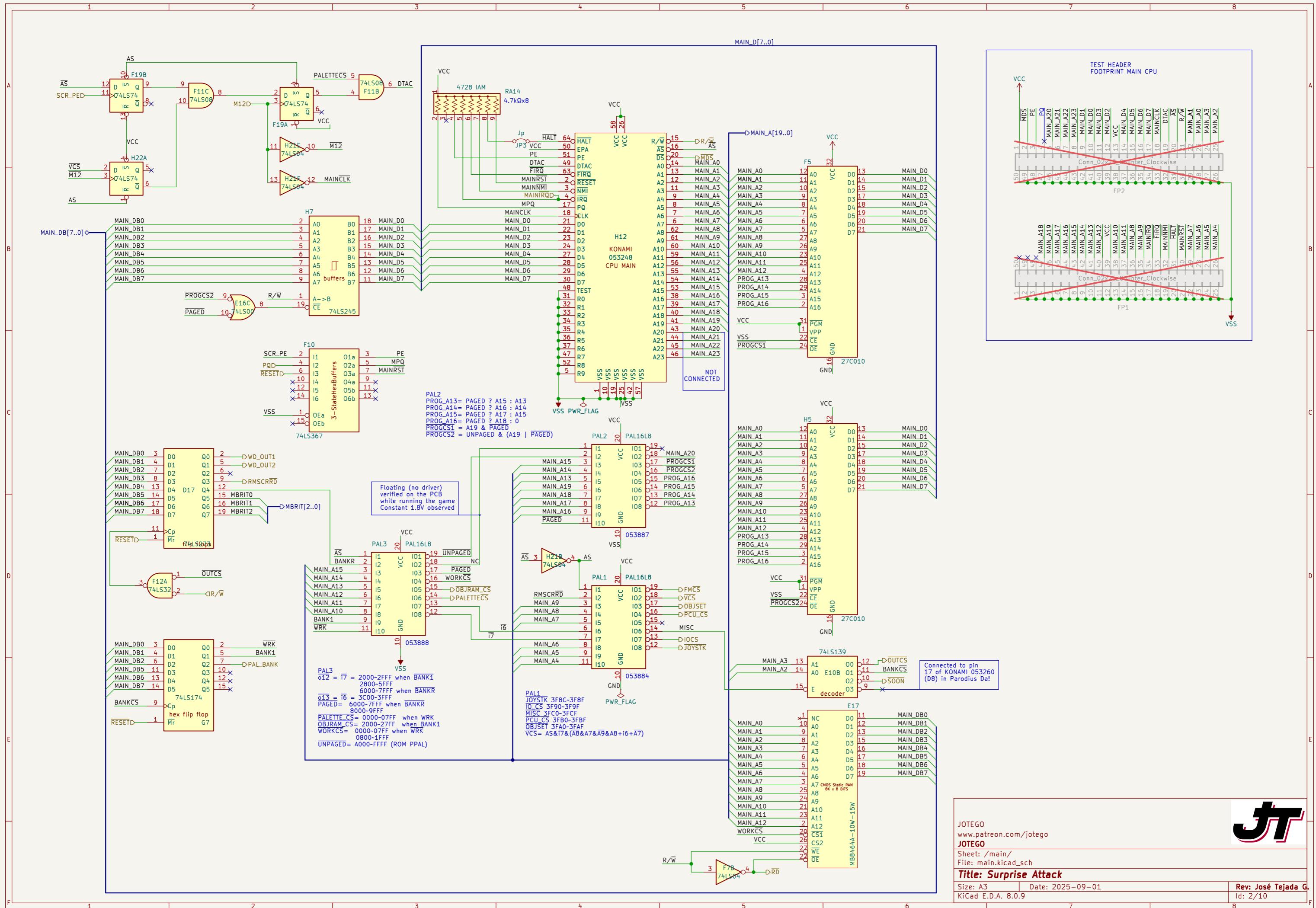


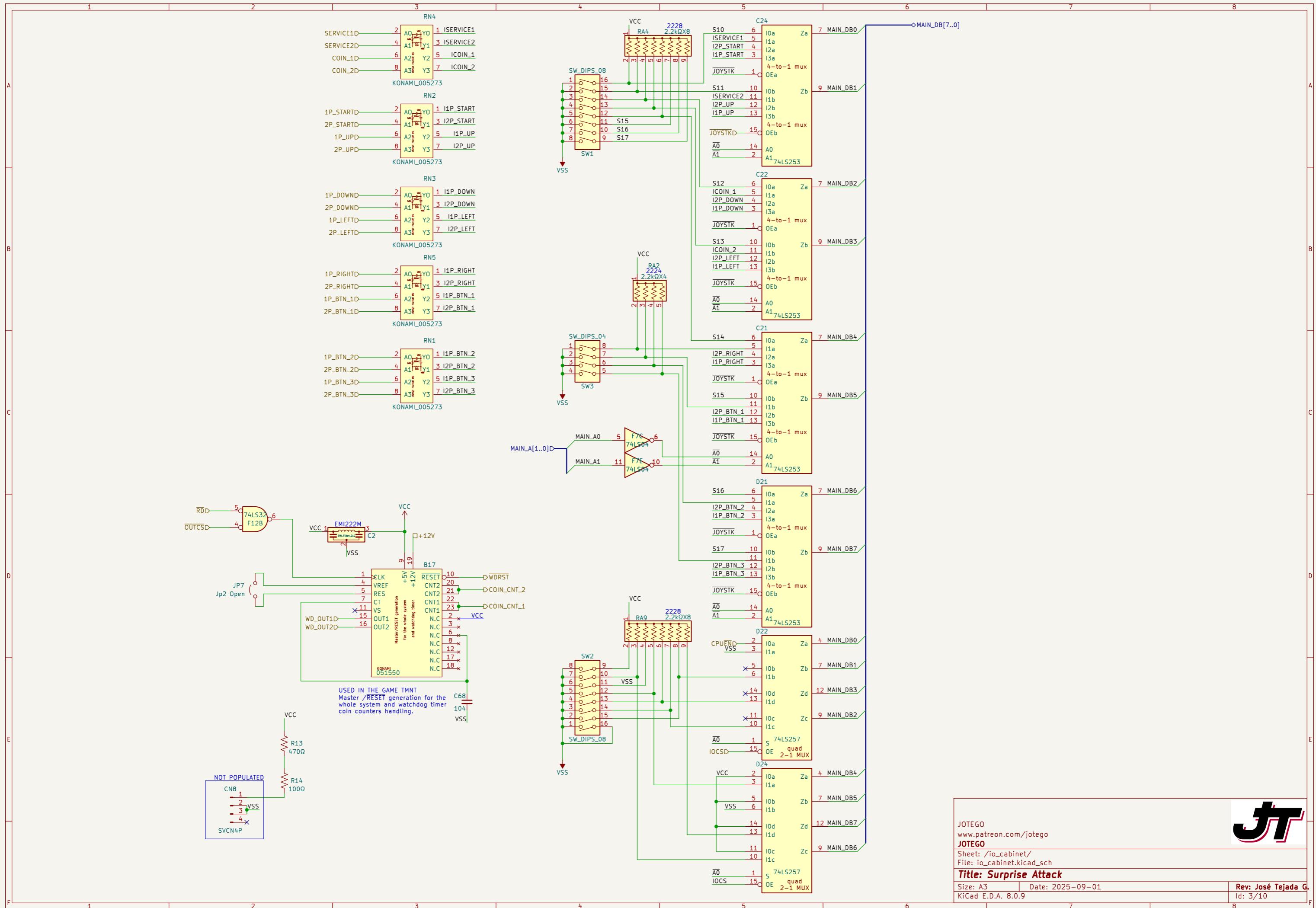
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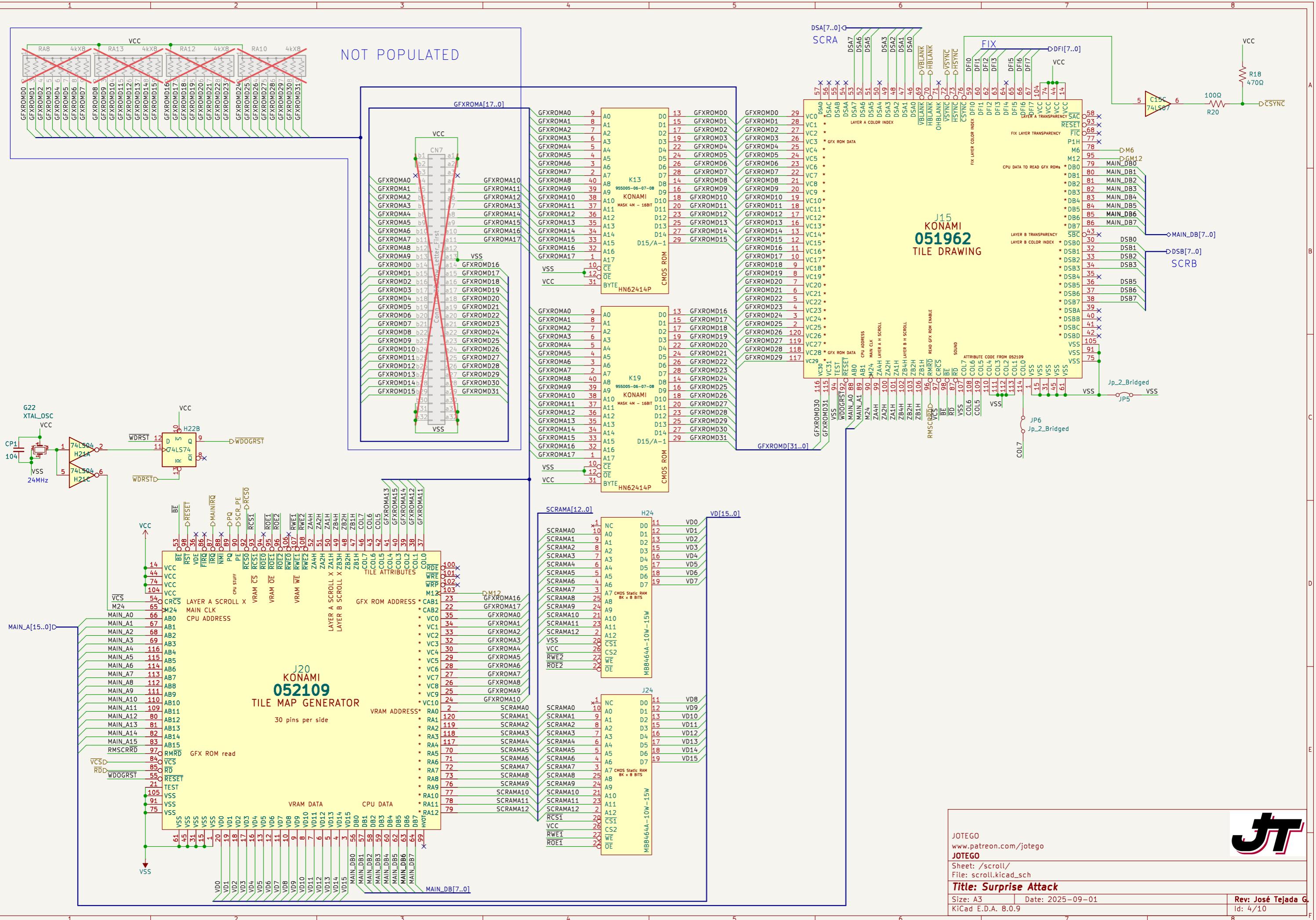
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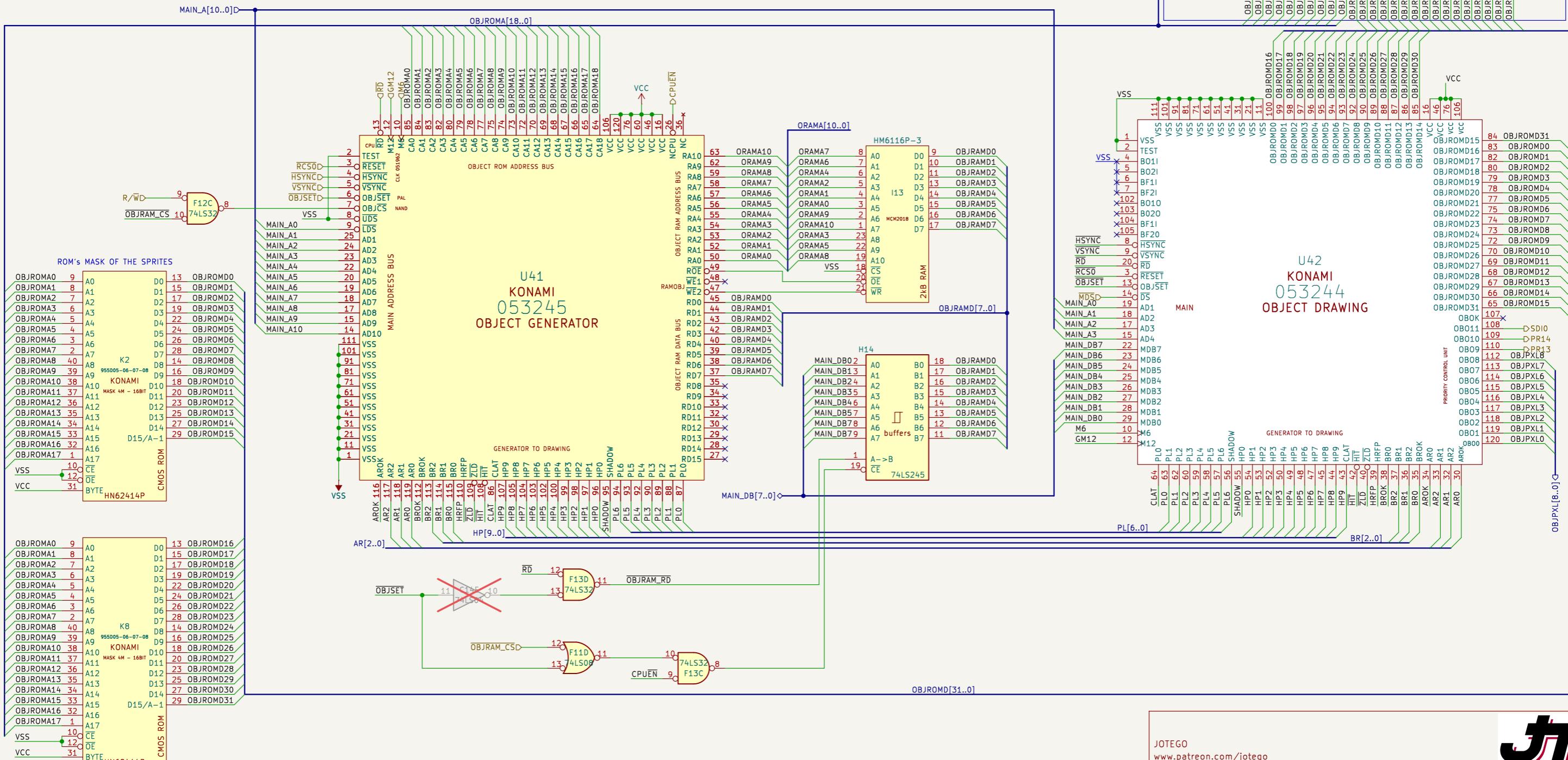
Rev: José Tejada G.
Id: 1/10

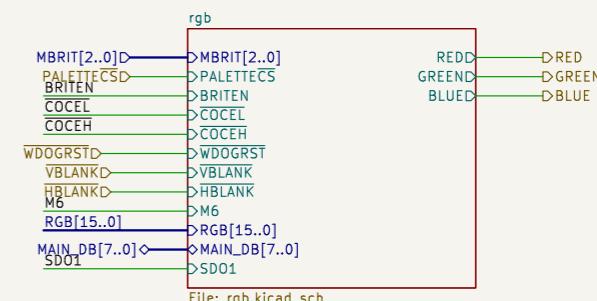
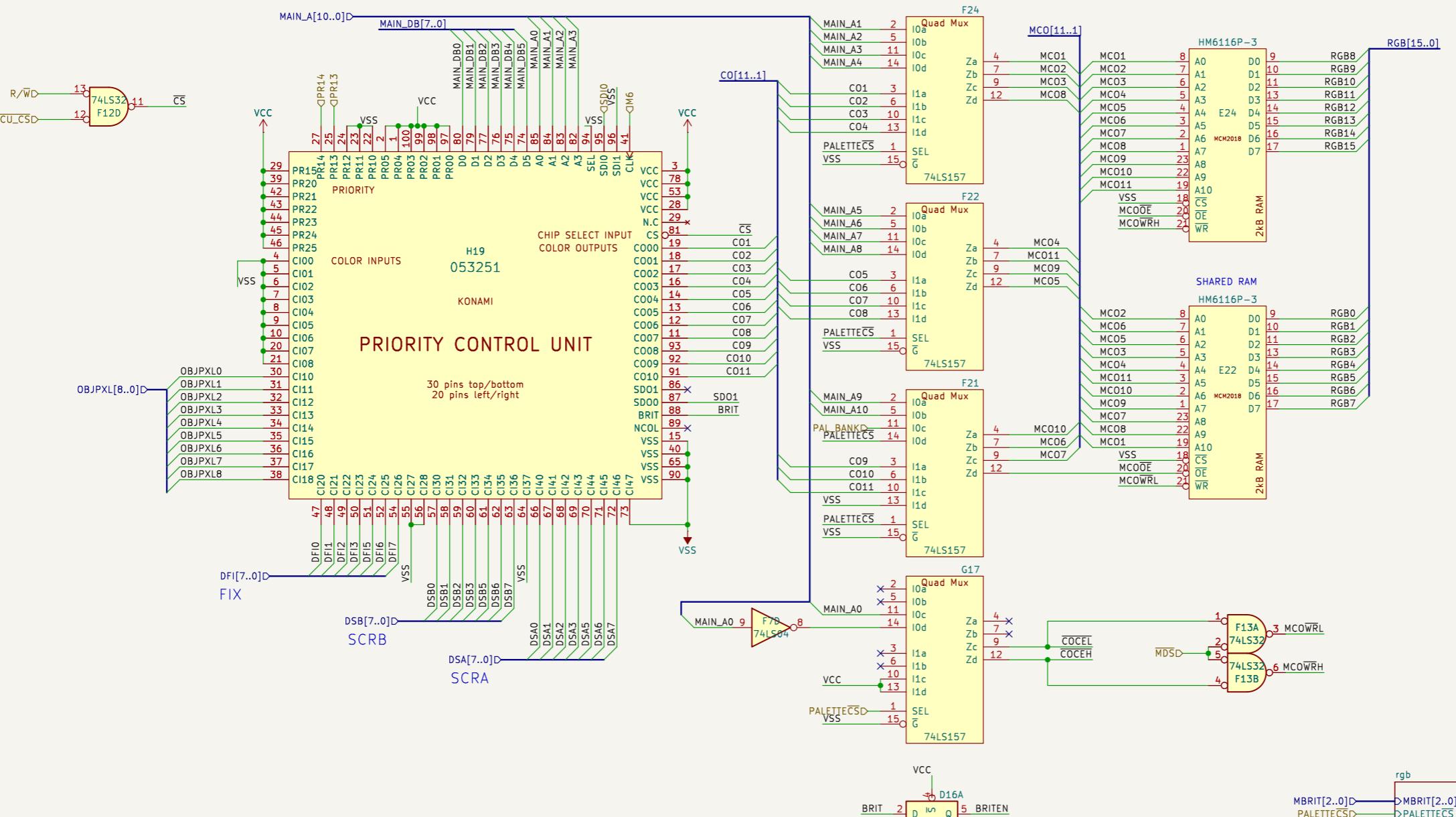






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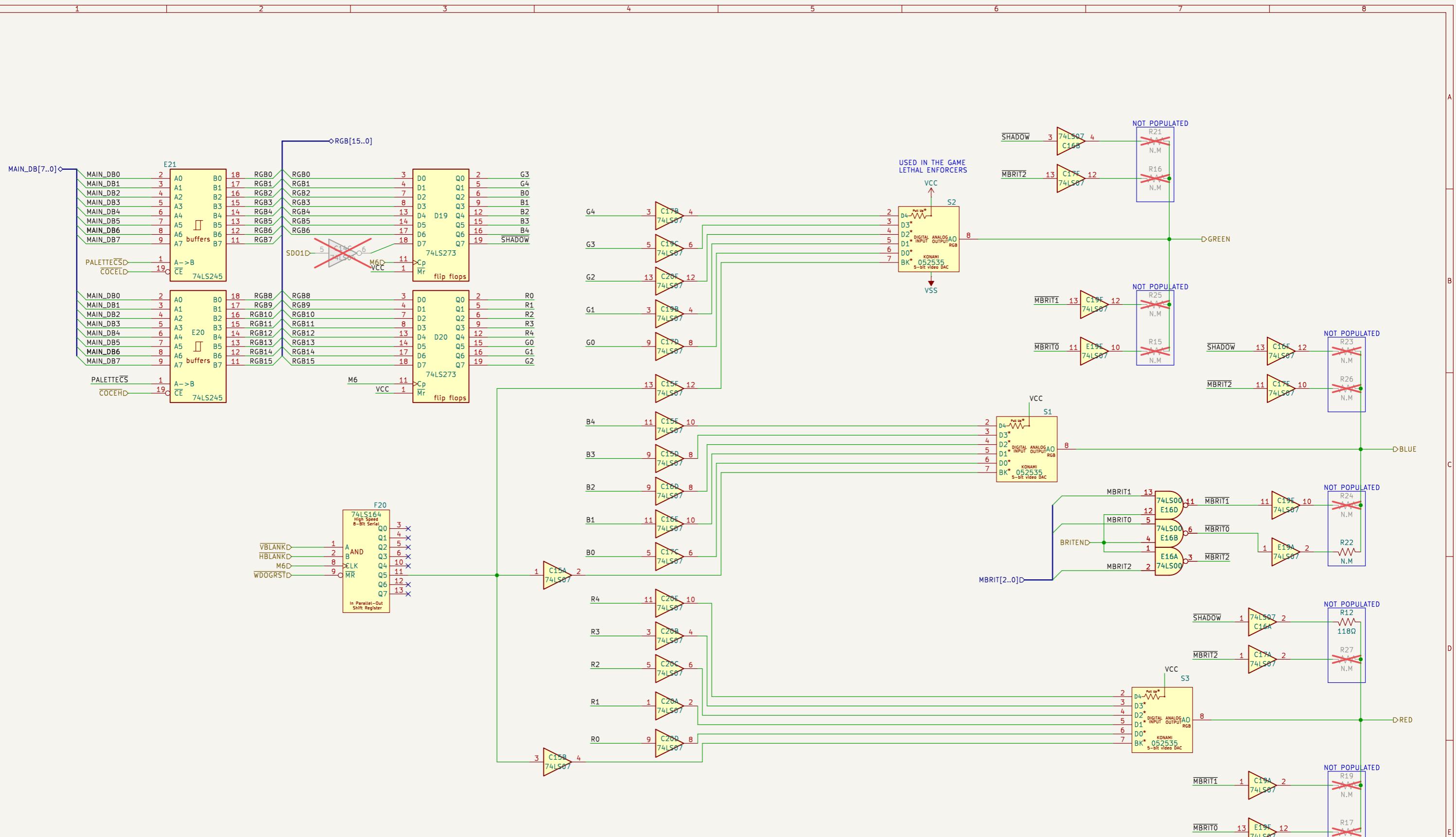
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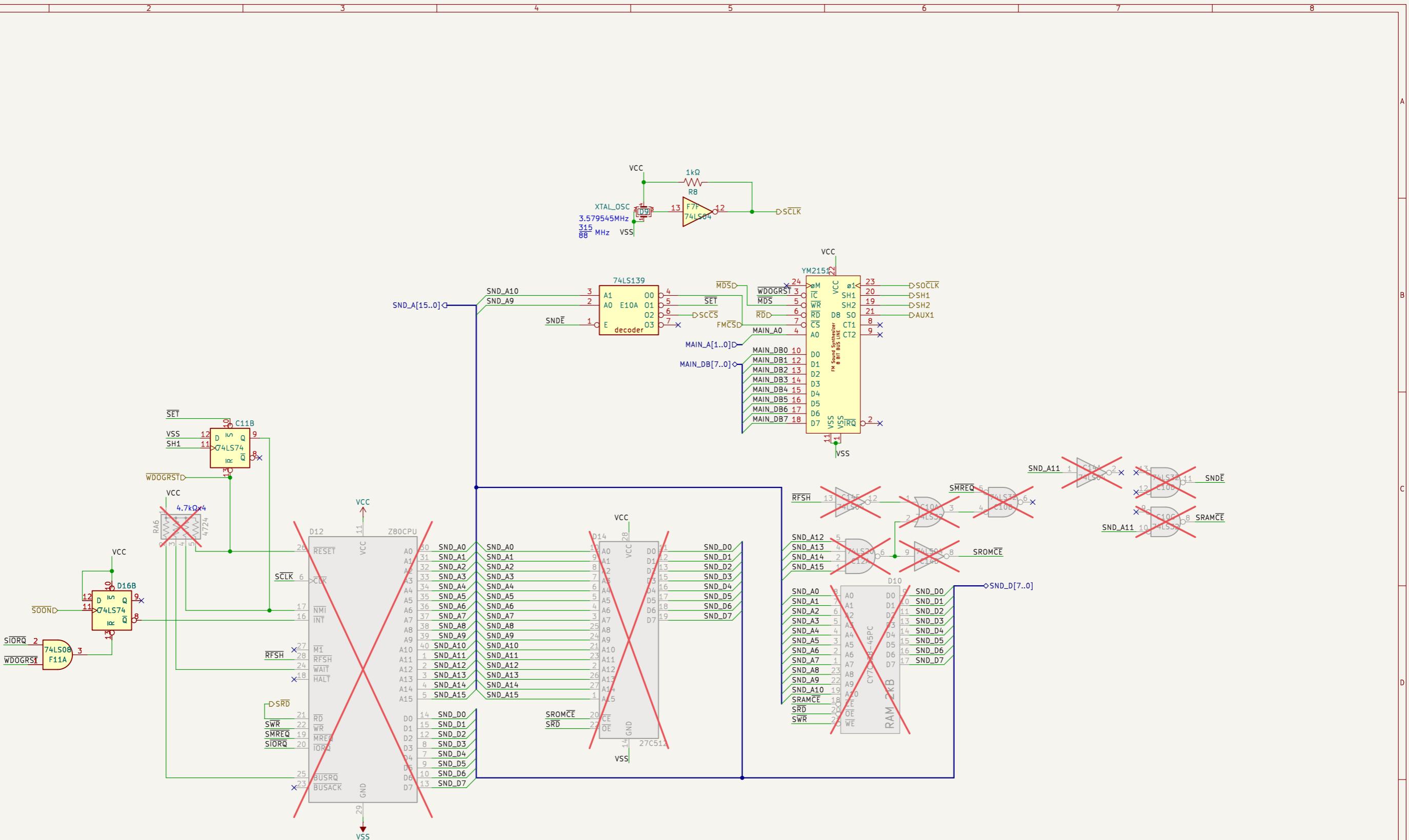
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KiCad E.D.A. 8.0.9



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Id: 6/10





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Title: Surprise Attack

Size: A3 | Date: 2025-09-01
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Id: 8/10

1 2 3 4 5 6 7 8

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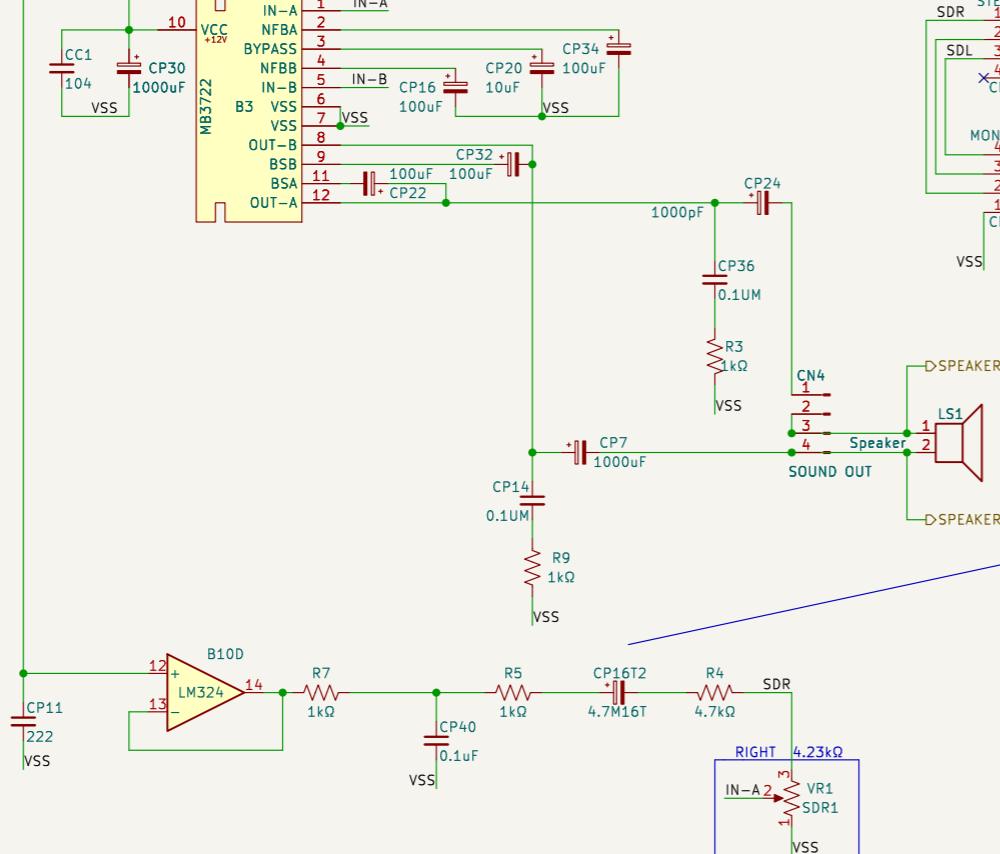
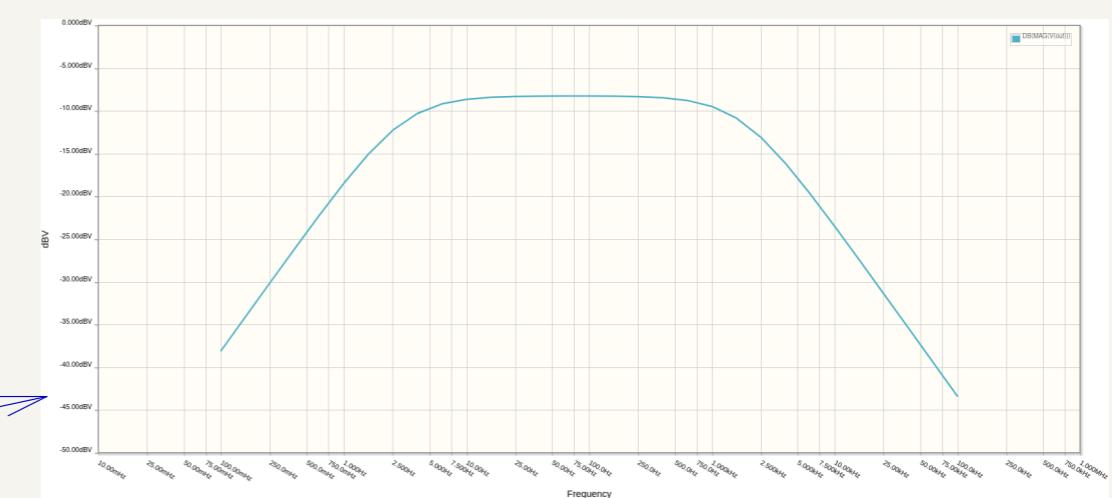
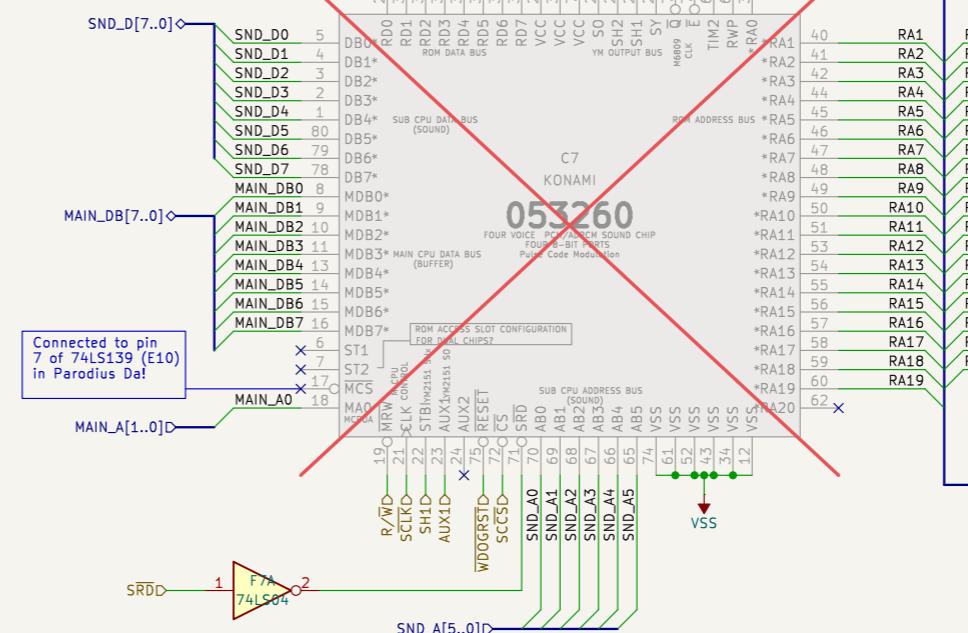
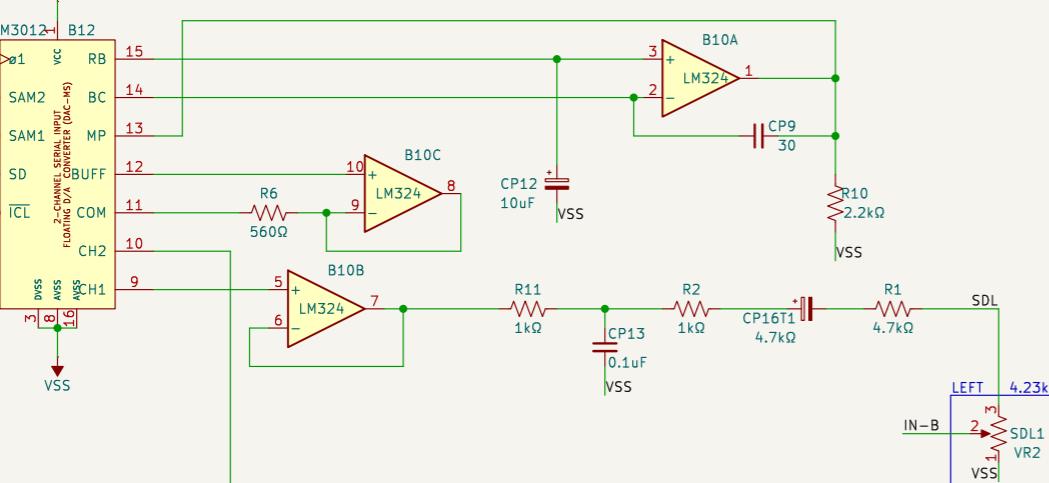
RA[19..0]

Communication between the main CPU
and the audio CPU.

RD[7..0]

Connected to pin 21 of alternate YM2151 footprint.

JP_2_Bridged



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Sheet: /pcm/
File: pcm.kicad_sch

Title: Surprise Attack

Size: A3 Date: 2025-09-01
KiCad E.D.A. 8.0.9

JT

Rev: José Tejada G.
Id: 9/10

1 2 3 4 5 6 7 8

A A

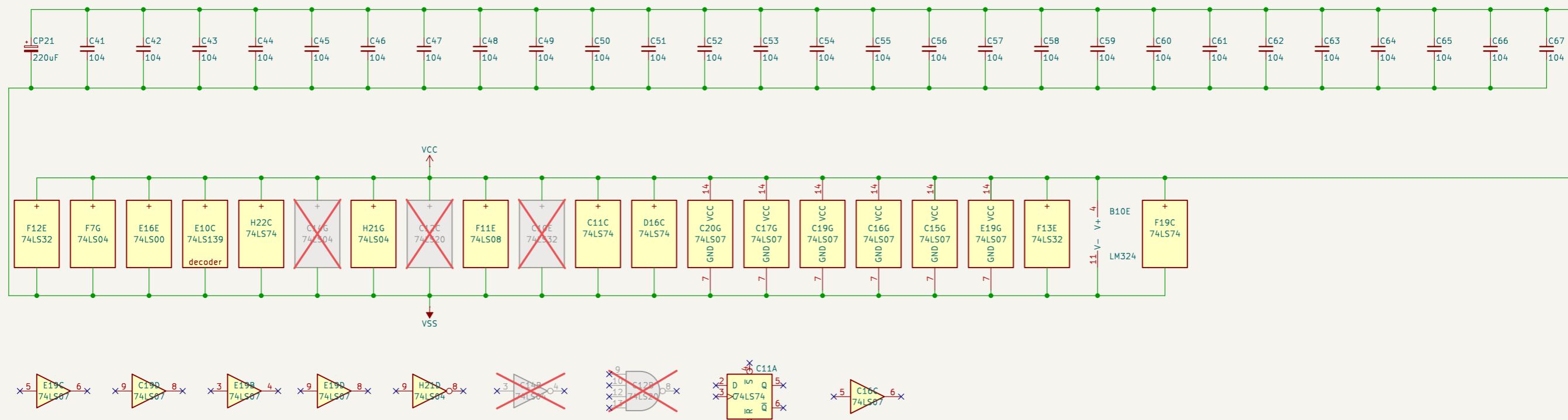
B B

C C

D D

E E

F F



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JOTEGO
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File: capacitors.kicad_sch
Title: Surprise Attack
Size: A3 | Date: 2025-09-01
KiCad E.D.A. 8.0.9 | Rev: José Tejada G.
Id: 10/10

