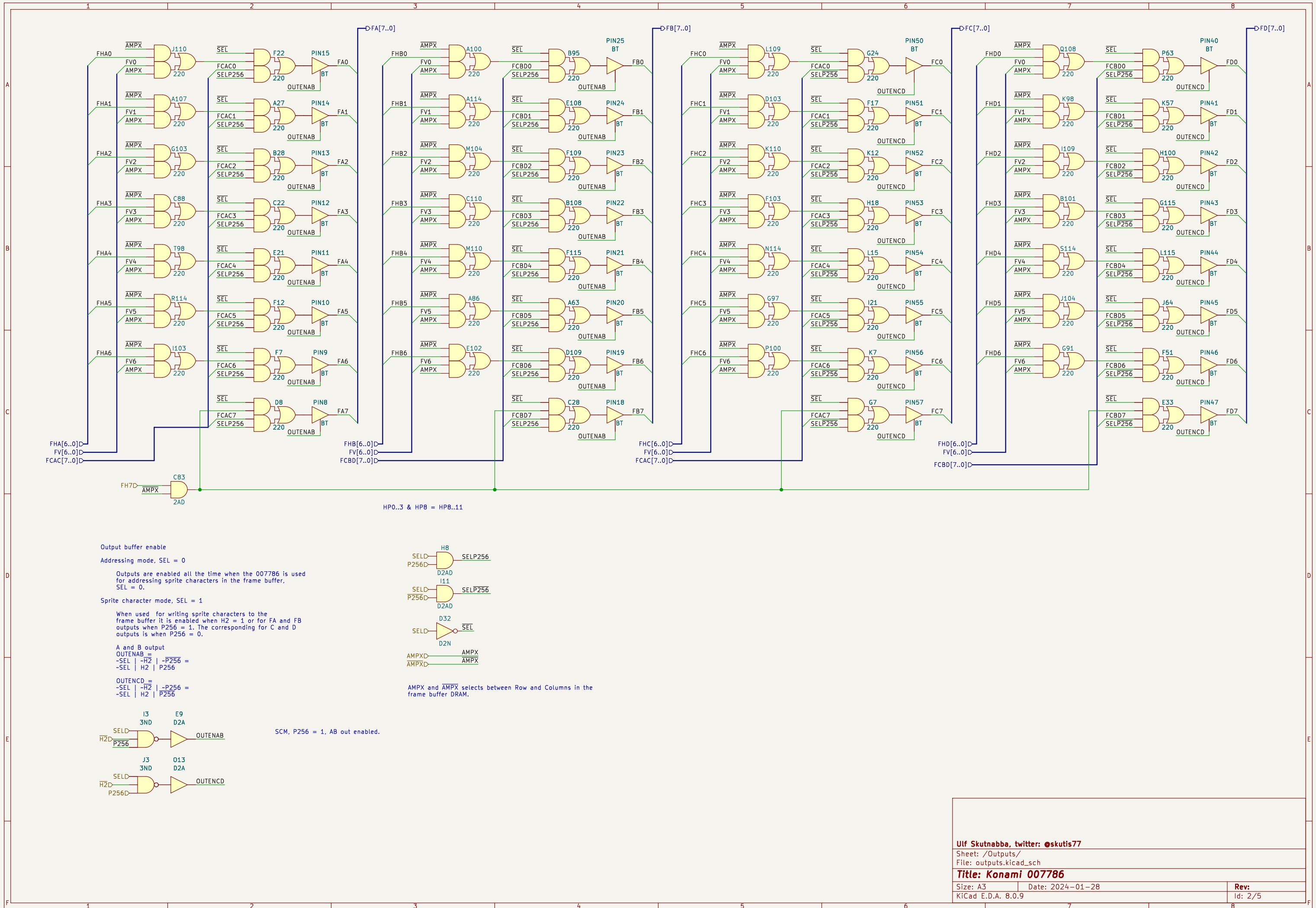
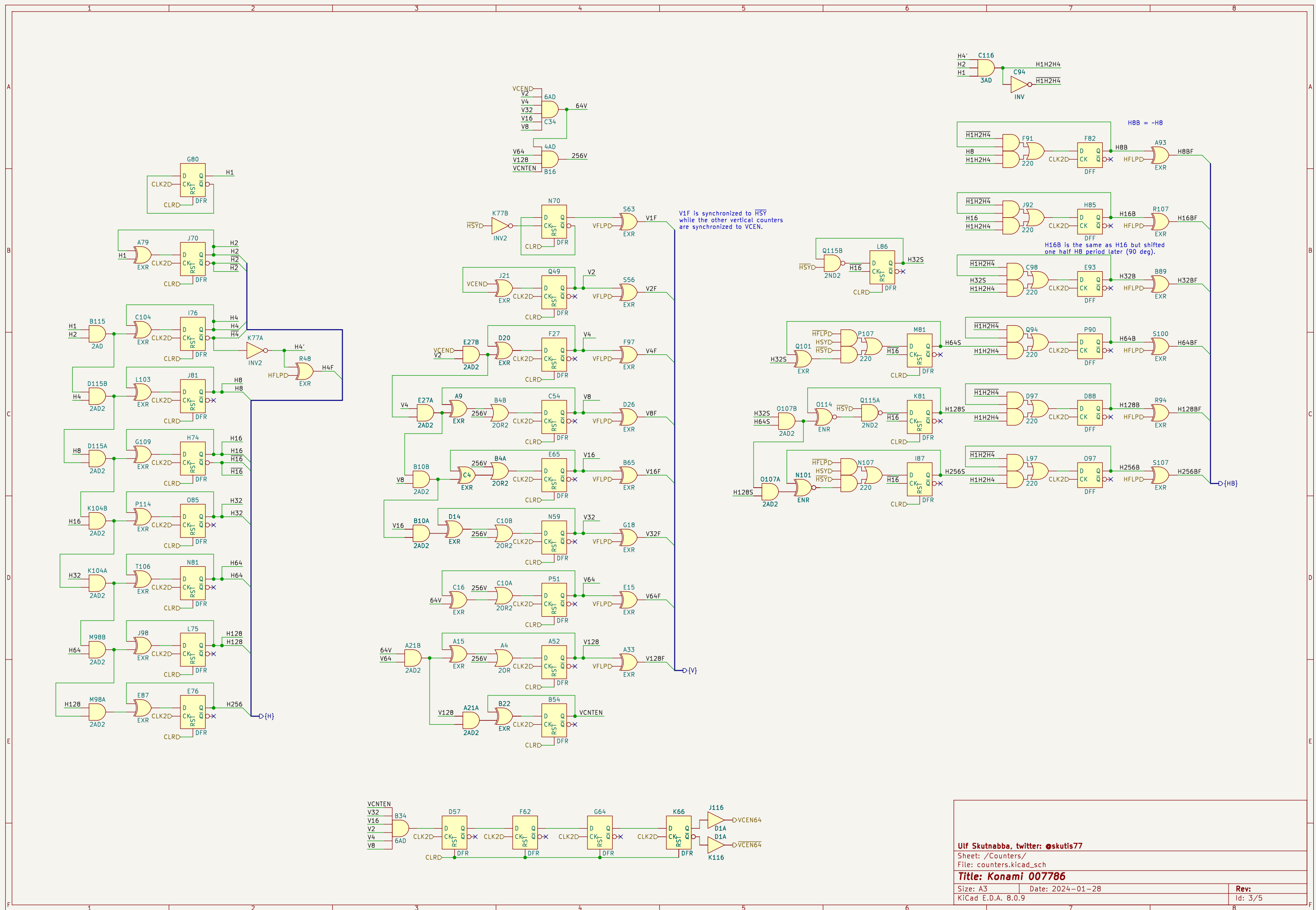
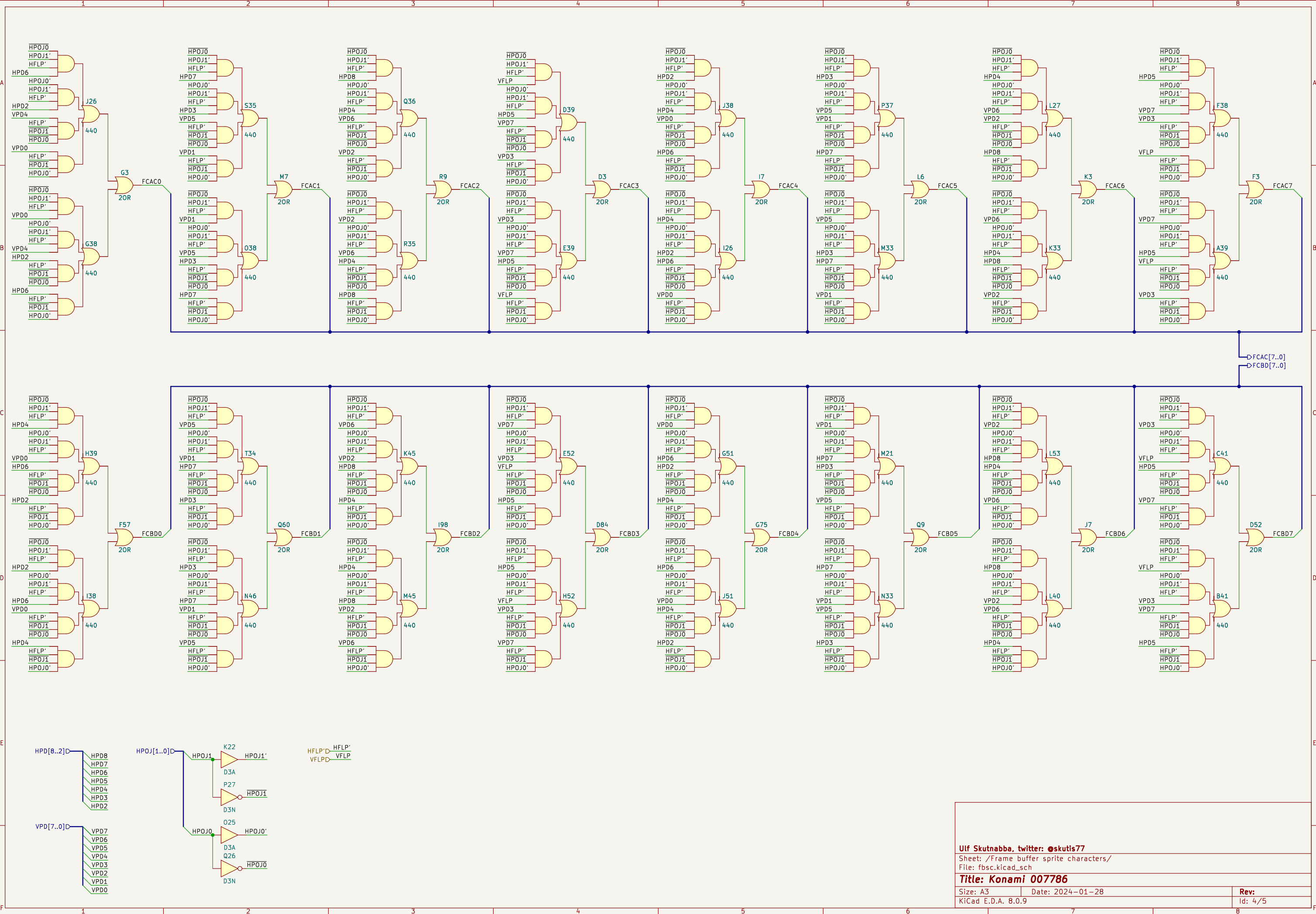


The 007786 is a sprite frame buffer controller.
It works in two modes:
- When SEL = 0, it generates addresses for the frame buffer.
- When SEL = 1, it parses sprite character data
It also handles refreshing of data in the DRAM memory.

The 007786 uses an OKI 74V000 CMOS gate array
with 2400 unit cells.

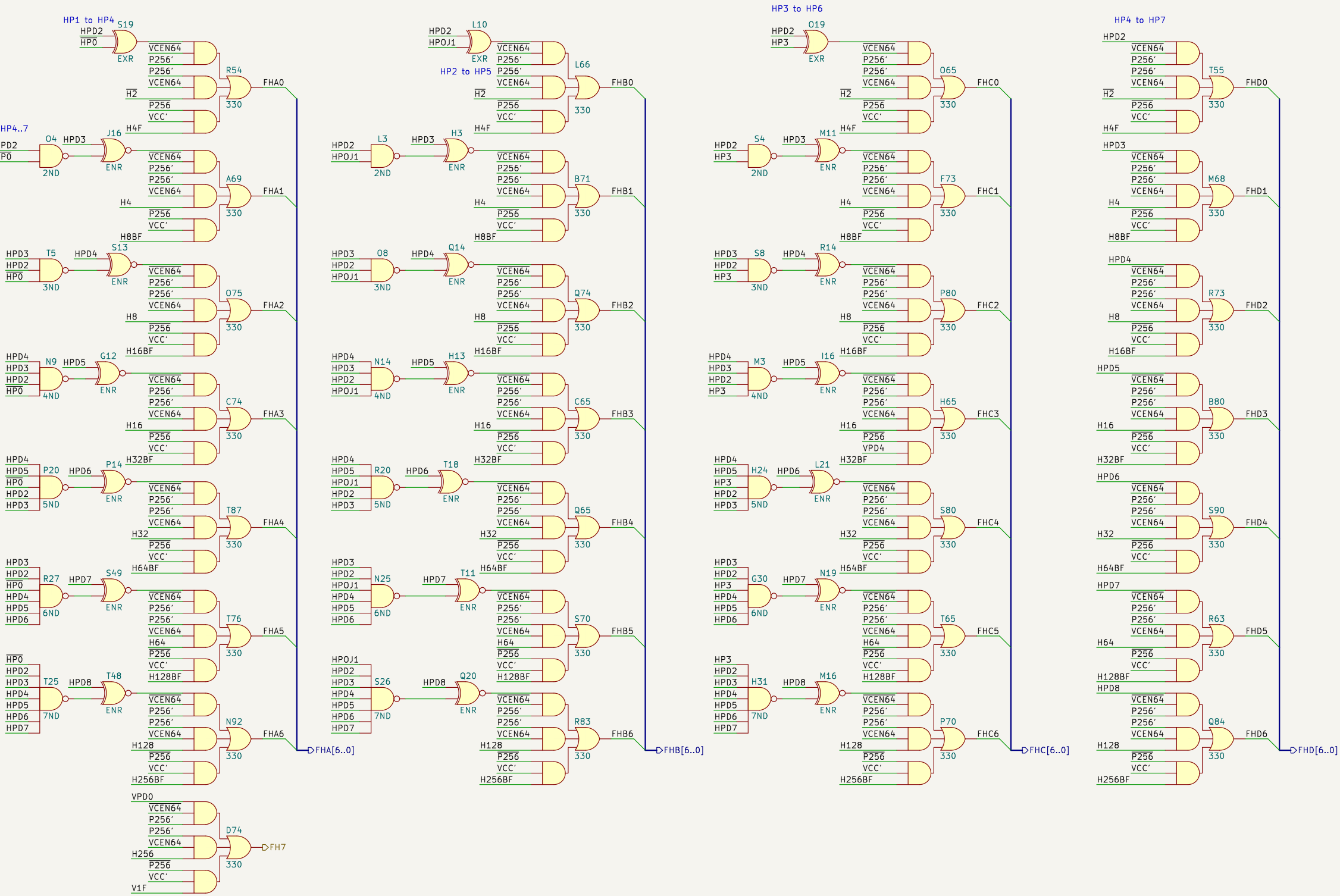




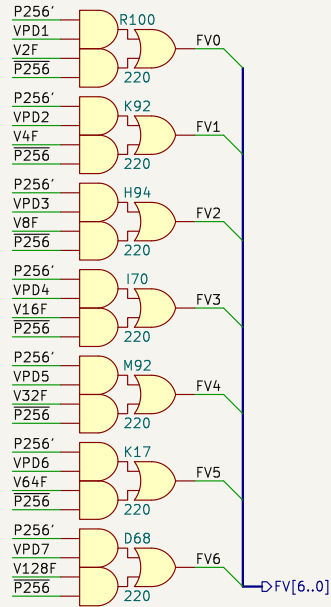


Horizontal Position

VCEN64 active: Data is written to the frame buffer
VCEN64 active, four times pre frame: Data is refreshed in DRAM, every 4ms.



Vertical Position



P256' active: Write addressing
P256 active: Read addressing

