

The frequency of CCLK is the frequency of P18M divided by 2.  
 $f_{CCLK} = f_{P18M} / 2 = 18.432\text{Mhz} / 2 = 9.216\text{MHz}$ .

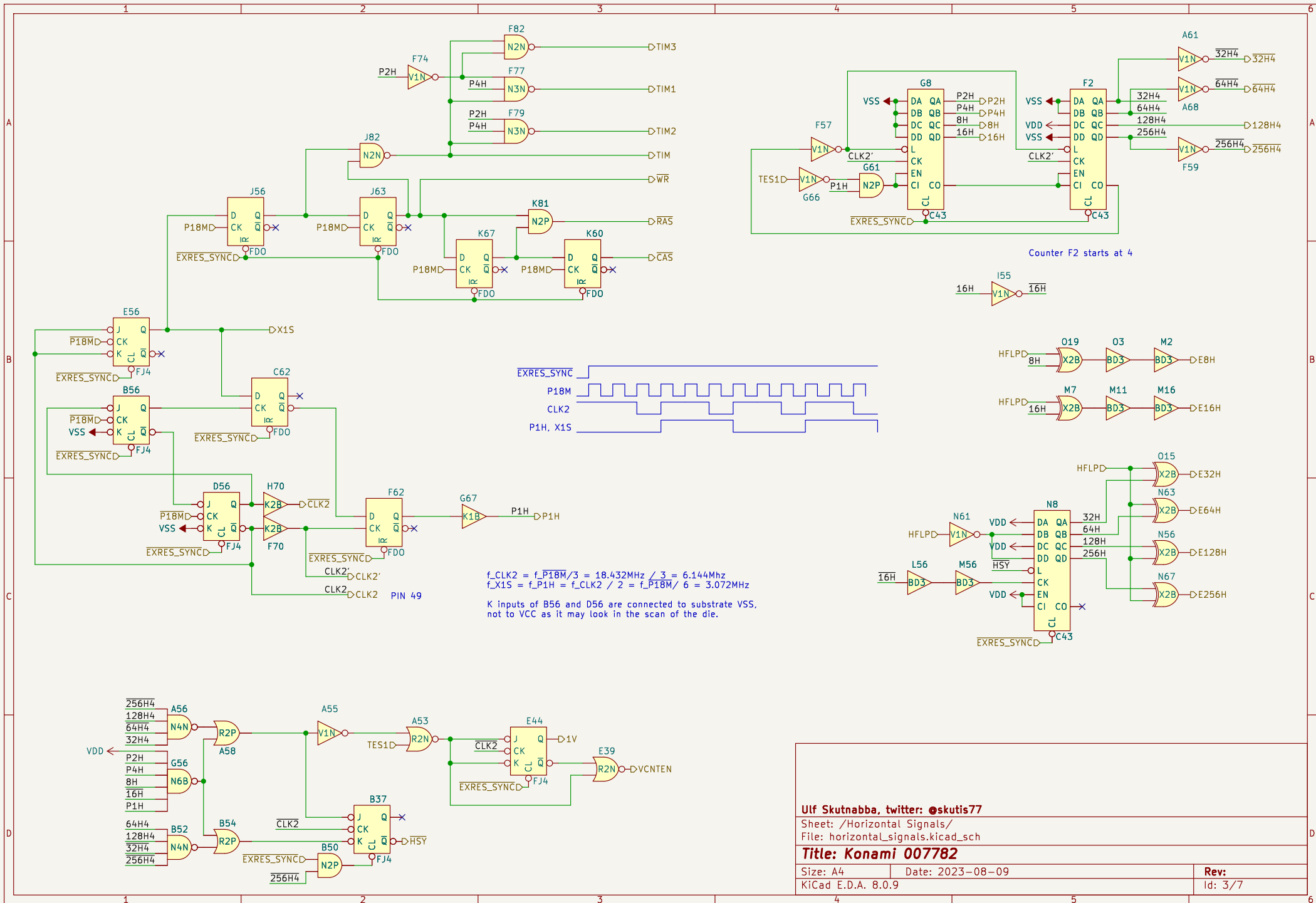


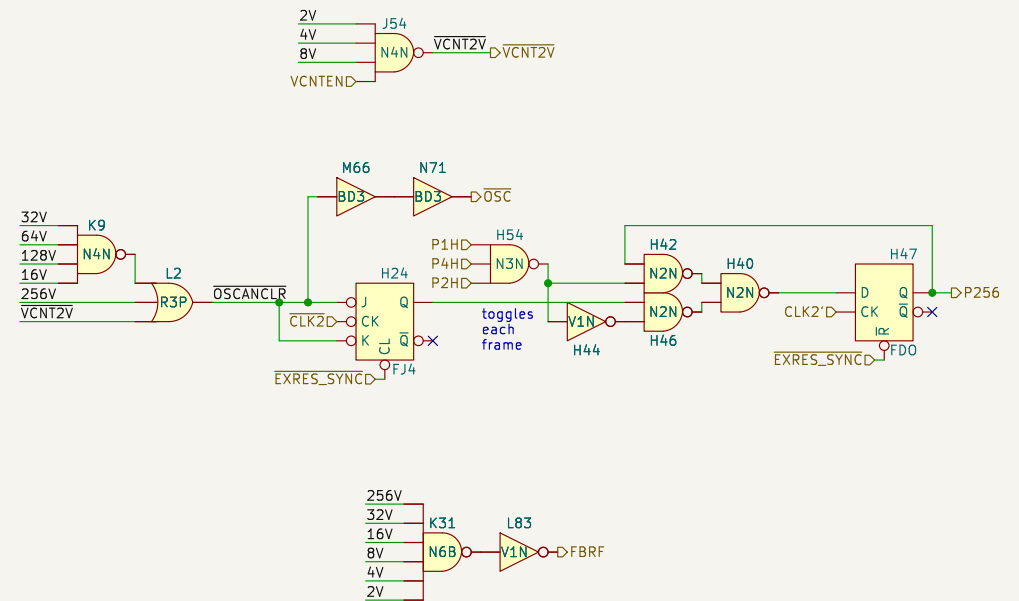
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 File: clocks\_and\_reset.kicad\_sch

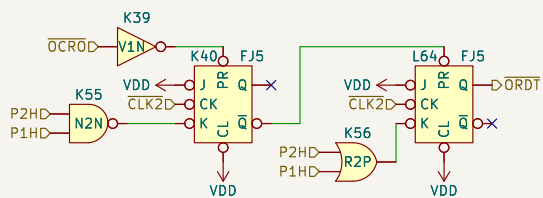
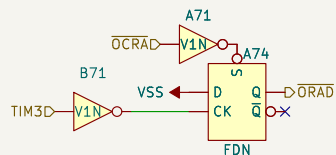
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Sheet: /Data Acknowledge/  
File: data\_acknowledge.kicad\_sch

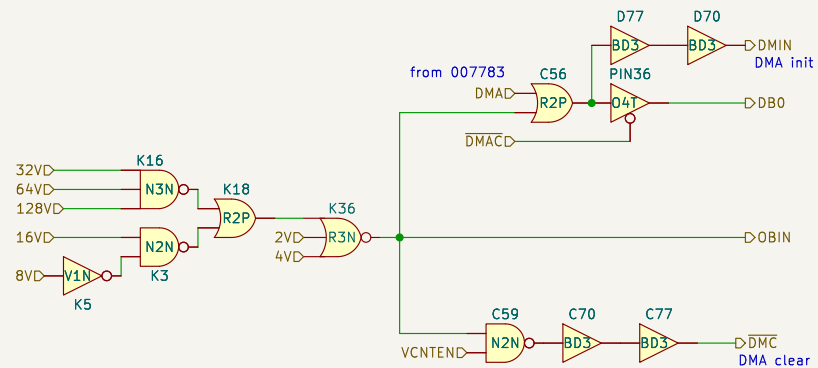
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Sheet: /DMA/  
File: dma.kicad\_sch

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