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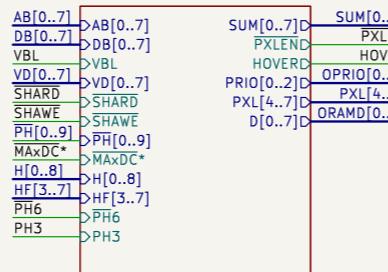
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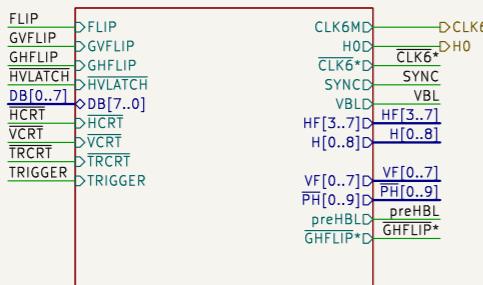
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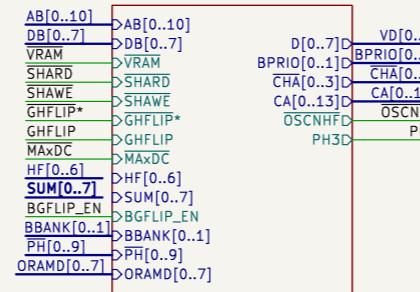
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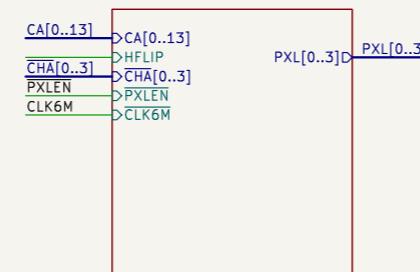
Timing



Video RAM



Tile ROM



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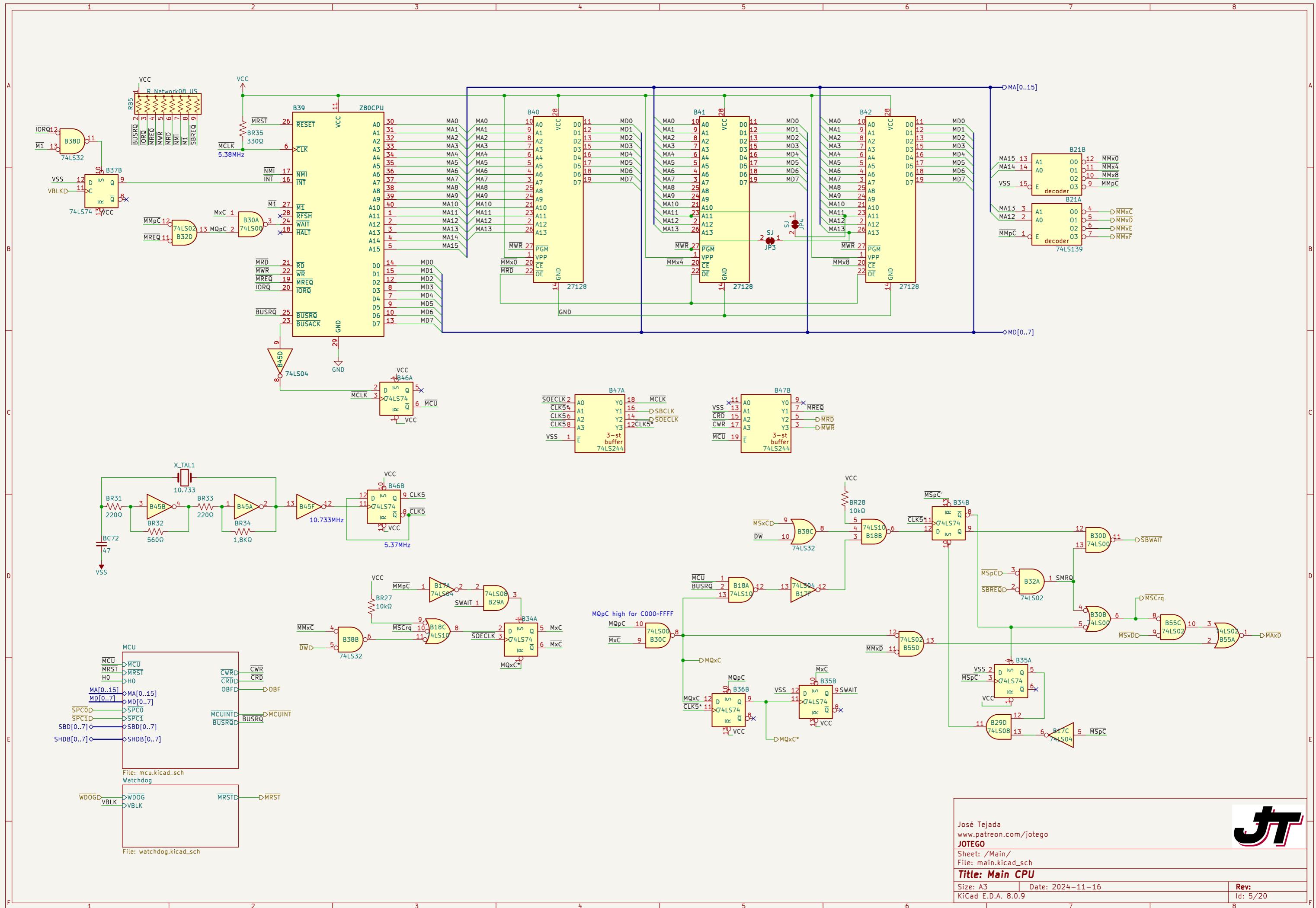
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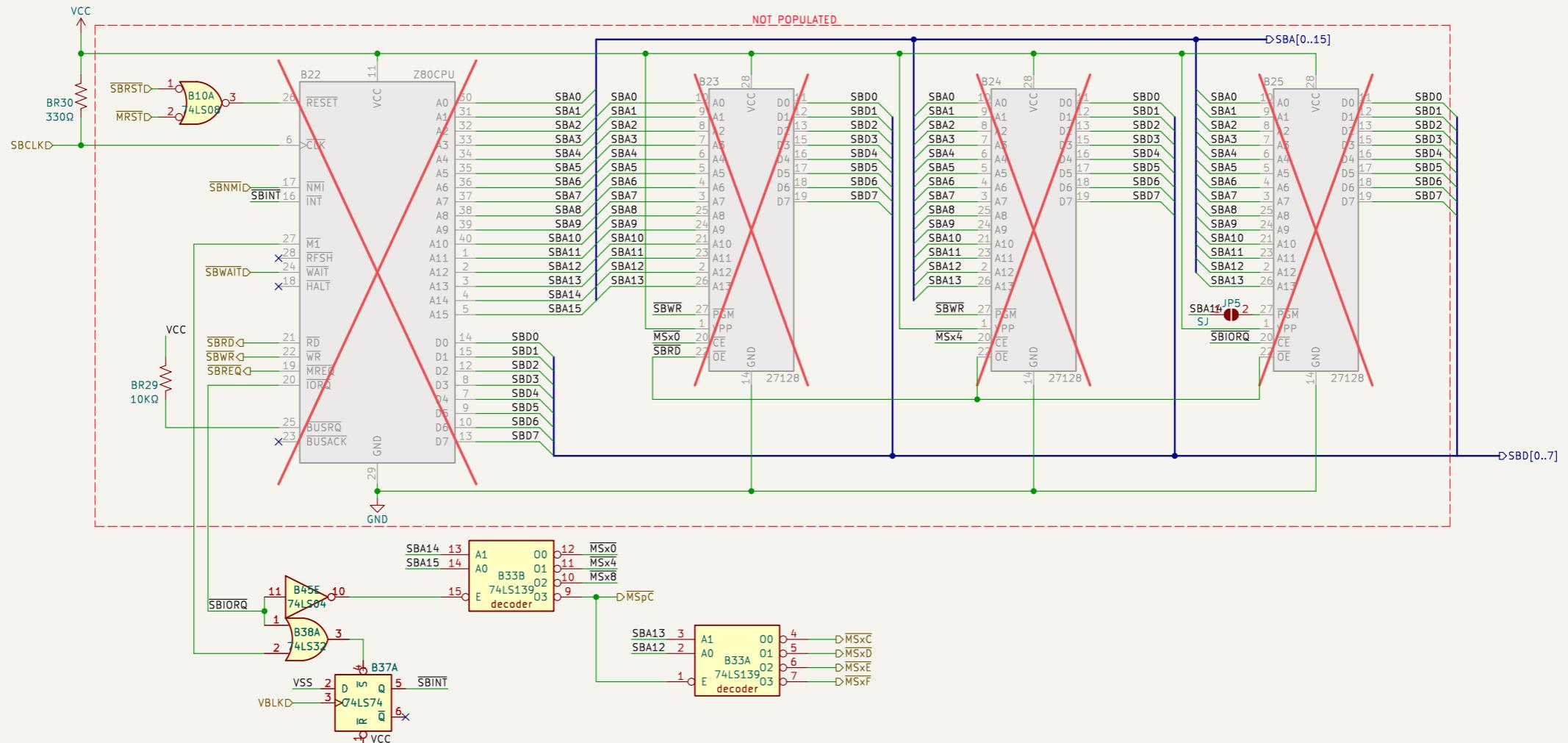
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Size: A3 | Date: 2024-11-16
 KiCad E.D.A. 8.0.9

Rev:
 Id: 7/20





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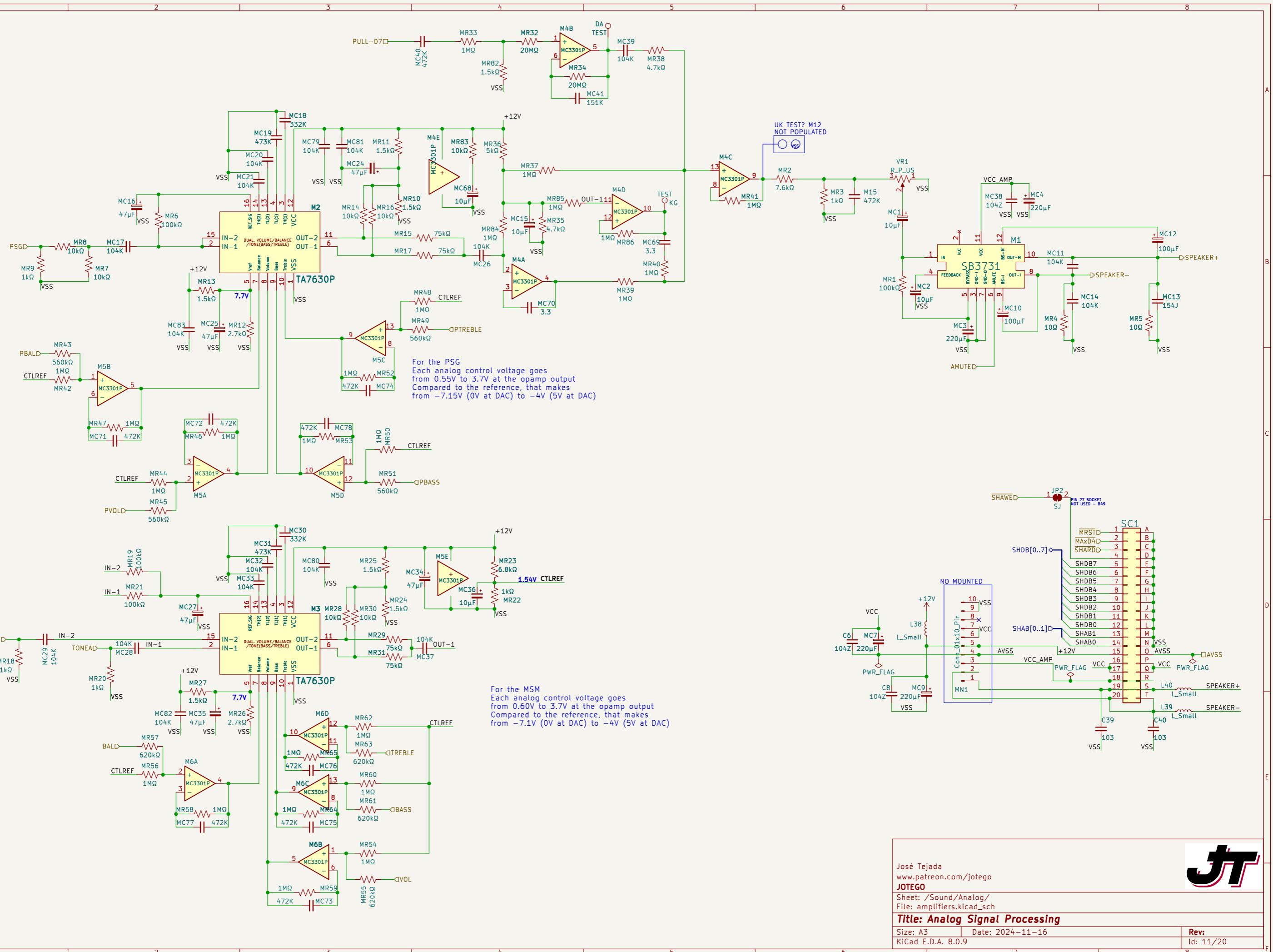
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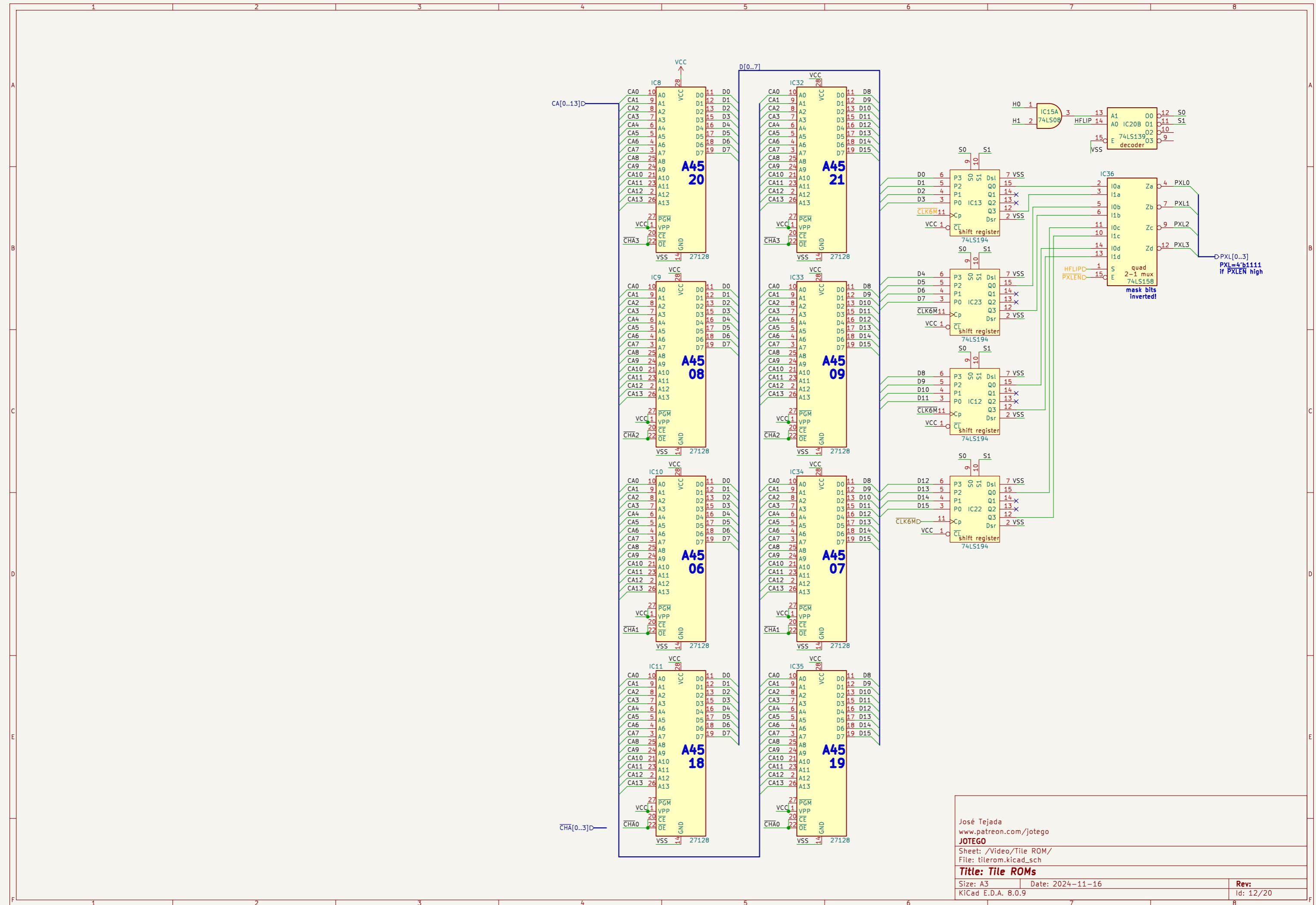
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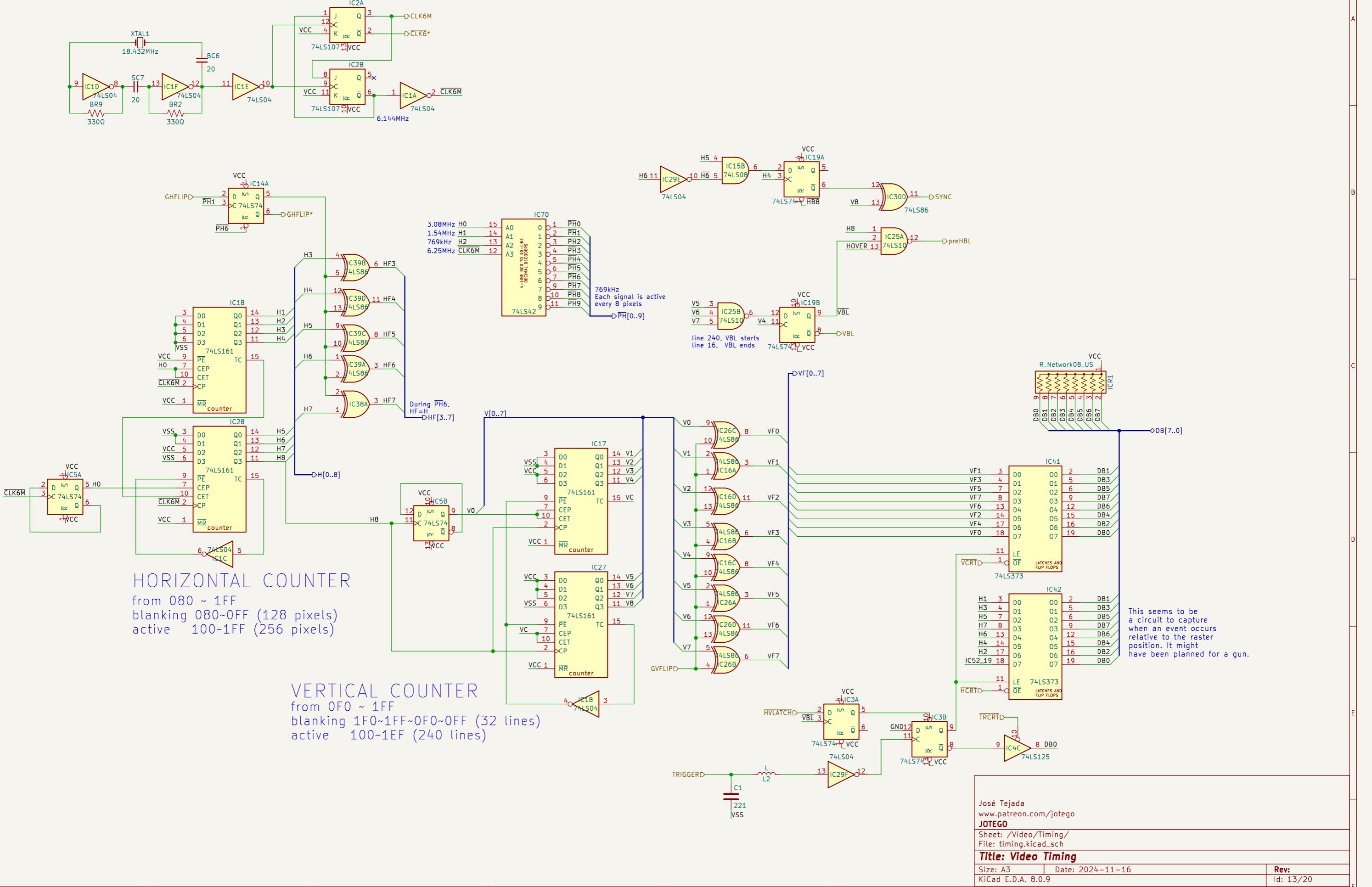
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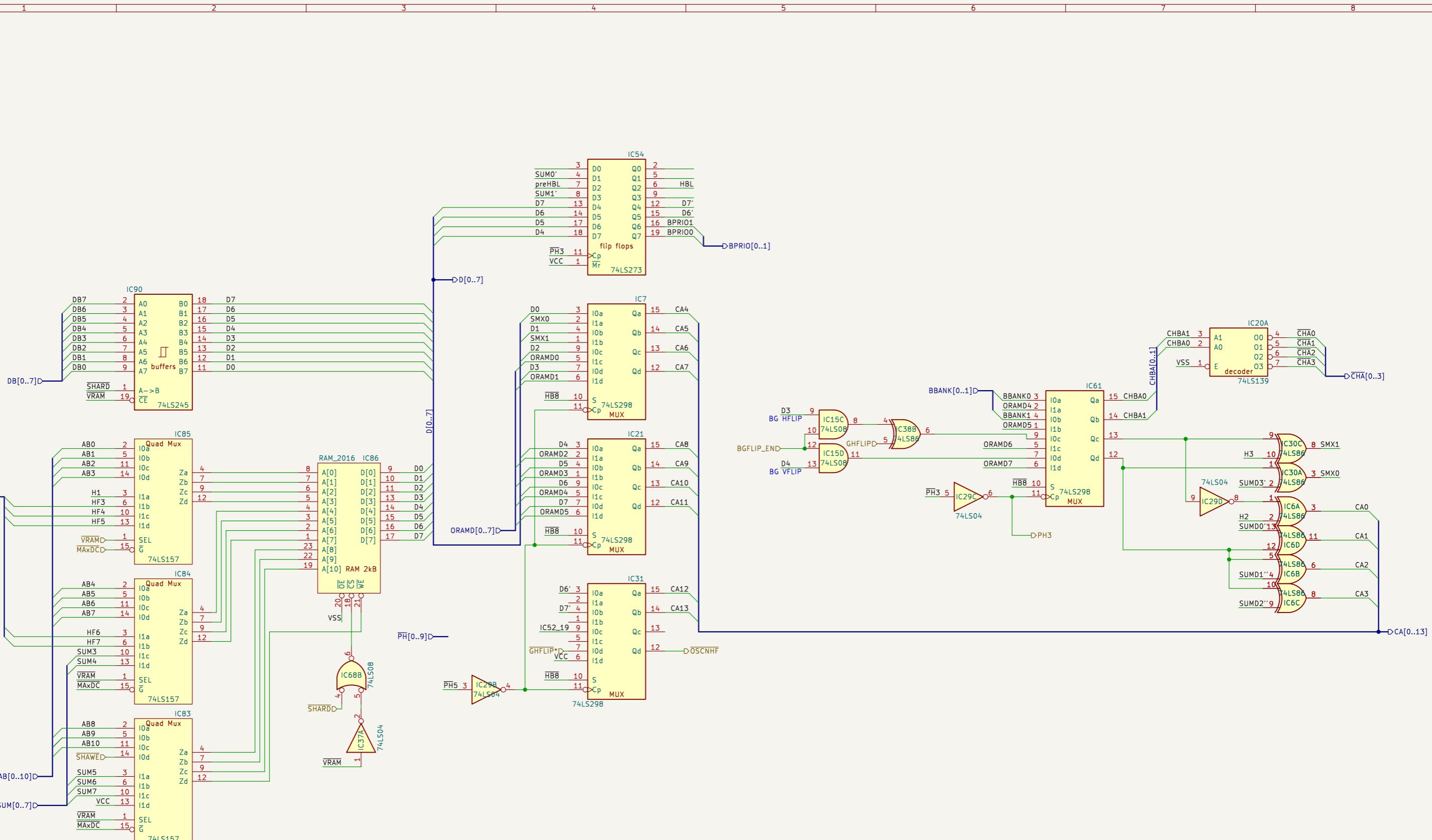
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Digitized by srujanika@gmail.com









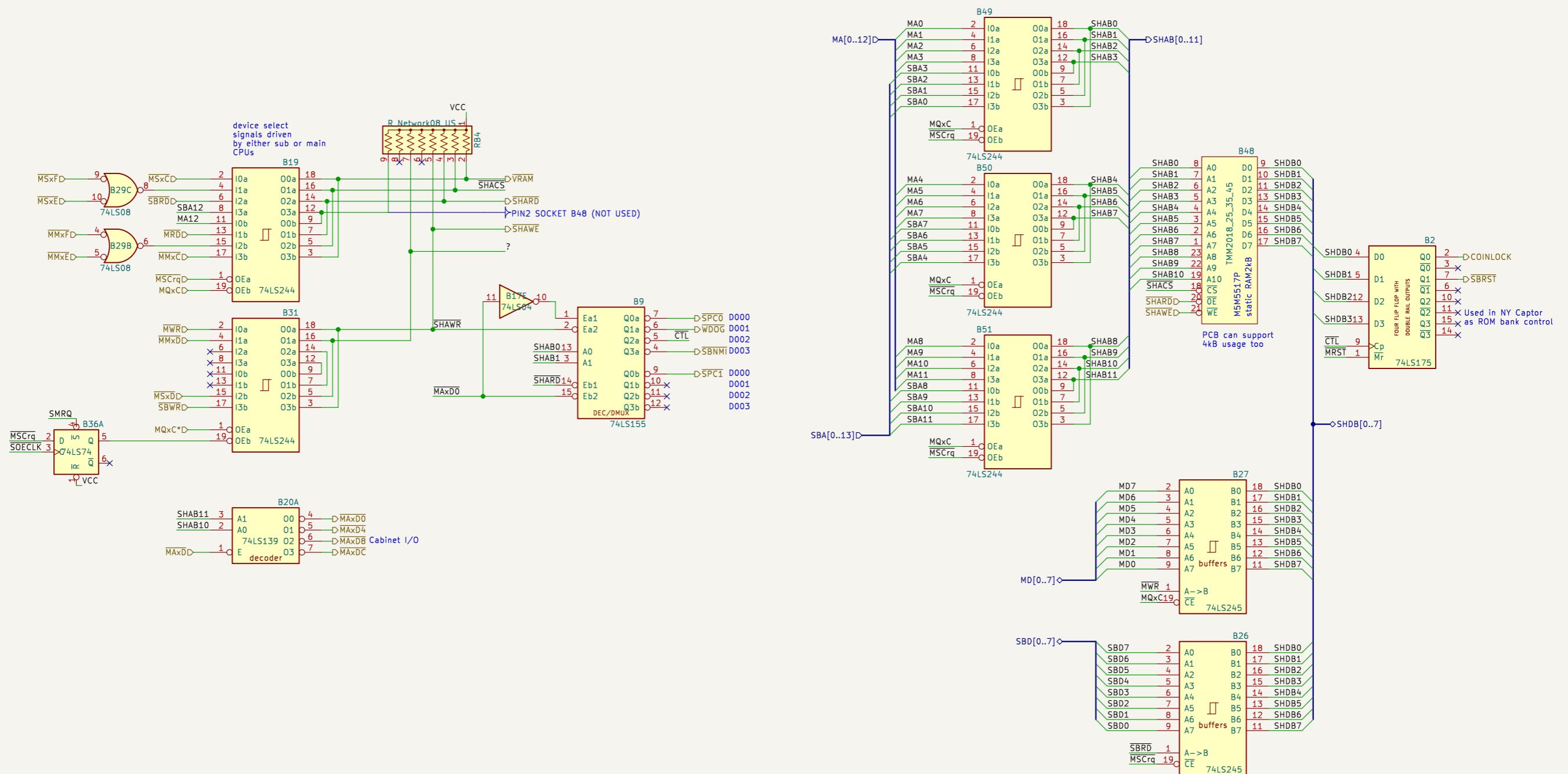
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File: vram.kicad_sch

Title: Background Video RAM

Size: A3 | Date: 2024-11-16
KiCad E.D.A. 8.0.9

Rev:
Id: 13/20





The MCU can take control of the share bus by requesting the main CPU bus

The MCU can communicate with both CPUs via latches B52 and B53

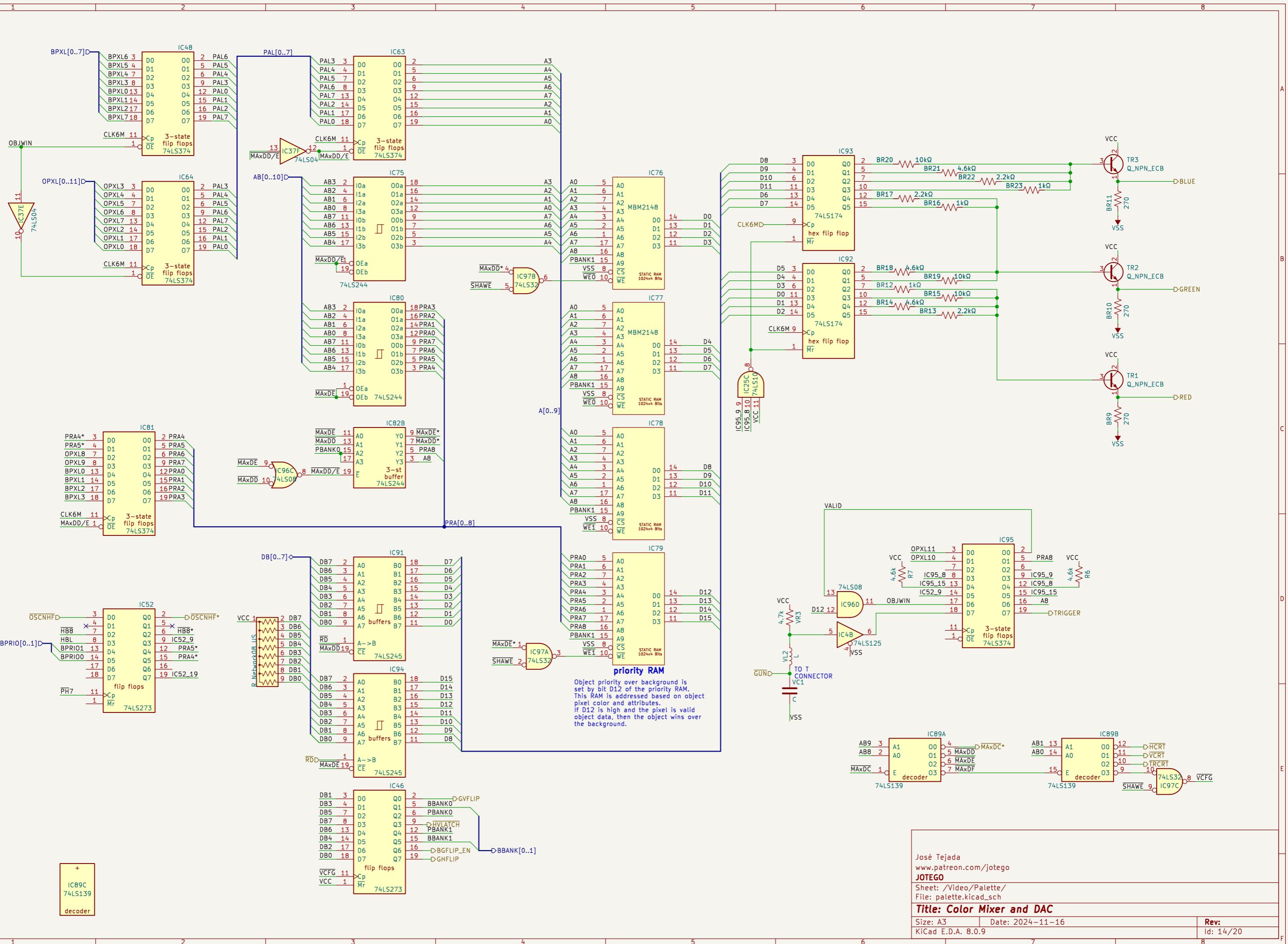
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Sheet: /Main/MCU/
File: mcu.kicad_sch

Title: MCU

Size: A3 Date: 2024-11-16
KiCad E.D.A. 8.0.9

Rev: Id: 14/20



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File: palette.kicad_sch

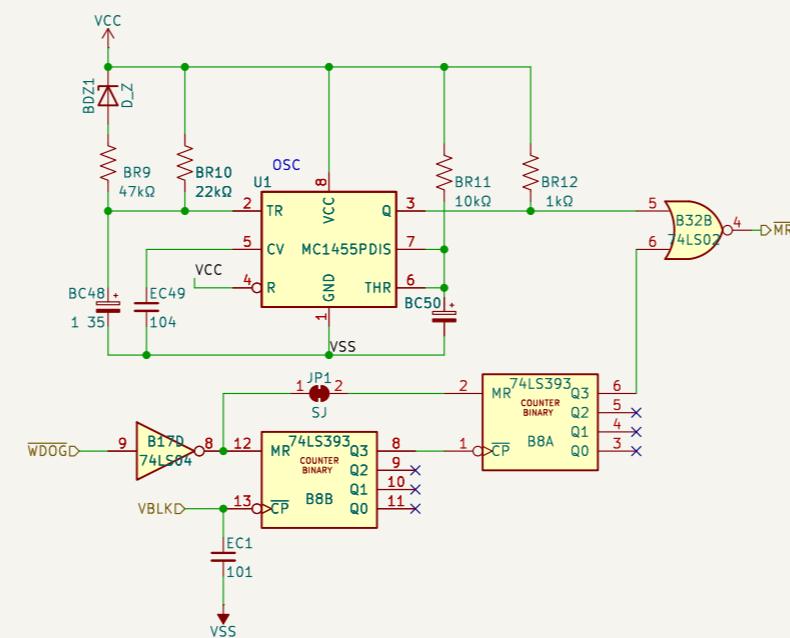
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KiCad E.D.A. 8.0.9

Rev:

Id: 14/20



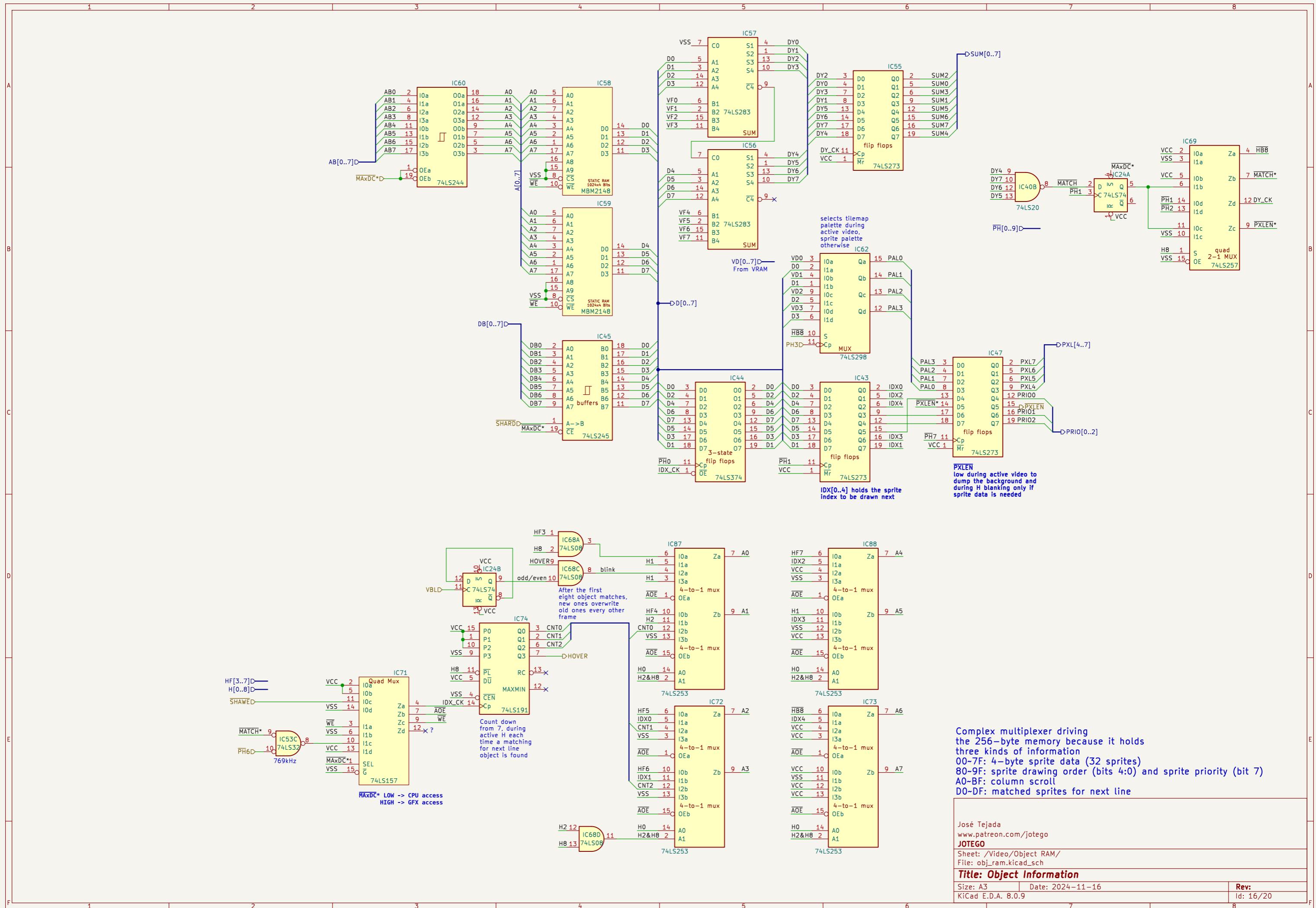
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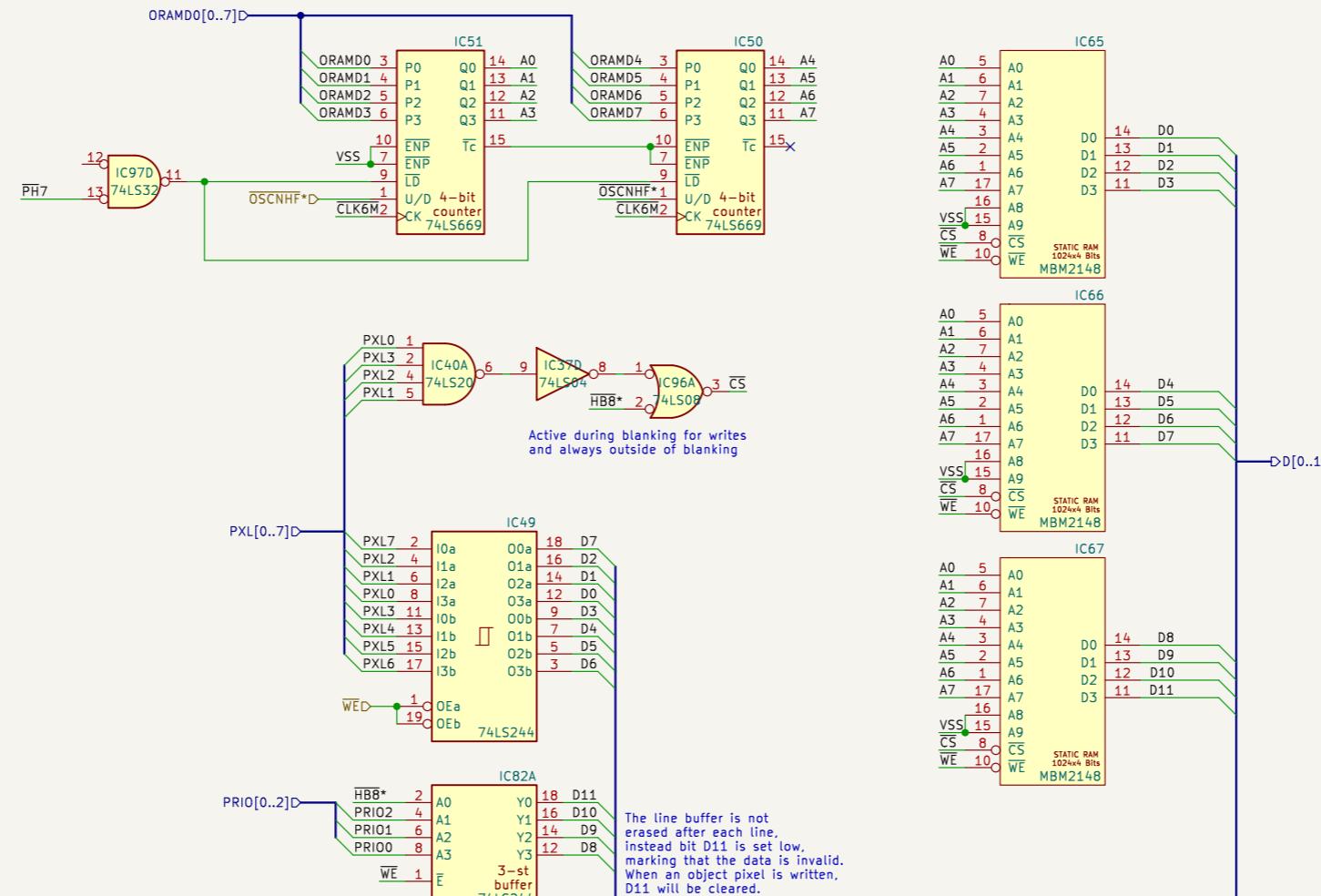
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Rev:
Id: 15/20



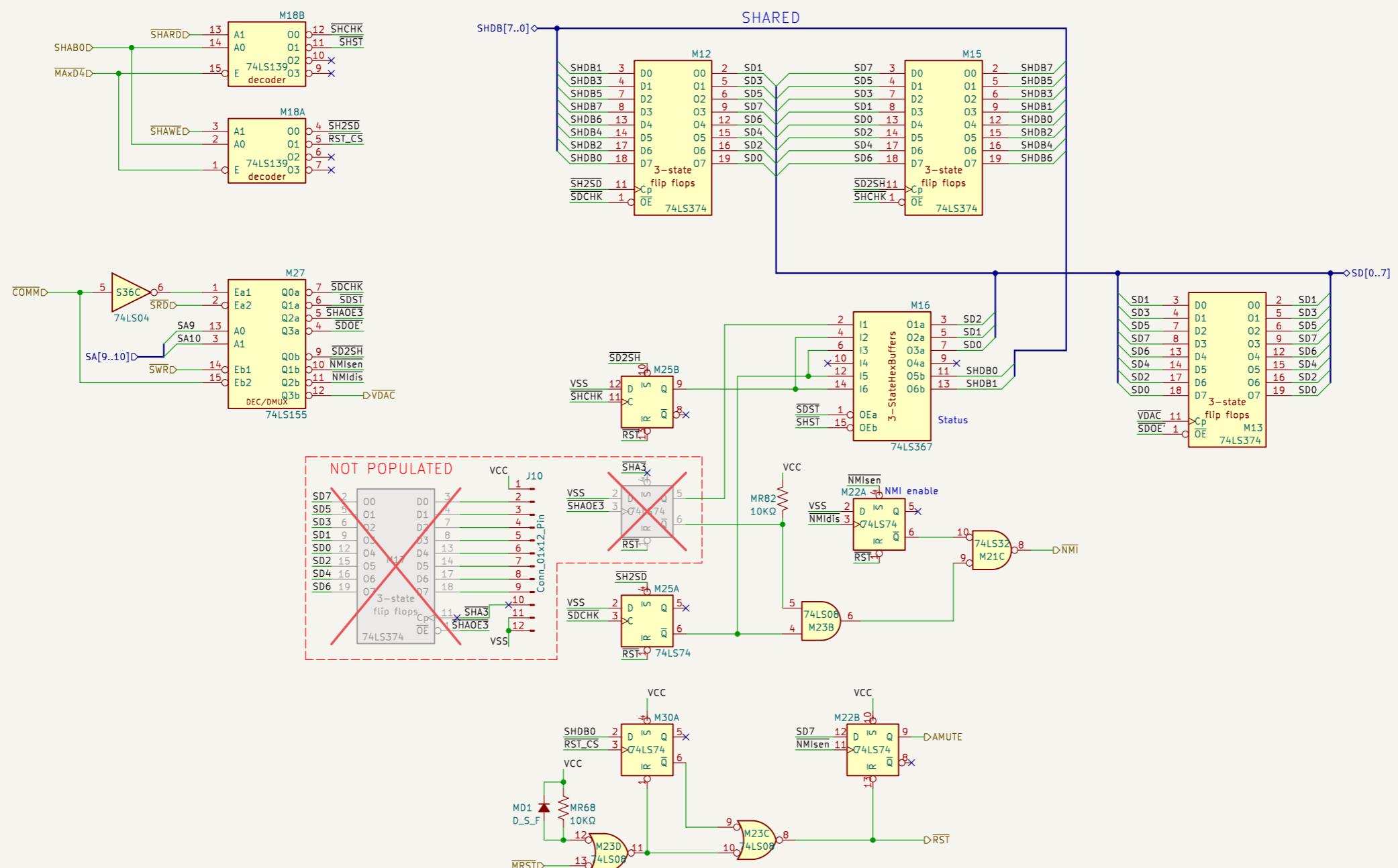


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Title: Single Line Video Buffer

Size: A3	Date: 2024-11-16
KiCad E.D.A. 8.0.9	Rev: 17/20



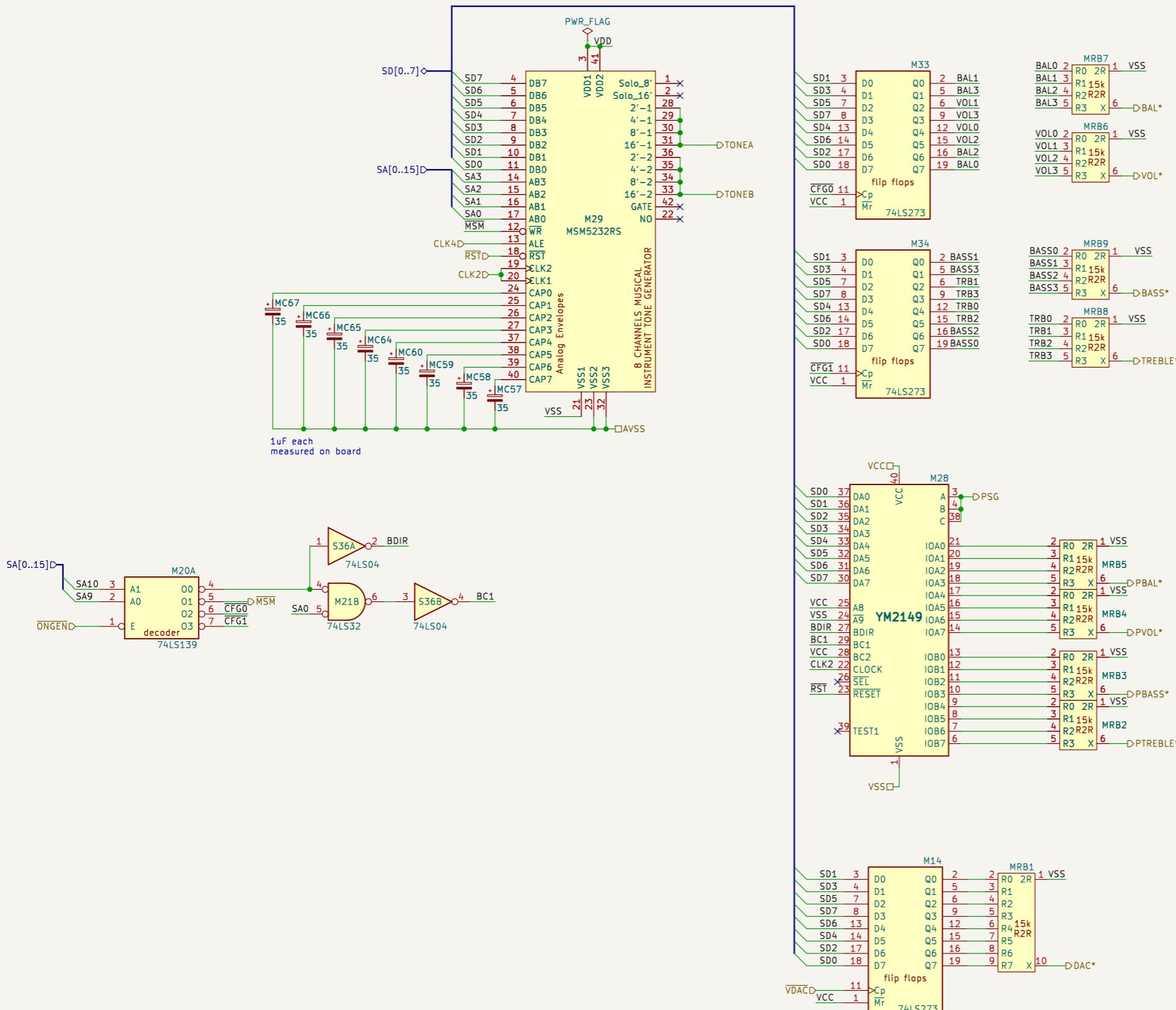
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Title: Communication Latches

Size: A3 | Date: 2024-11-16
KiCad E.D.A. 8.0.9

Rev:
Id: 18/20



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File: ongen.kicad_sch

Title: Sound Generators

Size: A3 Date: 2024-11-16
KiCad E.D.A. 8.0.9

Rev:
Id: 19/20

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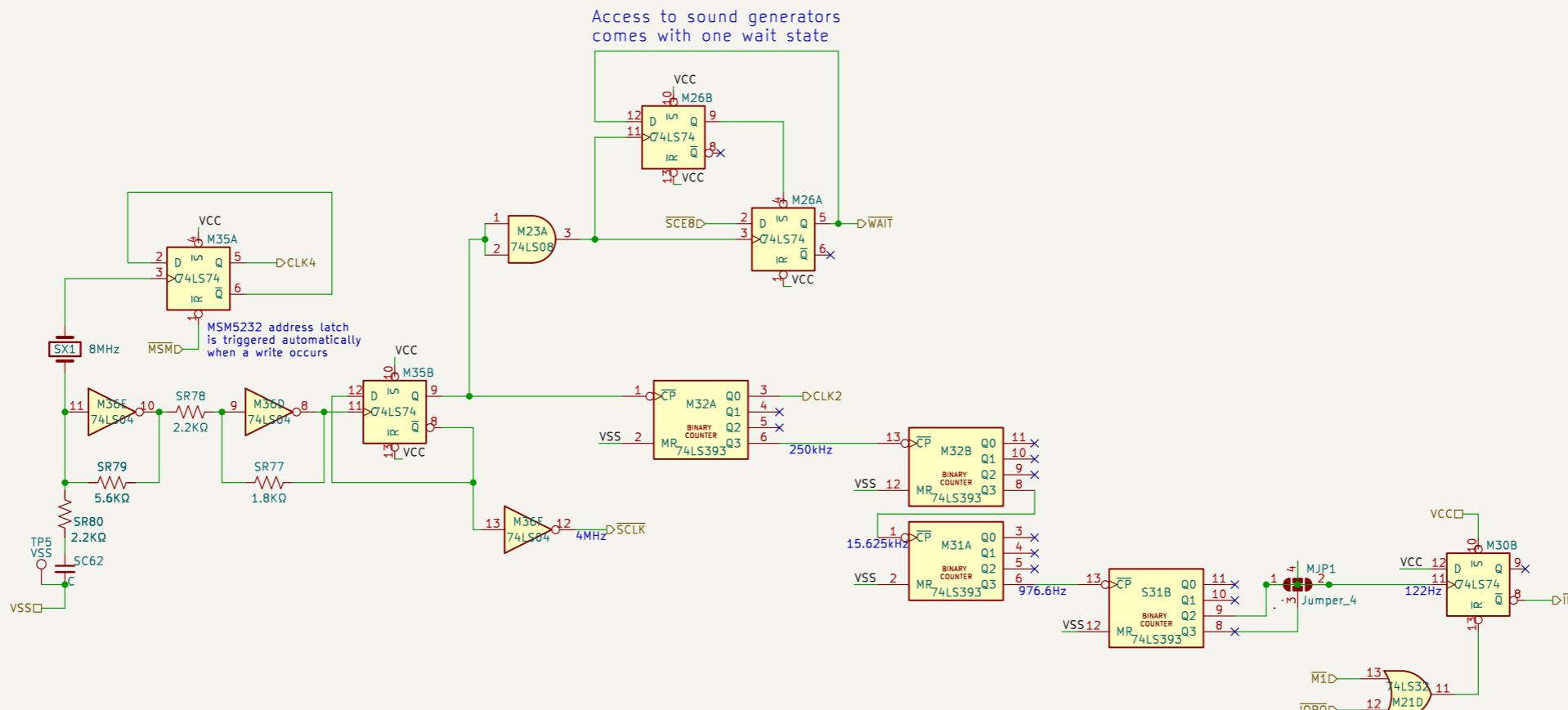
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File: snd_clocks.kicad_sch

Title: Sound Clock Generation

Size: A3	Date: 2024-11-16
KiCad E.D.A. 8.0.9	Rev: 1d: 20/20