

Digital Oscilloscope Memo

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Abstract—
THE purpose of this memo is to discuss several parameters on the digital oscilloscope and how they effect readings and measurements. The oscilloscope will then be used to simultaneously measure two channels of information from an LRC circuit to observe the circuit's response to a range of frequencies.

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I. METHODOLOGY

- 1) The signal generator was initialized with the frequency given in Table I and connected to channel 1 of the oscilloscope.
- 2) The self-calibration function on the oscilloscope was activated. The probes were then calibrated as well.
- 3) The circuit shown in I was constructed, with the initial values described in Table I. Several settings were modified, and the changes and effects were recorded.
- 4) Using the auto-measuring display, the values in Table II were recorded.

Table I
LAB PARAMETERS (GIVEN)

Wave Type	Sine
Frequency	100 kHz
Peak to Peak Voltage	12 V
Resistance	12k Ω
Inductance	17 mH
Capacitance	220 μ F
DC Offset	0 V

II. DATA AND ANALYSIS

A. Oscilloscope Parameter Variations

The following settings on the oscilloscope were adjusted, and the observed effects are explained below.

AC / DC Coupling:

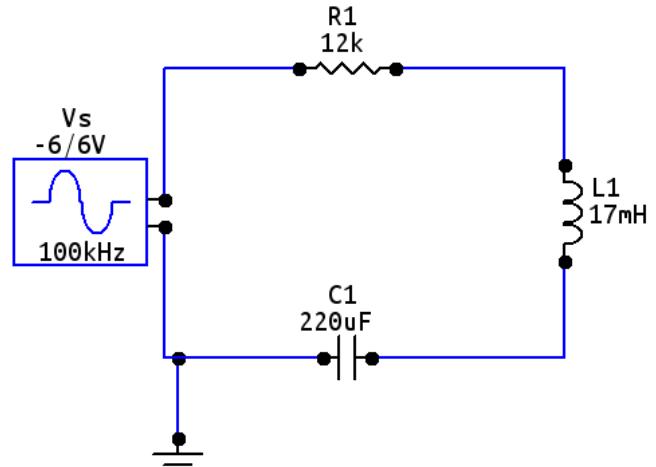


Figure 1. Theoretical circuit model.

Table II
MEASURED VALUES

Parameter	Measured Value	Percent Error
Frequency	(100 \pm 0.2) kHz	0.2%
Resistance	12.12 k Ω	1.0%
Inductance	17.1 μ H	0.6%
Capacitance	222 μ F	0.9%

Changing the coupling mode for this circuit did not exhibit a large change in the oscilloscope reading. This is largely due to the fact that the AC power source used did not have a DC offset. In DC coupling mode, the waveform will tend to oscillate about some positive voltage, i.e., the DC offset. By using AC coupling, the DC portion of the signal is blocked, and the waveform is shown oscillating about 0 volts instead. This can be useful for measuring small perturbations in AC measurements, as it serves as a noise gate that filters the signal and allows for more precise measurements at high magnifications.

1x / 10x Probe:

The probe is a device that allows the user to connect straight to the actual circuit, much like a multimeter, and measure the voltage across an element on the same screen as the oscilloscope. The probe used in this lab had a switch located on the handle that went from 1x to 10x. This switch makes the probe's impedance on the circuit greater by 10 which in return decreases the voltage that the oscilloscope picks up by 10.

This capability is extremely useful when trying to minimize

the amount of error caused by the probes resistance, especially if it is on the same order of magnitude as the resistances used in the circuit. There are many probes out there that have ranges greater than this one and those can be used in specific cases to lessen the errors caused by the probe.

Invert On / Off

On the oscilloscope there is a setting that allows the user to invert the wave about the x-axis. This would effectively reverse the sign of the voltages on a given channel, meaning positive voltages are negative and vice-versa. This setting can be used for comparing certain signals to see if they are exactly 180° out of phase. If invert was used on this instance then the waves would line up exactly.

B. Auto Measuring

RMS Voltage: 4.17 V Peak to Peak Amplitude: 11.8 V
 Period: 1 ms Frequency: (100 ± 0.2) kHz

C. Triggering

Triggering Type:	<u>Edge</u>
Triggering Source:	<u>Ch. 2</u>
Triggering Mode:	<u>Auto</u>

The trigger level dictates at what point the oscilloscope begins measuring a signal. It can be set so that measurement does not begin until some condition is met, such as the presence of a specific level of voltage. This allows for finer control of capturing data. In Edge mode, the trigger is set to activate at the edge of a signal – in this case, when the sine wave rises or falls. The trigger can be set to activate when the voltage condition is met and is either increasing or decreasing. The voltage level at which this occurs can be set via a knob on the scope, and the signal source can be set via the menu system.

It is possible for a circuit to never meet the initial trigger conditions – in this case, the AUTO triggering setting forces the oscilloscope to automatically trigger after a certain amount of time, even if the input conditions are not met. This ensures that a minimum amount of measurements per second will be taken.

D. Signals From Two Channels

The component from the circuit chosen for measurement on Channel 2 was the 17 mH capacitor. The frequency was lowered in order to exhibit a discernible response in the capacitor, and the following measurements were taken with a 35.2 Hz sine wave.

Peak to Peak measurements for both channels:

Ch.1 Peak to Peak (Source):	<u>12.0 V</u>
Ch.2 Peak to Peak (Capacitor):	<u>23.4 mV</u>
Period	<u>28.44 ms</u>

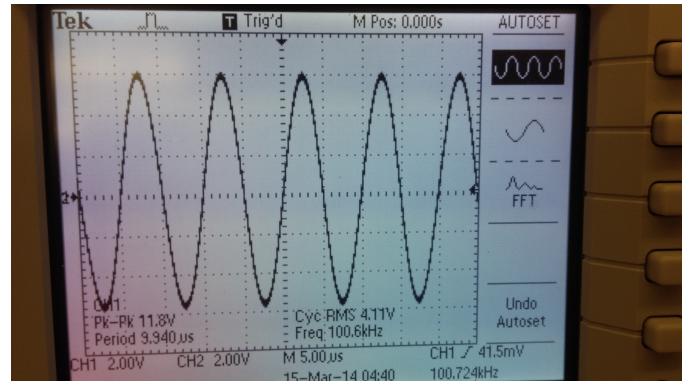


Figure 2. Display of source waveform on the oscilloscope.

Phase shift between both channels:

$$\Delta t = 7.000\text{ms} \quad \theta \approx 1.546 \text{ rad} = 88.58^\circ$$

Where θ is given by $2\pi \frac{\Delta t}{T}$ or $2\pi f \Delta t$ in radians ($360 \frac{\Delta t}{T}$ in degrees), Δt is the distance in the time domain between two identical points on the adjacent waves, T is the period (time in seconds between identical points on a waveform), and f is the frequency of the generator.

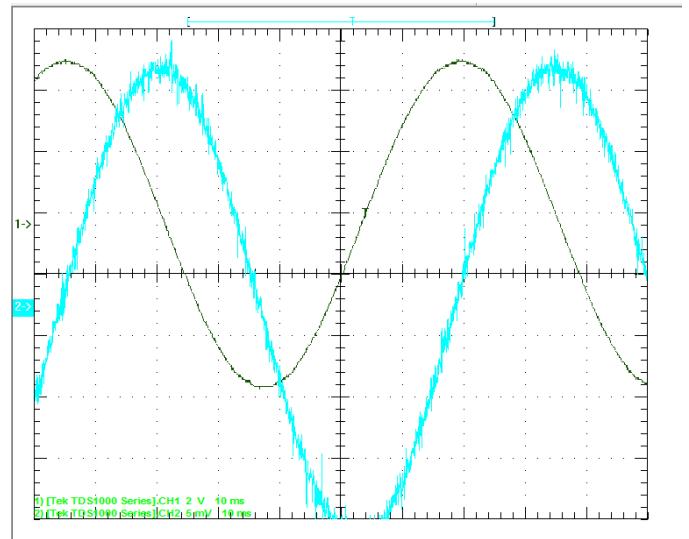


Figure 3. Comparison between waveforms of source voltage and capacitor voltage, scaled up to show phase difference.

III. RESULTS

Table III
 MEASURED VS. THEORETICAL RESULTS FROM CAPACITOR VOLTAGE
 MEASUREMENTS

Parameter	Measured Value	Theoretical Value	Percent Difference
V Peak to Peak	23.4 mV	21.14mV	10%
Period	28.44 ms	28.33 mS	0.39%

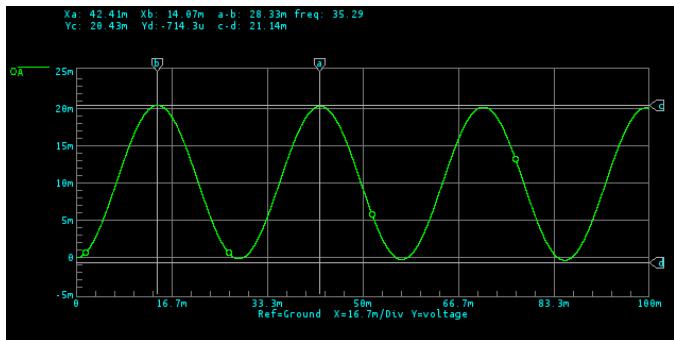


Figure 4. Theoretical capacitor voltage, as modeled in Circuit Maker 6.0

Although the resonant frequency for this circuit, given by $f_0 = 1/2\pi\sqrt{LC}$, was approximately 82.3 Hz, sweeping the circuit through a range of frequencies did not produce resonance or noticeable change the phase shift between the source and the capacitor voltages. Variation of other parameters, such as peak to peak voltage and wave type, likewise did not have an effect on the phase shift.

However, it was expected that the capacitor voltage would lag the source voltage by 90° , and the results were within 1.5% of theory. From the expression for the voltage across the capacitor, $(X_c/Z)V_{max} \sin(\omega t - \pi/2)$

APPENDIX A CIRCUIT PHOTOGRAPH

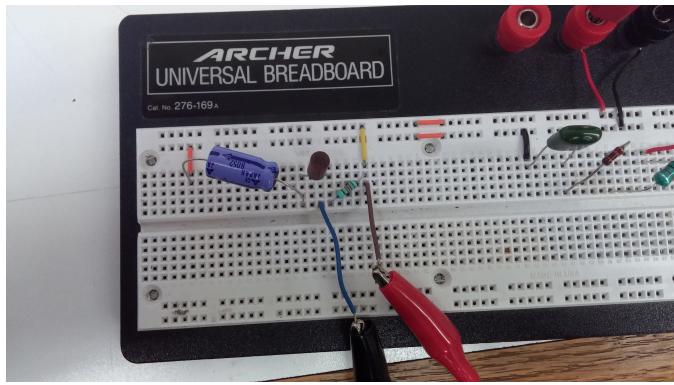


Figure 5. Photograph of Circuit Used