

RLC Circuits

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Abstract—

THE purpose of this experiment is to analyze an RLC circuit driven by alternating current in order to discover the relationships between the frequency at which it is driven, the voltages and currents through its elements, and the power delivered to a resistive load.

We examine the phenomenon of resonance, at which the inductive and capacitive impedance are equal to each other, causing the voltage and current in the circuit to be in phase and maximizing the power delivered. At this point, the overall impedance of the circuit is minimized, and purely resistive.

We will also find the difference between the measured resistance in the circuit and the actual resistance present by analyzing the circuit's response over a range of frequencies, and we will also use this data to compare our theoretical calculation of resonant frequency to a value we can measure.

This will allow us to better understand the relationship between a capacitor and an inductor in a series RLC circuit, and observe the ways in which their behavior is frequency-dependent.

I. THEORY

The following relationships were used in the analysis of this circuit:

$$\omega = 2\pi f$$

$$\omega_0 = \sqrt{\frac{1}{LC}} \Rightarrow f_0 = \frac{1}{2\pi\sqrt{LC}}$$

$$X_L = \frac{V_L}{I}$$

$$X_C = \frac{V_C}{I}$$

$$Z = \frac{V_s}{I}$$

$$L = \frac{X_L}{2\pi f}$$

$$C = \frac{1}{2\pi f X_C}$$

$$\theta = 360^\circ f \Delta t$$

$$P = I_{\text{rms}} V_{\text{rms}} \cos \theta$$

$$Q = \frac{\omega_0 L}{R}$$

$$Q = \frac{f_0}{\Delta f} = \frac{\omega_0}{\Delta \omega}$$

Δf is the width of the resonance peak between the points where $V = \frac{1}{\sqrt{2}} V_{\text{max}}$

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II. METHODOLOGY

- 1) A simple RLC series circuit was constructed. DMMs were wired to measure ΔV_L , and ΔV_C , the voltages of the inductor L and the capacitor C respectively. In order to measure V_R , the resistor was repositioned so one terminal was grounded.
- 2) An ammeter was wired in series with the previous elements to measure I , and an oscilloscope was wired to measure ΔV_s and ΔV_R , the voltages across the source and the resistor R respectively.
- 3) The resistance of R was measured in order to determine phase difference between the source voltage V_s and the current I . Δt between these two signals was recorded.
- 4) The potential difference of the supply was set to a constant 2.0 V (rms), and the ammeter was set to the 430 mA scale.
- 5) The expected resonance frequency f_0 was calculated, and a range of frequencies symmetric about f_0 were chosen for measurement.
- 6) For each frequency f , the following quantities were measured:

- a) I_{rms}
- b) V_L
- c) V_C

Note: Extra data points were taken at frequencies approaching f_0 .

III. RESULTS

Resistance:

$$R = .96\Omega$$

Inductance:

$$L = 2.64\text{mH}$$

Capacitance:

$$C = 9.20\mu\text{F}$$

A. Average Inductance/Capacitance Values

$$L_{\text{avg}} = 2.532 \pm .002 \text{ mH}$$

$$C_{\text{avg}} = 8.833 \pm .009 \mu\text{F}$$

B. Impedance vs. Frequency

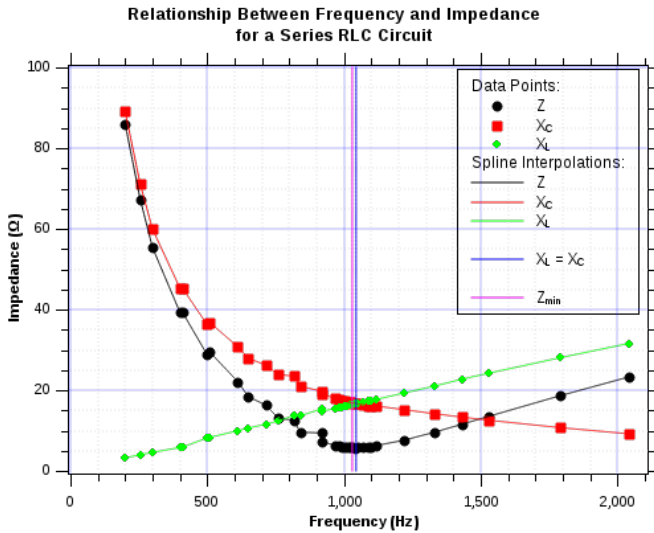


Figure 1. Plot of X_L , X_C , and Z vs. f . The resonant frequency is determined from the intersection of the X_C and X_L curves, and the resistance of the circuit is given at the minimum of the Z curve.

From the plotted data, we have

$$\begin{aligned} Z_{\text{min}} &= R_{\text{meas}} = 5.72\Omega \\ f_{\text{res, meas}} &= 1044 \text{ Hz} \end{aligned}$$

Why is R (meas) not the same value of the resistor used in the circuit?

This indicates that there was resistance in the circuit that was unaccounted for. Possible sources for this resistance are the inductor coils, the internal resistance of the power supply, and erosion or damage to the terminal contacts.

C. Current vs. Frequency

The data points were fitted to the function

$$I_{\text{rms}}(\omega) = \frac{V_{\text{rms}}\omega}{\sqrt{(R\omega)^2 + L^2(\omega^2 - \omega_0^2)^2}} \quad (1)$$

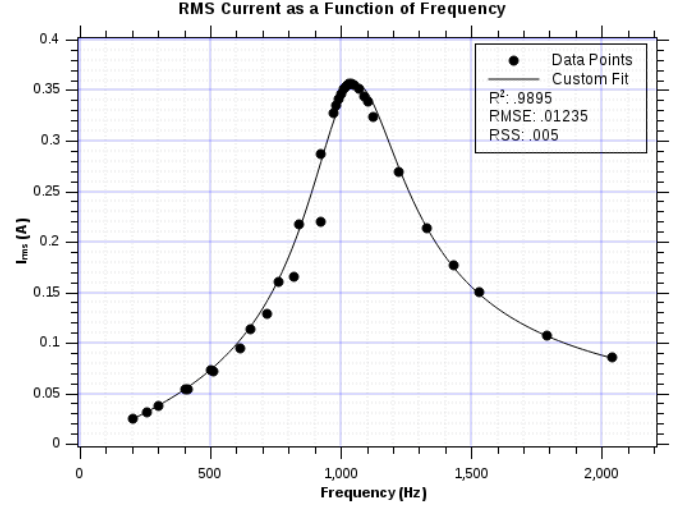


Figure 2. Plot of I_{rms} vs. f . V_{rms} and L_{avg} were used as constants, while R and ω_0 were left as fitting parameters.

From the custom fit described above, the values obtained from the fitting parameters were:

$$\begin{aligned} R_{\text{fit}} &= 5.80 \pm .06\Omega \\ f_{\text{res, fit}} &= 1059 \pm 5 \text{ Hz} \end{aligned}$$

D. Difference between measured and fitted values

What is the percent difference between R (measured) and R (fit)? Is R (measured) within the uncertainty of R (fit)?

$$\text{Percent Difference: } 0.87\%$$

The uncertainty in R (fit) was $.06\Omega$ – however, the values differed by $.08\Omega$. While the two values are generally in good agreement, there are slight errors introduced in the interpolation used to find Z (min) on the graph, as well as a certain amount of random error introduced by transient voltage fluctuations when the circuit is near resonance. However, both values are relatively consistent with each other, so we deduce that this value more accurately represents the actual resistance present in the circuit than the value measured with the ohmmeter.

E. Theoretical Resonant Frequency

The resonant frequency, calculated from the mean values of L and C from step 1 is given by

$$f_{\text{res, theor}} = \frac{1}{2\pi\sqrt{L_{\text{avg}}C_{\text{avg}}}} \quad (2)$$

So the theoretical resonant frequency is:

$$f_{0(\text{theor})} = 1064 \text{ Hz}$$

Comparing this to the values determined in Steps 2 and 3 above, we have

$$\begin{aligned} \% \text{ Difference, Step 2: } & 1.9\% \\ \% \text{ Difference, Step 3: } & 0.47\% \end{aligned}$$

F. Voltage vs Frequency

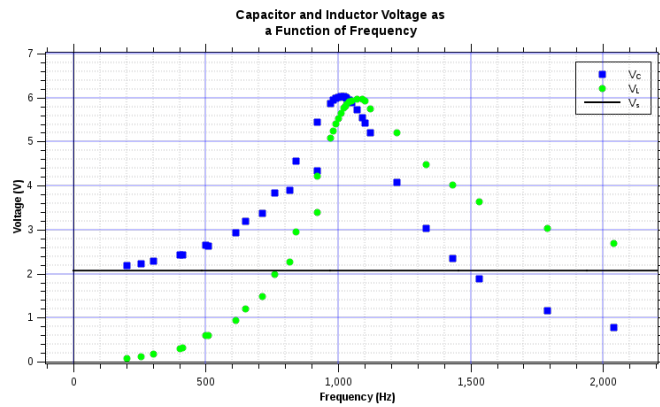


Figure 3. Plot of V_L and V_C vs f , extrapolated to zero frequency.

What does this graph tell you?

Several things are apparent from the graph – the most immediate characteristic of the series RLC circuit is that in the limit of low frequencies, the voltage on the capacitor V_C approaches the source voltage. This agrees with our theoretical understanding of the behavior of a capacitor, which would tend to act as a short circuit at low frequencies. This effect can also be recovered by considering the equation for the impedance of the capacitor, $X_C = 1/2\pi fC$, which predicts that the impedance approaches infinity as the frequency approaches zero. An infinite impedance is effectively a short, necessarily implying that no current will flow and the capacitor will be forced to remain at the potential difference of the source. This is reflected in the graph, as the low-frequency limit of the capacitor voltage approaches the source voltage.

A symmetric but opposite is observed in the inductor, for which the voltage V_L approaches zero in the low-frequency limit. From the impedance equation $X_L = 2\pi fL$, we see that in this limit, the impedance approaches zero – effectively creating a shunt in the circuit. This causes the inductor to behave like a wire, which agrees with our theoretical model of inductance.

In the limit of high frequencies, the behavior of the inductor and the capacitor switch roles. Both voltages peak symmetrically about the resonant frequency, and the capacitor voltage then approaches zero while the inductor voltage approaches the source voltage. These effects can be seen by considering the limit as f approaches infinity in the previously mentioned inductance equations, and noting that the capacitor's impedance tends to zero while the inductor's impedance tends to infinity. This leads to the capacitor now behaving like a

wire, and the inductor like a shunt, which again agrees with our theoretical model of impedance.

The last important characteristic of this graph shows that the maximum voltage is delivered at the resonant frequency. This occurs midway between the maximums the two individual curves, and represents the point at which maximum power is delivered.

G. Phase Angle vs. Frequency

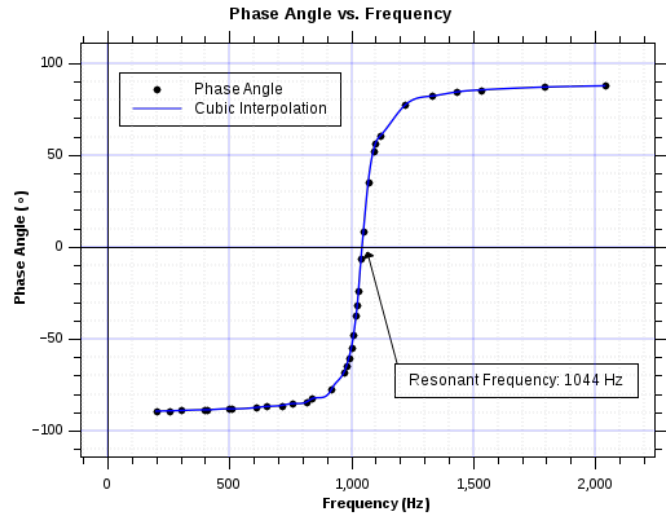


Figure 4. Plot of phase angle ($360^\circ f \Delta t$) vs. f . The y-intercepts represents the frequency at which the current and the voltage are in phase, and corresponds to the circuit's resonant frequency.

Does this curve agree with theory? Explain.

The curve agrees with theory in several ways. The most noticeable factor is that the phase angle is equal to zero at exactly the same value found in previous fits. This agrees with the notion that having the current and voltage in phase results in circuit resonance. The graph also shows that the phase angle is negative below resonance, and positive above resonance, approaching $\pm 90^\circ$ as f approaches ∞ and 0 respectively. From the expression

$$\phi = \frac{X_L - X_C}{R} = \frac{\omega L - \frac{1}{\omega C}}{R}, \quad (3)$$

we see that as the frequency approaches infinity, X_C goes to zero and we expect the circuit to be primarily inductive. In this case, we would expect the voltage to lead the current, which is reflected in the positive phase angle above resonance.

As the frequency approaches zero, X_L goes to zero, and we would expect the circuit to be more capacitive. This would lead to the current leading the voltage – or in other words, the voltage lagging the current. This is also reflected in the negative phase angles below resonance.

H. Power vs. Frequency

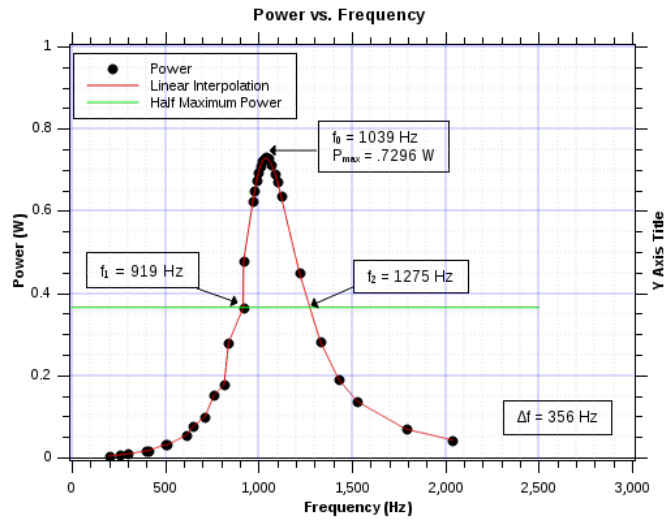


Figure 5. Plot of P vs f , used to determine the full-width at half-max in order to calculate Q .

Bandwidth: $\Delta f = 111.5 \text{ Hz}$

I. Quality Factor

The calculated quality factor is given by

$$Q_{\text{calc}} = \frac{f_{\text{max, meas}}}{\Delta f} \quad (4)$$

The theoretical Quality factor is given by

$$Q = \frac{\omega_0 L}{R_{\text{meas}}}, \quad (5)$$

where ω_0 is given by $2\pi f_{\text{res, meas}}$ from Step 2 and L is L_{avg} from Step 1.

And the uncertainty in Q is given by

$$\sqrt{\left(\frac{\partial Q}{\partial f}\right)^2 (\delta f)^2 + \left(\frac{\partial Q}{\partial \Delta f}\right)^2 (\delta \Delta f)^2} \quad (6)$$

Assuming an uncertainty of 1 Hz in both f and Δf , we have:

Calculated Quality Factor: $Q_{\text{calc}} = 2.919 \pm .009$

Theoretical Quality Factor: $Q = 2.904$

Percent Difference: 0.52%
