1. 自我介绍

面试官您好，我是丁志文，目前就读于杭州电子科技大学，所学的专业是电子科学与技术，在校期间负责导师的一个项目，在项目中主要工作是改善材料的制备工艺以提高材料的载流性能，以及每个月项目汇报的ppt制作，具有较强的数据分析处理能力，能够熟练操作office软件。以上就是我的自我介绍，谢谢。

Thank you so much for giving me this opportunity to have this job interview. I would like to introduce myself to you briefly. My name is Zhiwen Ding. I’m 24 years old, and I come from Jiangxi Province. I’m currently studying for my master’s degree at Hangzhou Dianzi University of electronic science and technology. My major is electronic science and technology, and I will graduate in July next year. Although my major is not related to teaching, it doesn't matter. Since I was a child, I was always at the top of my class in math. When I was in high school, my dream was to become a math teacher. I had learned several lessons related to math during my university, such as advanced mathematics, probability theory, methods in mathematical physics, and so on. In addition, I had a two-month experience as a math teacher during a summer holiday. As a result, I have a good command of how to teach and manage students. I believe that I will get along well with students, and I will be patient with them. I’m quite interested in math teacher, so I applied for this job. I sincerely hope to get the chance to work for your company. Thank you for your attention.

1. 项目

设计SoC系统AHB-Lite总线：1x Master，2x Slaves；设计AHB到APB的转接bridge。设计SoC系统APB总线：可支持7 APB外设；

1. RISC-V架构五级流水线CPU实现

使用Verilog语言完成基于RISC-V架构的CPU开发，实现五级流水线架构。