LY-CAM-G005A

Modification records

	Version	Date	Modification	Name	Check
1	VER A	2023/04/28	New	许辉	

<Variant Name>

Title	VER A	
Size C	Document Number LY-SGM-F008A	Rev A
Date: Saturday, May 25, 2024	Sheet 1 of 19	

<Variant Name>

Title	BLOCK DIAGRAM	
Size: C	Document Number: LY-SGM-A001A	Rev: A
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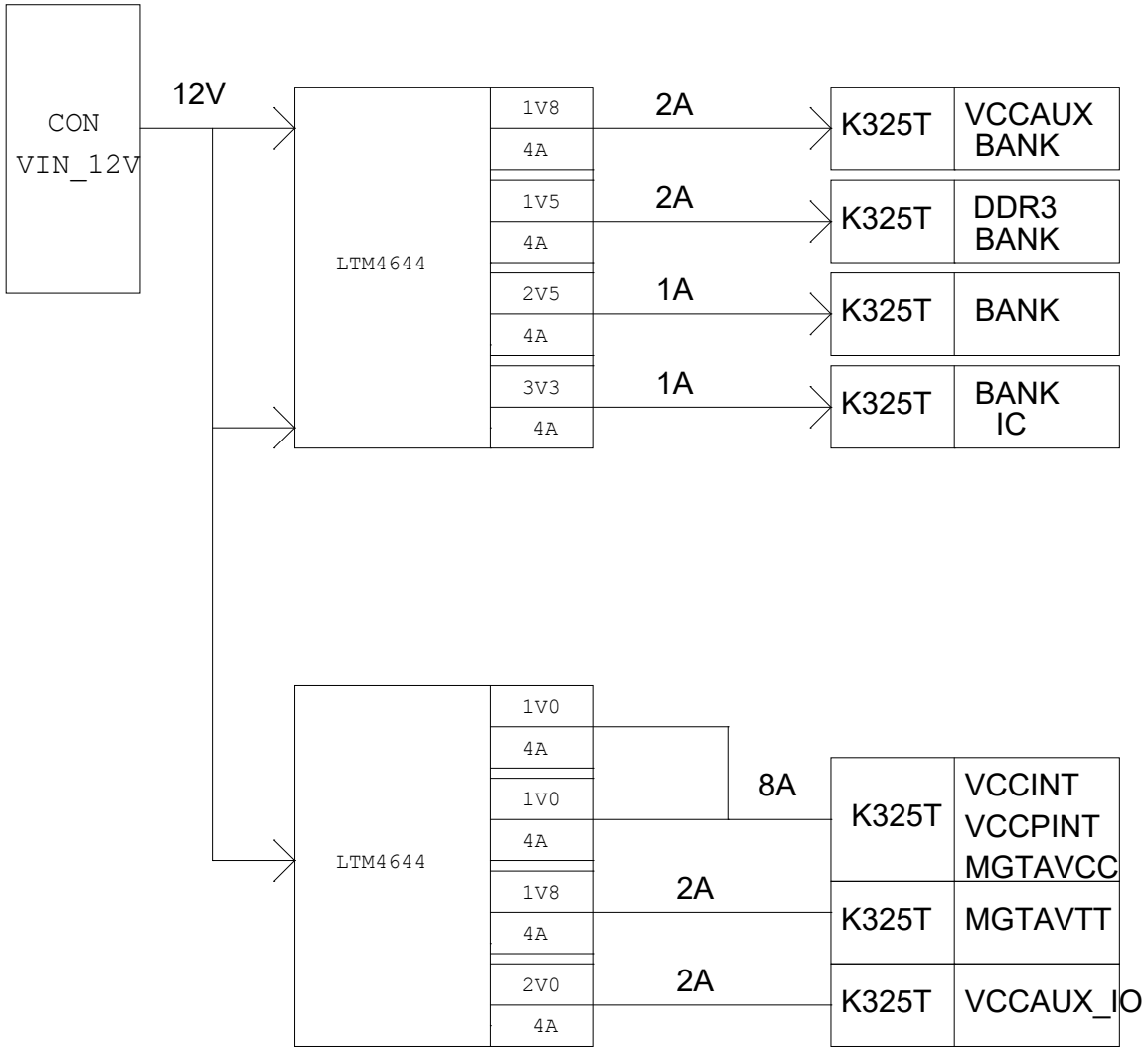
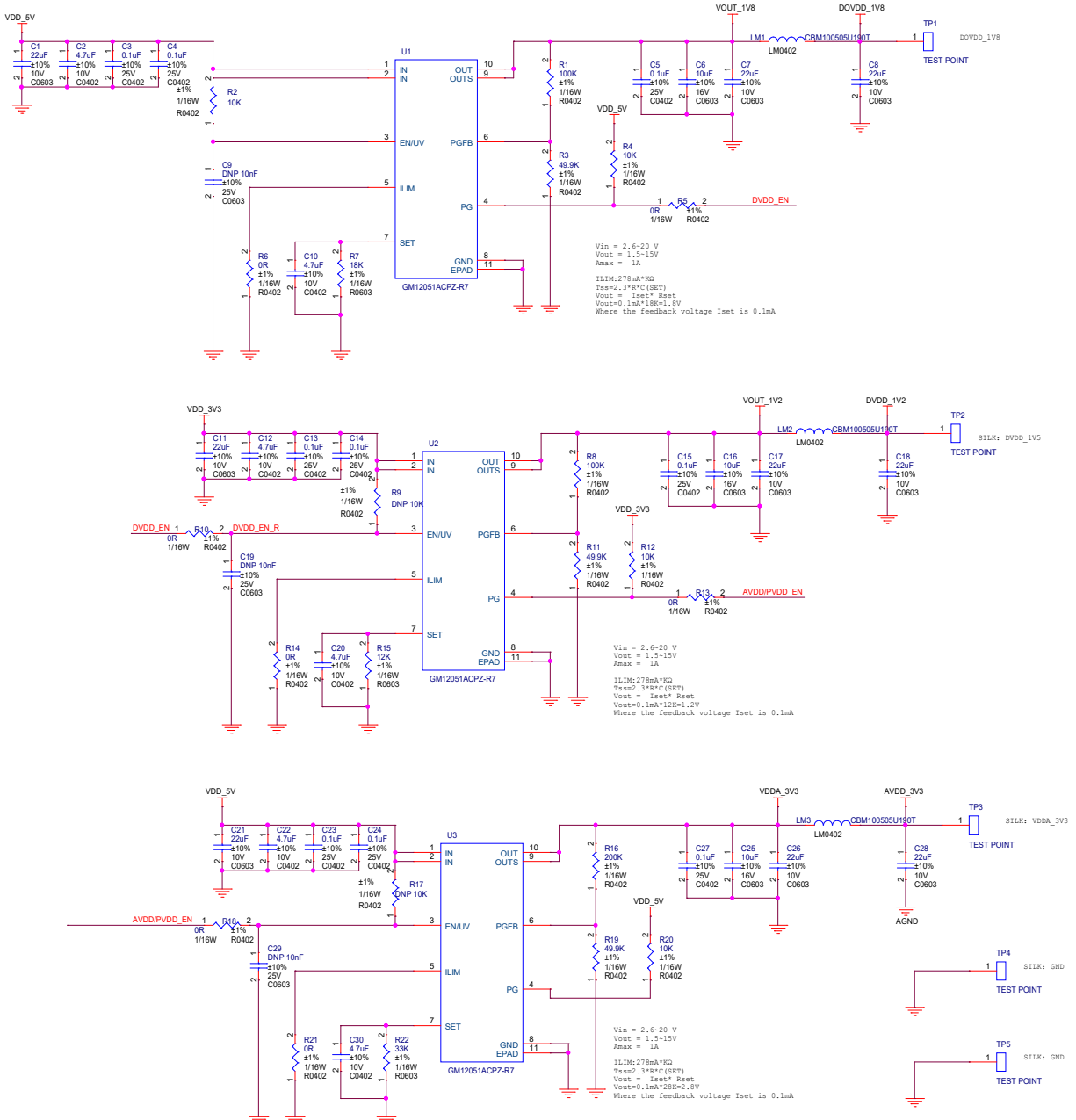


<Variant Name>

Title	POWER TREE	
Size C	Document Number LY-SGM-A001A	Rev A
Date: Saturday, May 25, 2024	Sheet 3 of 19	



POWER ON : VDD_5V0 --> VDDIO_1V8 --> DVDD_1V2-->AVDD_3V3

<Variant Name>

Title	MAIN PWR	
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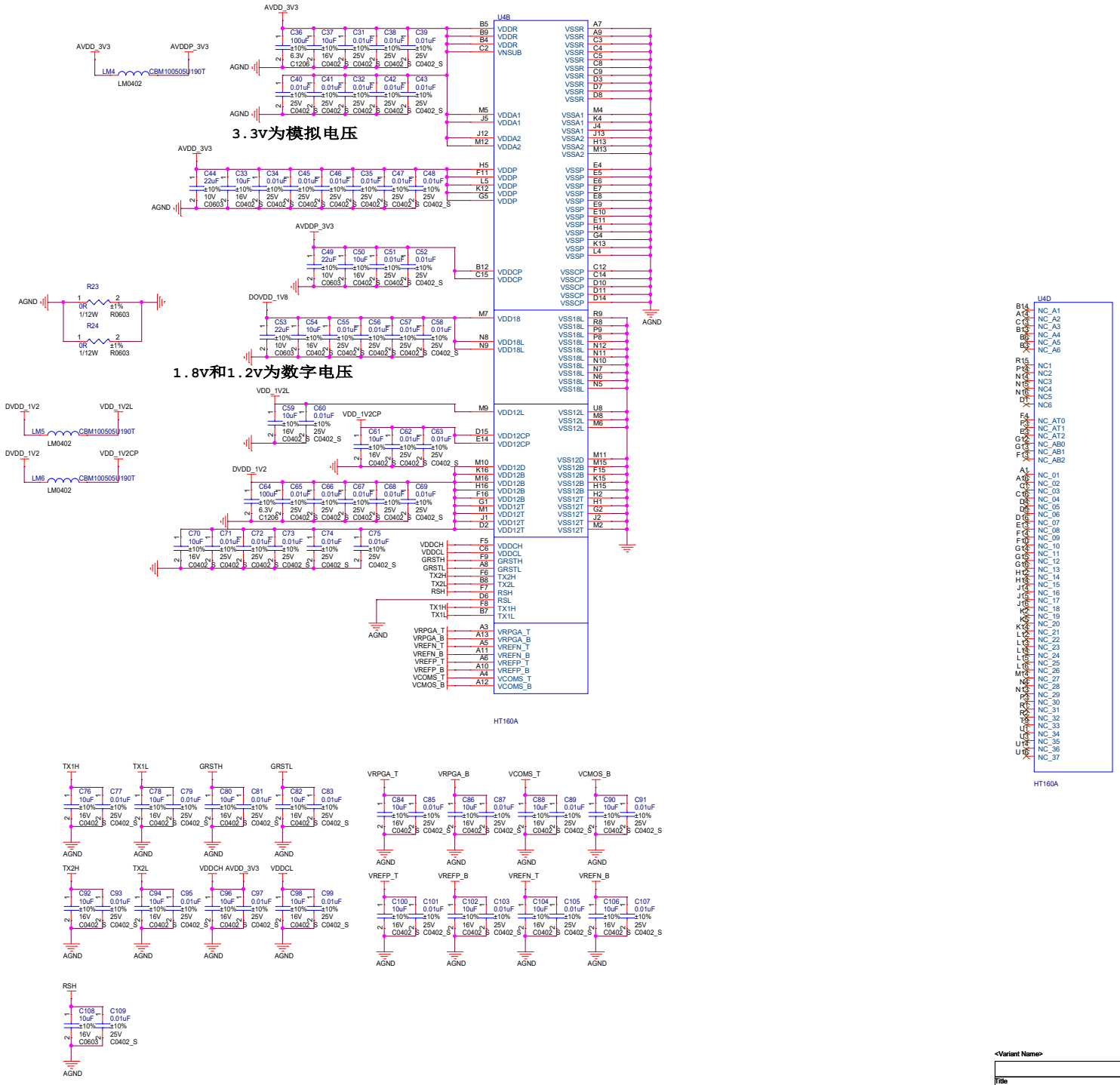
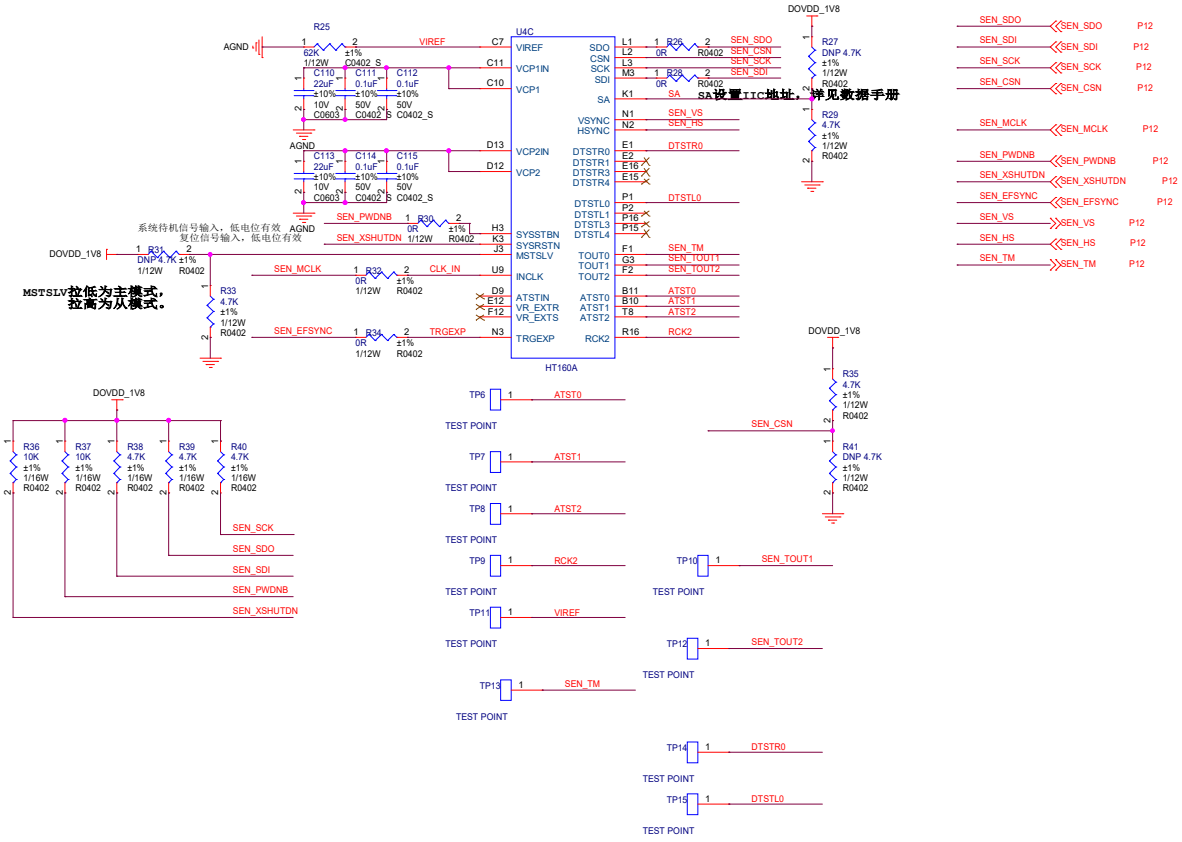
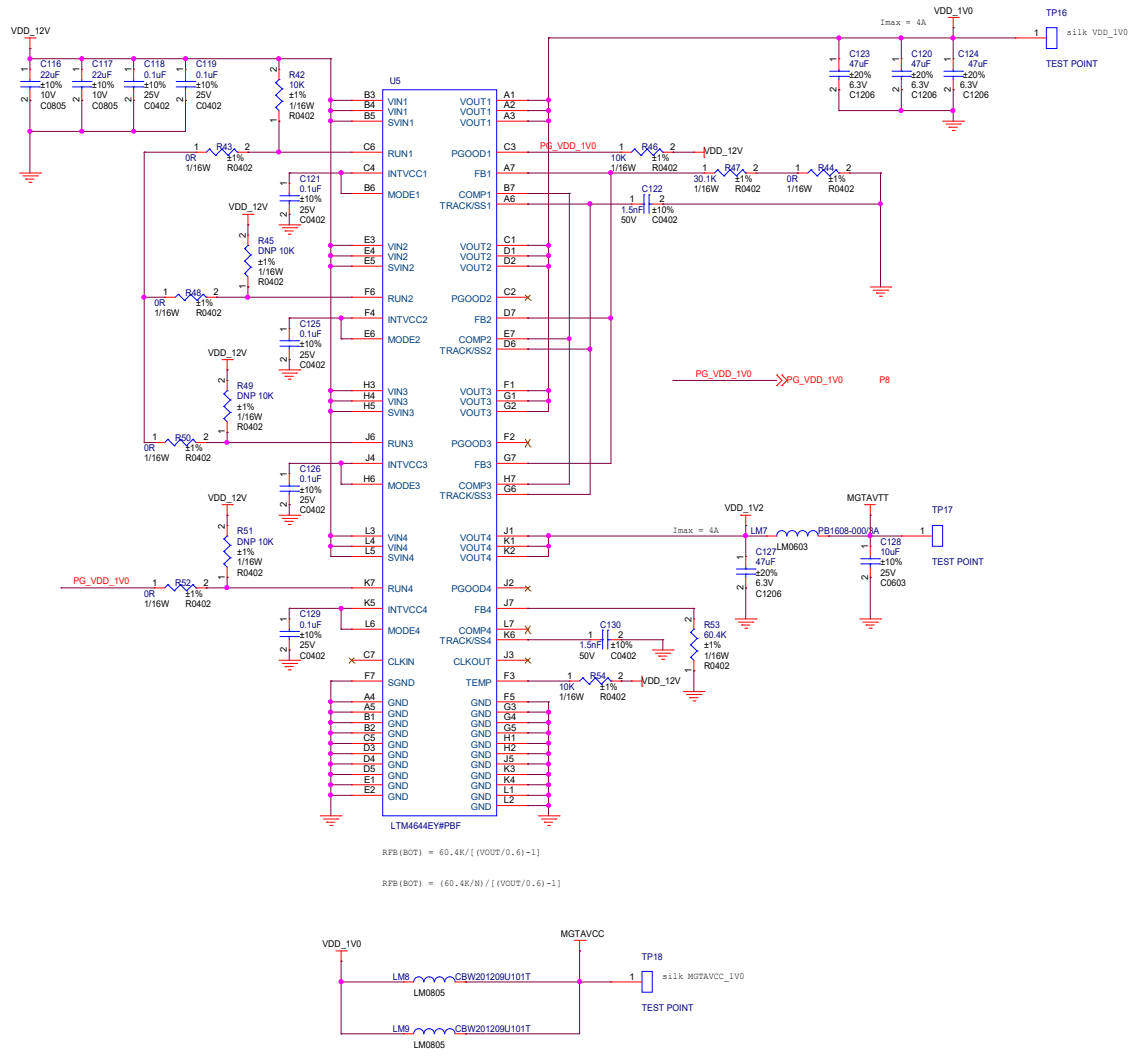

3.3v为模拟电压
1.8v和1.2v为数字电压
HT160A
HT160A



<Variant Name>

Title	CONNECTOR	
Size C	Document Number LY-SEN-A001A	Rev A
Date: Saturday, May 25, 2024	Sheet 6 of 19	



<Variant Name>

Title	MAIN PWR	
Size C	Document Number LY-SGM-A001A	Rev A
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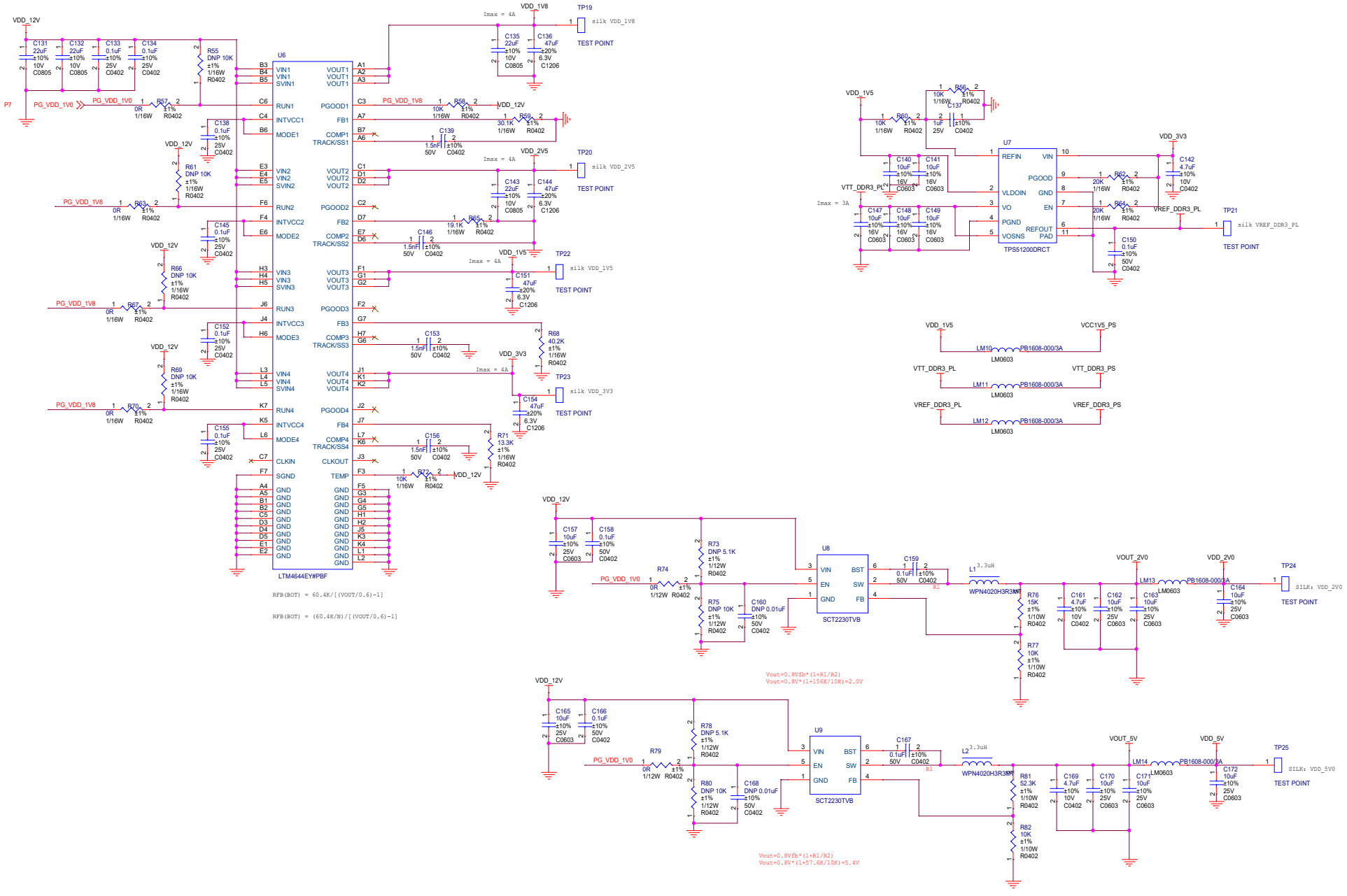

PL/PS : VDD_1V0 --> VDD_1V8 /VDD_2V0--> VDD_1V5/2V5/3V3;
GTX : VDD_1V0 == MGTAVCC --> MGTAVTT

<variant_name>

Title	GROUP POWER	
Size C	Document Number LY-SGM-A001A	Rev A
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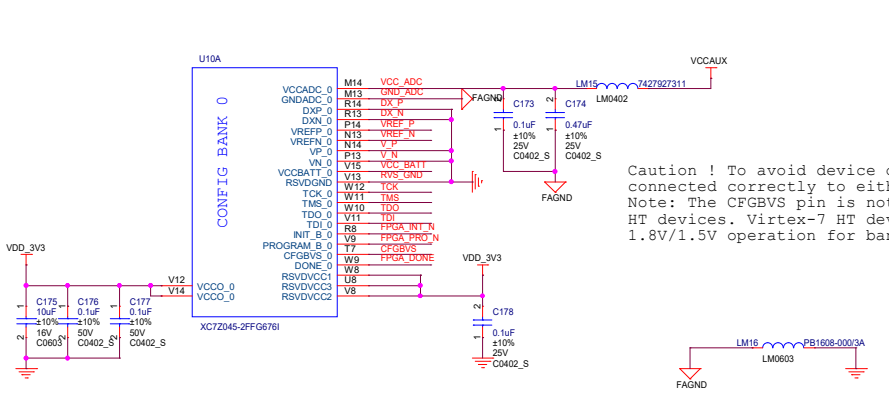
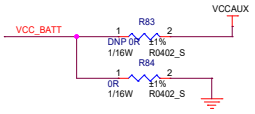
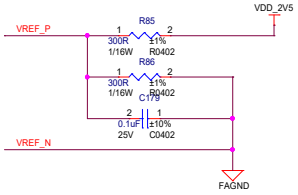
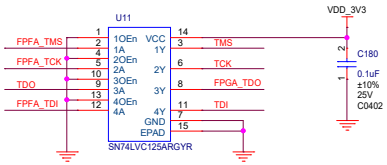


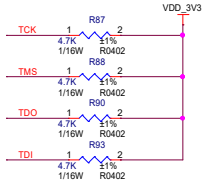
Caution ! To avoid device damage, CFGBVS must be connected correctly to either VCCO_0 or GND.
Note: The CFGBVS pin is not available on Virtex-7 HT devices. Virtex-7 HT devices support only 1.8V/1.5V operation for bank 0.



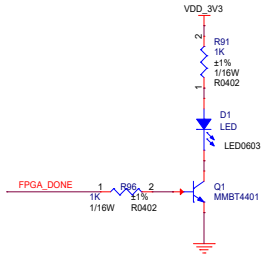


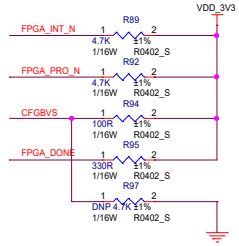
FPGA JTAG





FPGA CONFIG





<Variant Name>

Title	7Z035_BANK0	
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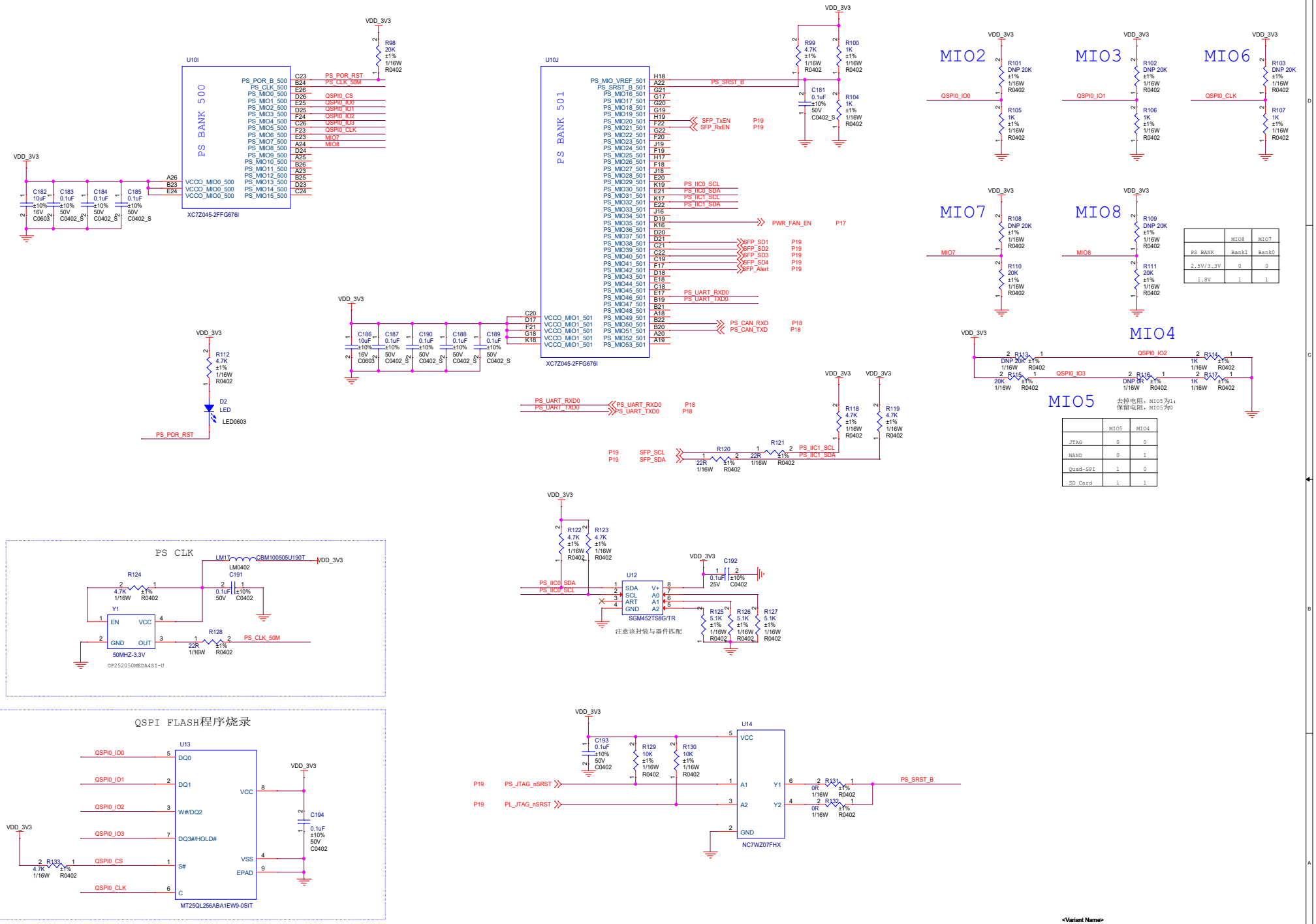

	MIO8	MIO7
PS_BANK	Bank1	Bank0
2.5V/3.3V	0	0
1.8V	1	1

	MIO5	MIO4
JTAG	0	0
NAND	0	1
Quad-SPI	1	0
SD Card	1	1

<Variant Name>

Title	7Z035_BANK500-501	
Size C	Document Number LY-SGM-A001A	Rev A
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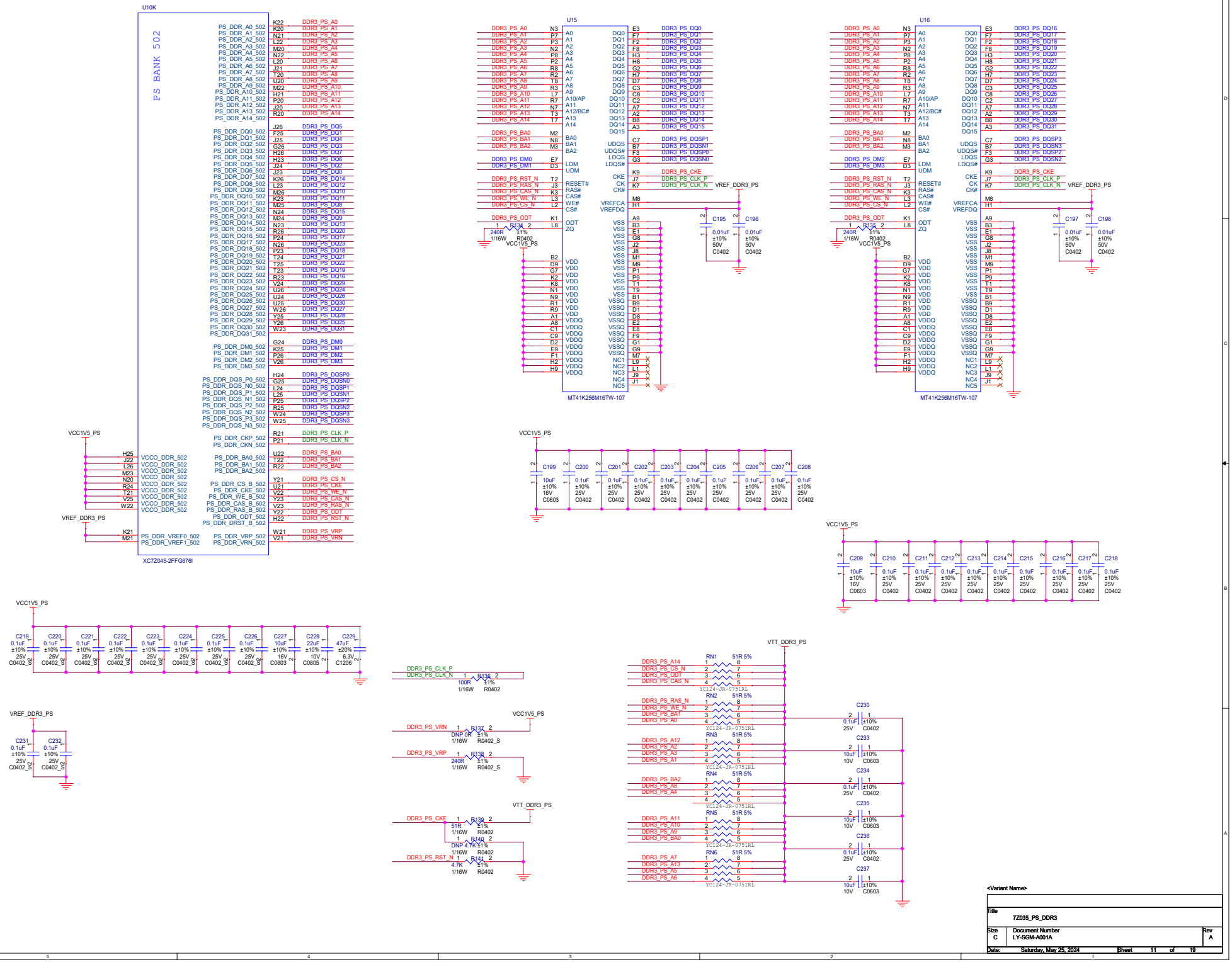
U10K
PS BANK 502
XC7Z045-2FFG676I
U15
U16
MT41K256M16TW-107
VCC1V5_PS
VREF_DDR3_PS
VTT_DDR3_PS
C195
C196
C197
C198
C199
C200
C201
C202
C203
C204
C205
C206
C207
C208
C209
C210
C211
C212
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C214
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C216
C217
C218
C219
C220
C221
C222
C223
C224
C225
C226
C227
C228
C229
C230
C231
C232
C233
C234
C235
C236
C237
R134
R135
R136
R137
R138
R139
R140
R141
RN1
RN2
RN3
RN4
RN5
RN6
YC124-JR-0751RL
<Variant Name>
Title
7Z035_PS_DDR3
Size C
Document Number LY-SGM-A001A
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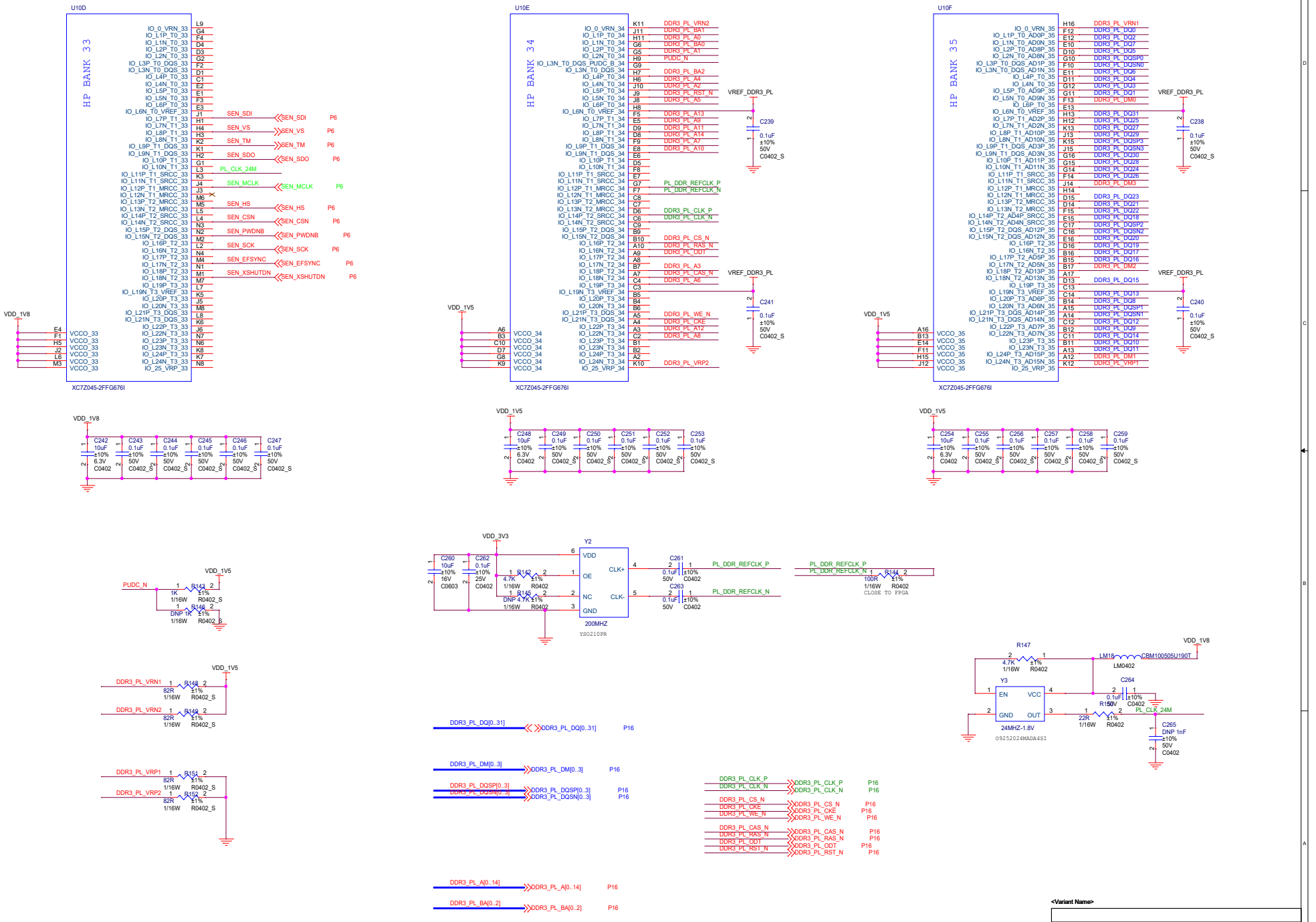

<Variant Name>

Title	7Z035_HP BANK	
Size C	Document Number LY-SGM-A001A	Rev A
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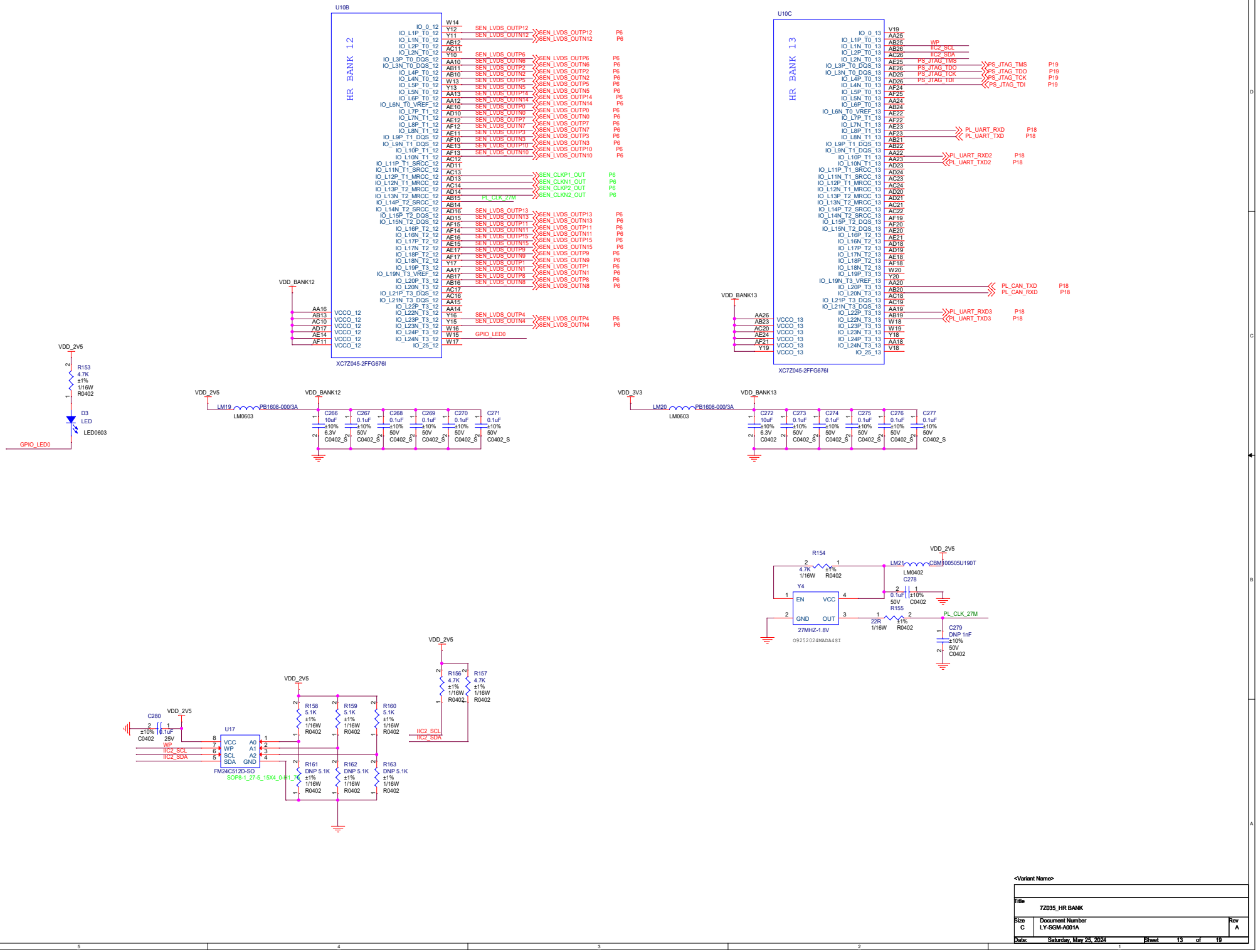

<Variant Name>

Title	7Z035_HR BANK	
Size C	Document Number LY-SGM-A001A	Rev A
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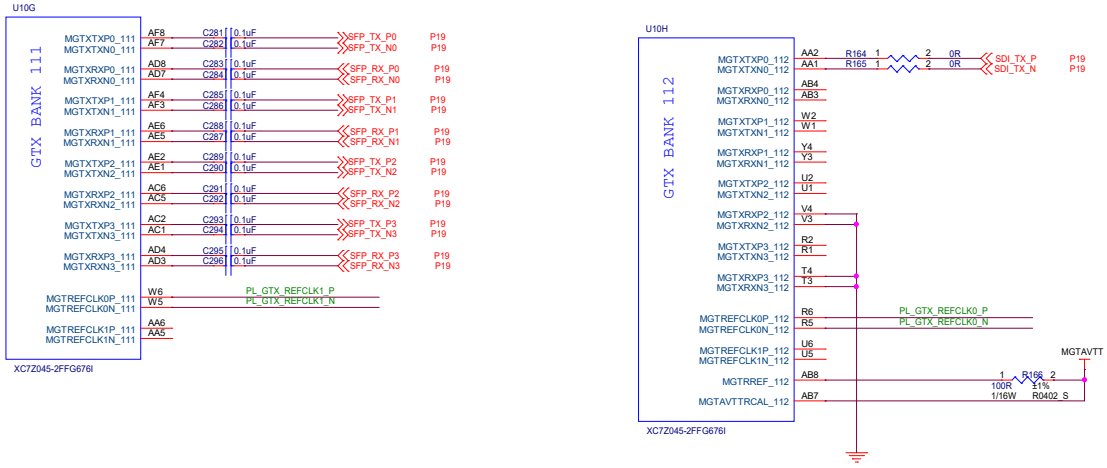

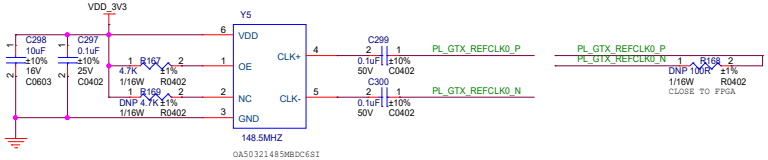

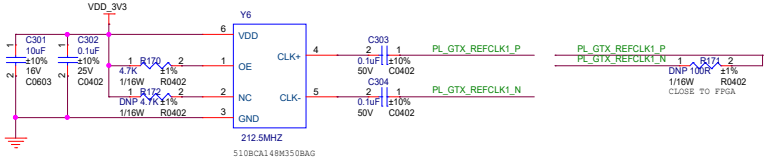

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Title	7Z035_GTX BANK	
Size C	Document Number LY-SGM-A001A	Rev A
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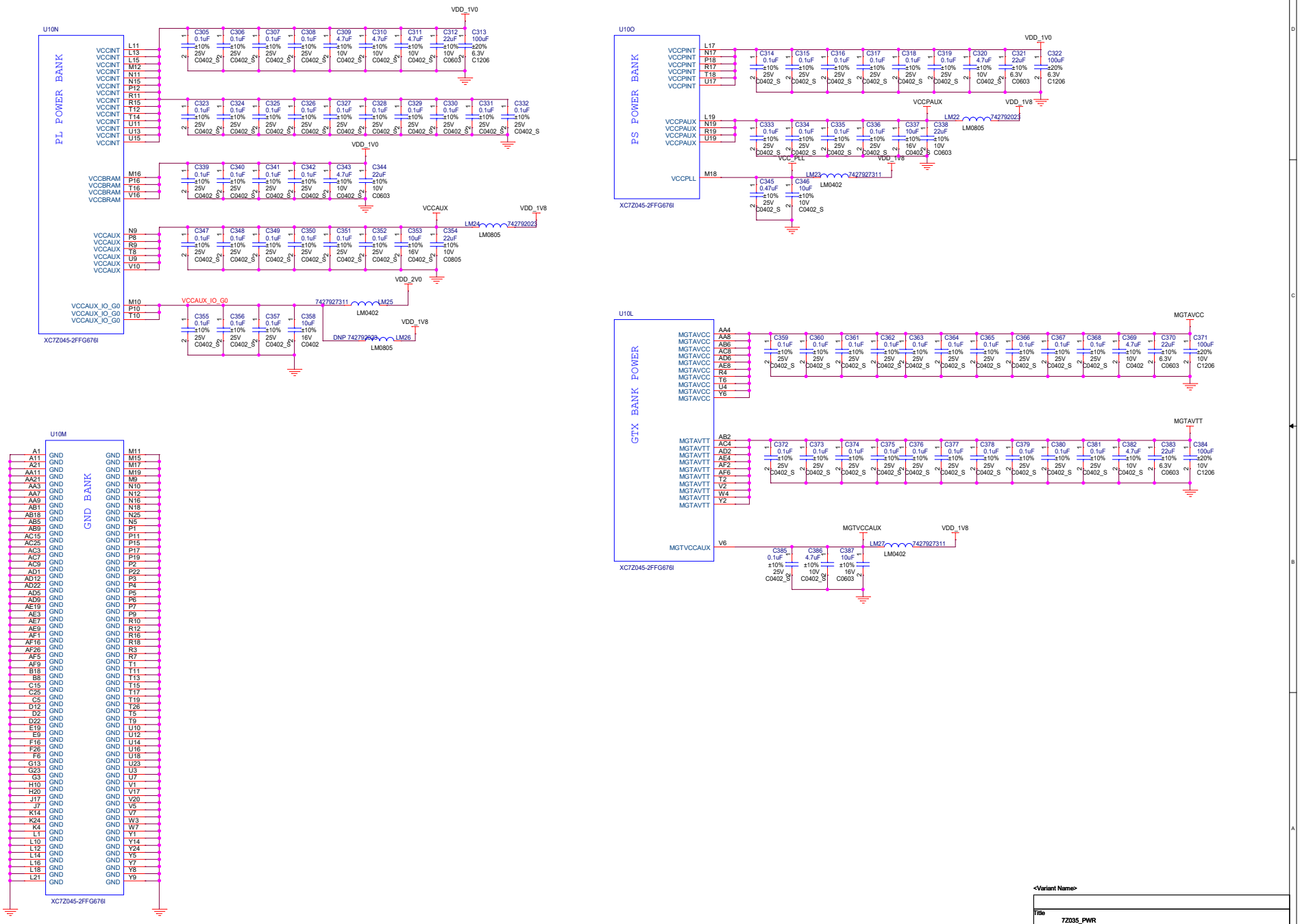

<Variant Name>	
Title	7Z035_PWR
Size: C	Document Number: LY-SGM-A001A
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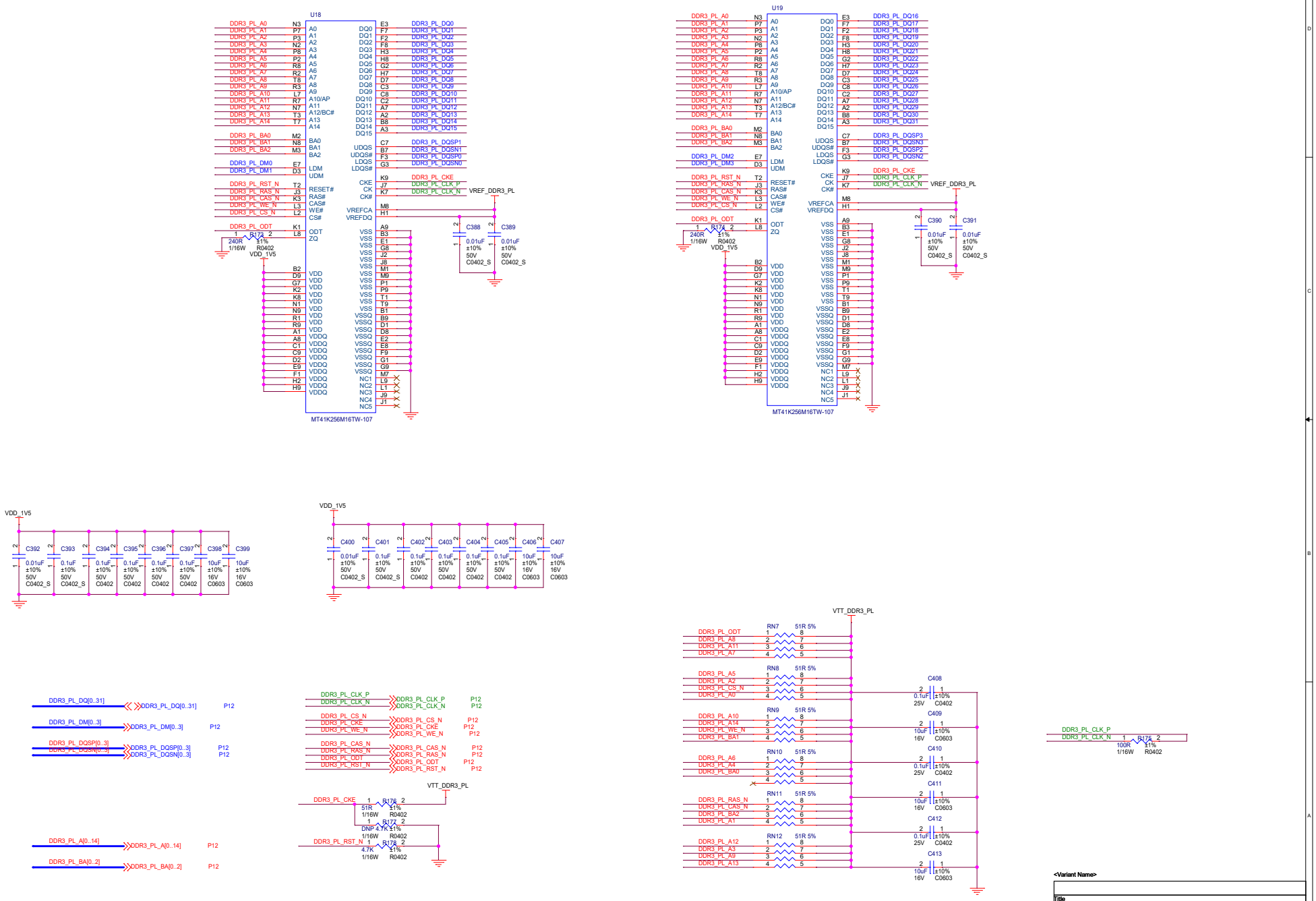

<variant_name>

Title	BLOCK DIAGRAM	
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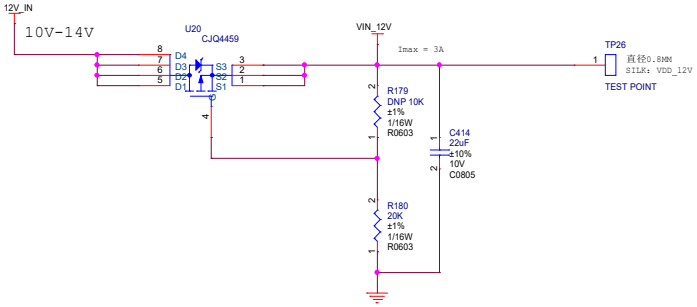
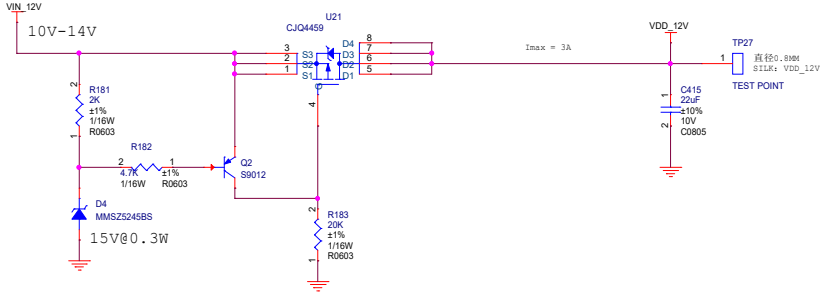
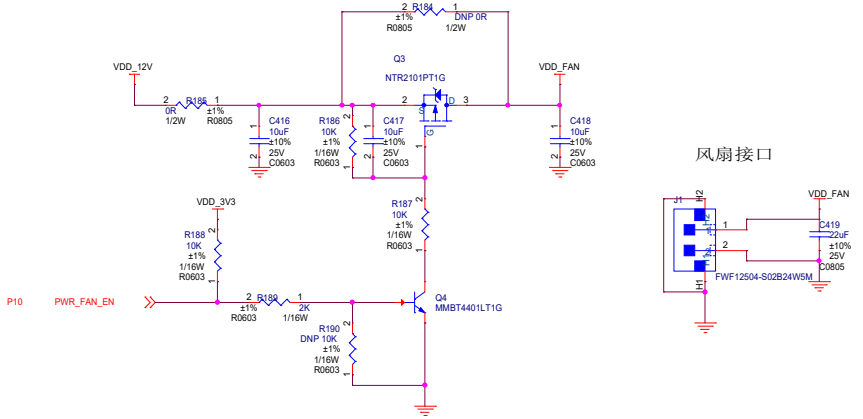
电源防反接电路



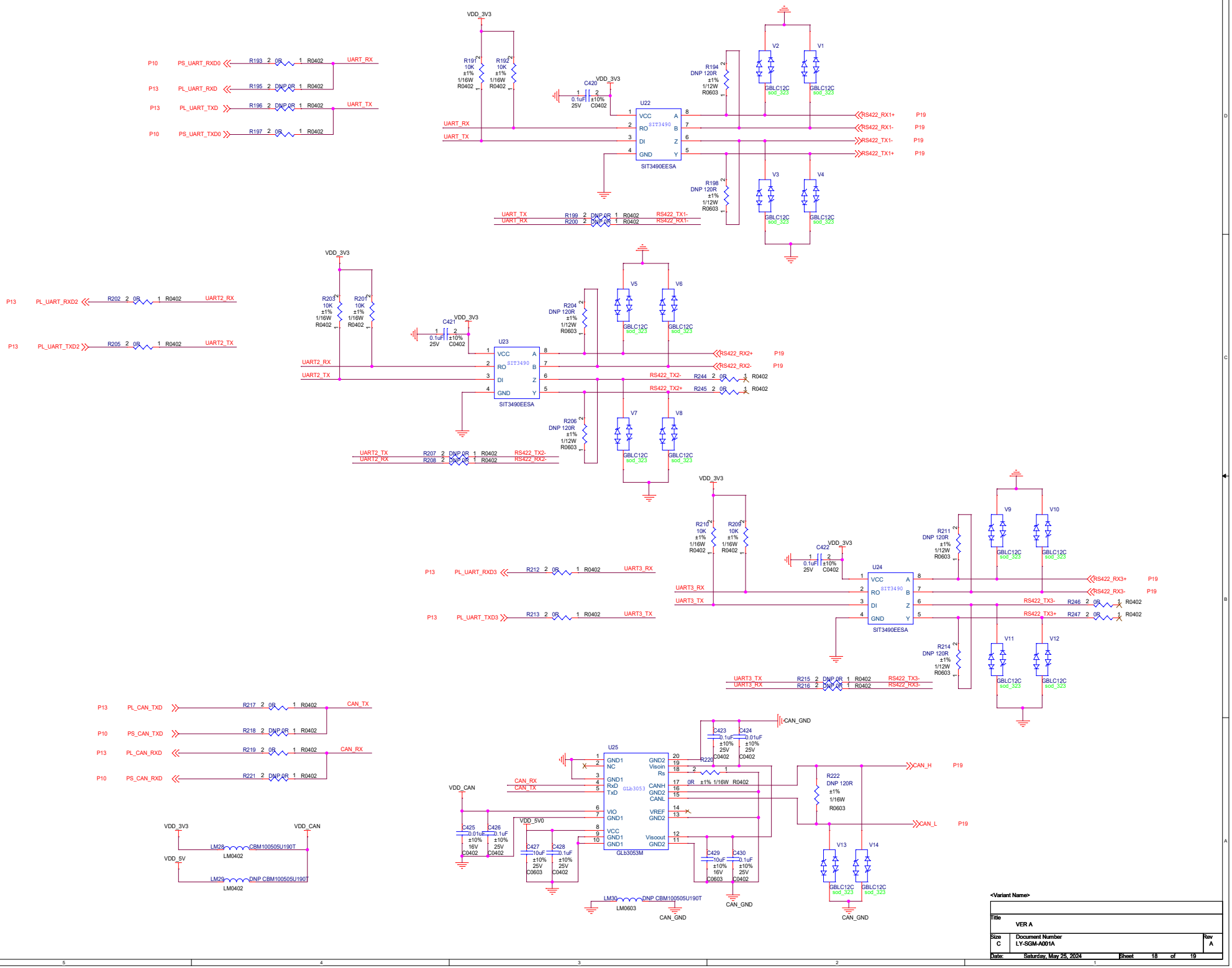
电源防过压电路





风扇接口

图像数据接口


P10 PS_UART_RXD0 R193 2 0R 1 R0402 UART_RX
P13 PL_UART_RXD R195 2 DNP 0R 1 R0402
P13 PL_UART_TXD R196 2 DNP 0R 1 R0402 UART_TX
P10 PS_UART_TXD0 R197 2 0R 1 R0402
VDD_3V3
R191 10K ±1% 1/16W R0402
R192 10K ±1% 1/16W R0402
C420 0.1uF 25V ±10% C0402
U22 SIT3490 SIT3490EESA
VCC RO DI GND A B Z Y
UART_RX UART_TX
R194 DNP 120R ±1% 1/12W R0603
R198 DNP 120R ±1% 1/12W R0603
V1 V2 V3 V4 GBLC12C sod_323
RS422_RX1+ P19
RS422_RX1- P19
RS422_TX1- P19
RS422_TX1+ P19
UART_TX R199 2 DNP 0R 1 R0402 RS422_TX1-
UART_RX R200 2 DNP 0R 1 R0402 RS422_RX1-
P13 PL_UART_RXD2 R202 2 0R 1 R0402 UART2_RX
P13 PL_UART_TXD2 R205 2 0R 1 R0402 UART2_TX
R203 10K ±1% 1/16W R0402
R201 10K ±1% 1/16W R0402
C421 0.1uF 25V ±10% C0402
U23 SIT3490 SIT3490EESA
R204 DNP 120R ±1% 1/12W R0603
R206 DNP 120R ±1% 1/12W R0603
V5 V6 V7 V8
RS422_RX2+ P19
RS422_RX2- P19
RS422_TX2- R244 2 0R 1 R0402
RS422_TX2+ R245 2 0R 1 R0402
UART2_TX R207 2 DNP 0R 1 R0402 RS422_TX2-
UART2_RX R208 2 DNP 0R 1 R0402 RS422_RX2-
P13 PL_UART_RXD3 R212 2 0R 1 R0402 UART3_RX
P13 PL_UART_TXD3 R213 2 0R 1 R0402 UART3_TX
R210 10K ±1% 1/16W R0402
R209 10K ±1% 1/16W R0402
C422 0.1uF 25V ±10% C0402
U24 SIT3490 SIT3490EESA
R211 DNP 120R ±1% 1/12W R0603
R214 DNP 120R ±1% 1/12W R0603
V9 V10 V11 V12
RS422_RX3+ P19
RS422_RX3- P19
RS422_TX3- R246 2 0R 1 R0402
RS422_TX3+ R247 2 0R 1 R0402
UART3_TX R215 2 DNP 0R 1 R0402 RS422_TX3-
UART3_RX R216 2 DNP 0R 1 R0402 RS422_RX3-
P13 PL_CAN_TXD R217 2 0R 1 R0402 CAN_TX
P10 PS_CAN_TXD R218 2 DNP 0R 1 R0402
P13 PL_CAN_RXD R219 2 0R 1 R0402 CAN_RX
P10 PS_CAN_RXD R221 2 DNP 0R 1 R0402
VDD_3V3 VDD_CAN VDD_5V
LM28 CBM100505U190T LM0402
LM29 DNP CBM100505U190T LM0402
U25 GLb3053 GLb3053M
GND1 NC RxD TxD VIO GND1 VCC GND1 GND1 GND2 Visoin Rs CANH GND2 CANL VREF GND2 Visout GND2
CAN_RX CAN_TX
C423 0.1uF ±10% 25V C0402
C424 0.01uF ±10% 25V C0402
R220 0R ±1% 1/16W R0402
C425 0.01uF ±10% 16V C0402
C426 0.1uF ±10% 25V C0402
C427 10uF ±10% 25V C0603
C428 0.1uF ±10% 25V C0402
C429 10uF ±10% 16V C0603
C430 0.1uF ±10% 25V C0402
R222 DNP 120R ±1% 1/16W R0603
V13 V14 GBLC12C sod_323
CAN_H P19
CAN_L P19
LM30 DNP CBM100505U190T LM0603
CAN_GND
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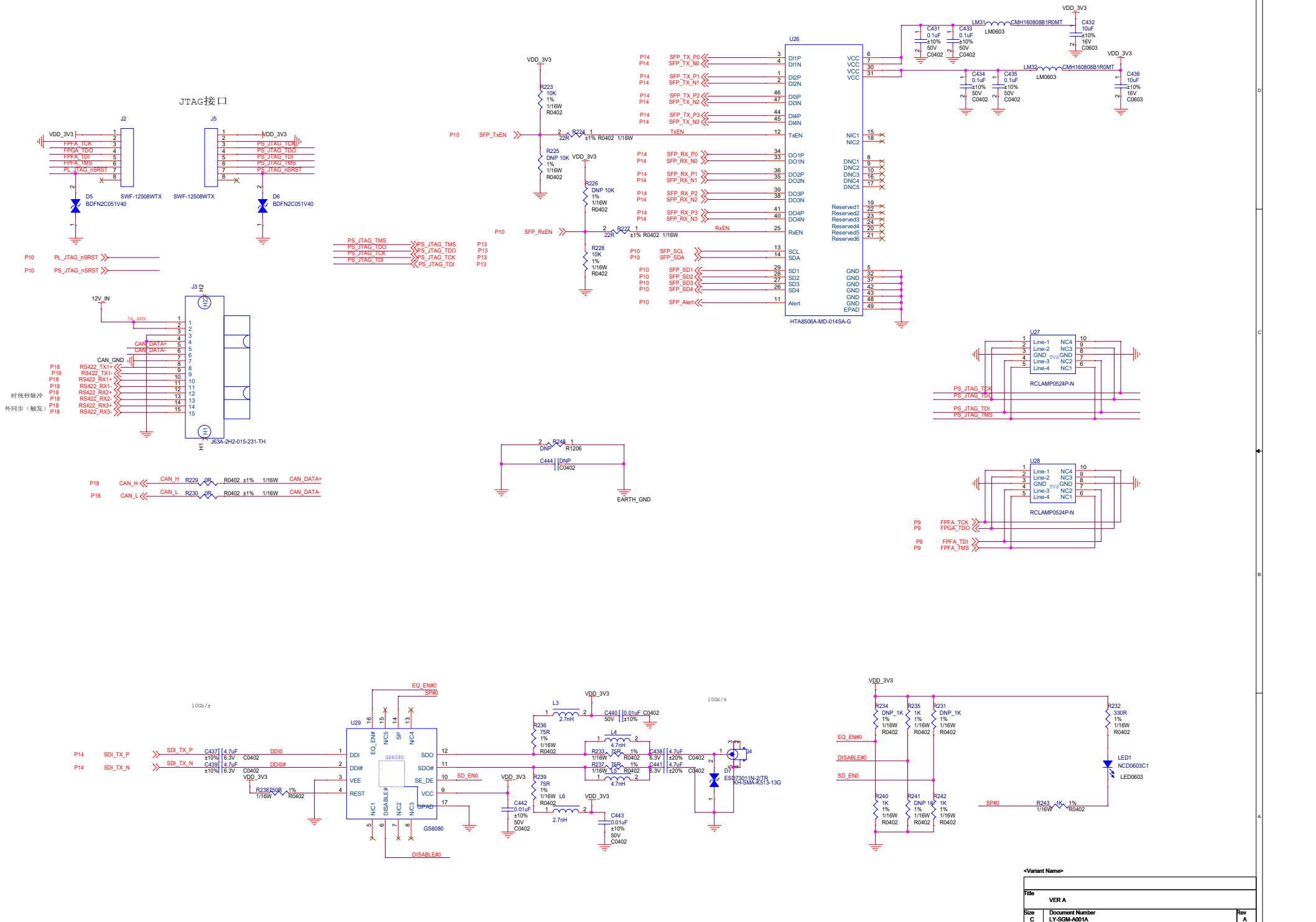
JTAG接口
J2
J5
VDD_3V3
FPFA_TCK
FPGA_TDO
FPFA_TDI
FPFA_TMS
PL_JTAG_nSRST
PS_JTAG_TCK
PS_JTAG_TDO
PS_JTAG_TDI
PS_JTAG_TMS
PS_JTAG_nSRST
SWF-12508WTX
D5
BDFN2C051V40
D6
P10
P13
U26
HTA8506A-MD-014SA-G
SFP_TxEN
SFP_RxEN
SFP_SCL
SFP_SDA
SFP_Alert
U27
U28
RCLAMP0524P-N
J3
12V_IN
3A AMX
CAN_DATA+
CAN_DATA-
CAN_GND
RS422_TX1+
RS422_TX1-
RS422_RX1+
RS422_RX1-
RS422_RX2+
RS422_RX2-
RS422_RX3+
RS422_RX3-
时统秒脉冲
外同步（触发）
J63A-2H2-015-231-TH
CAN_H
CAN_L
CAN_DATA+
CAN_DATA-
EARTH_GND
U29
GS6080
SDI_TX_P
SDI_TX_N
EQ_EN#0
SP#0
DISABLE#0
SD_EN0
LED1
NCD0603C1
LED0603
<Variant Name>
VER A
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