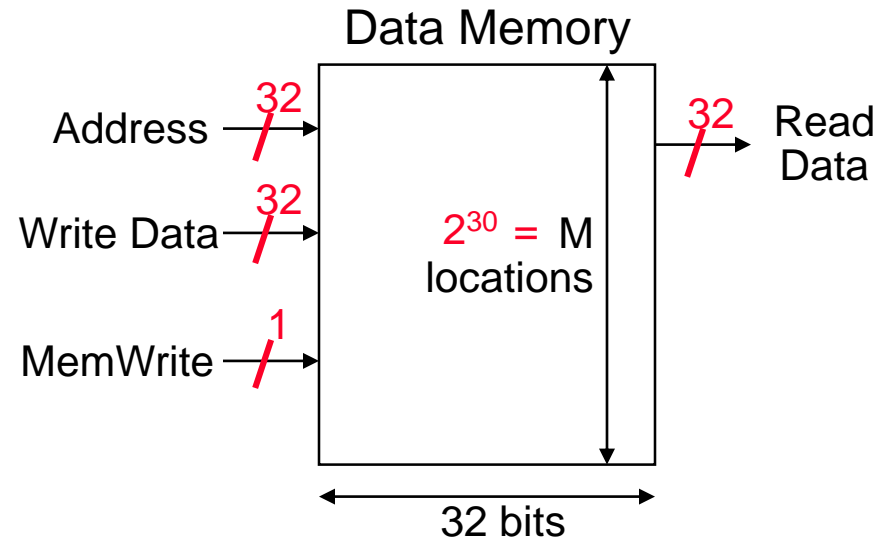
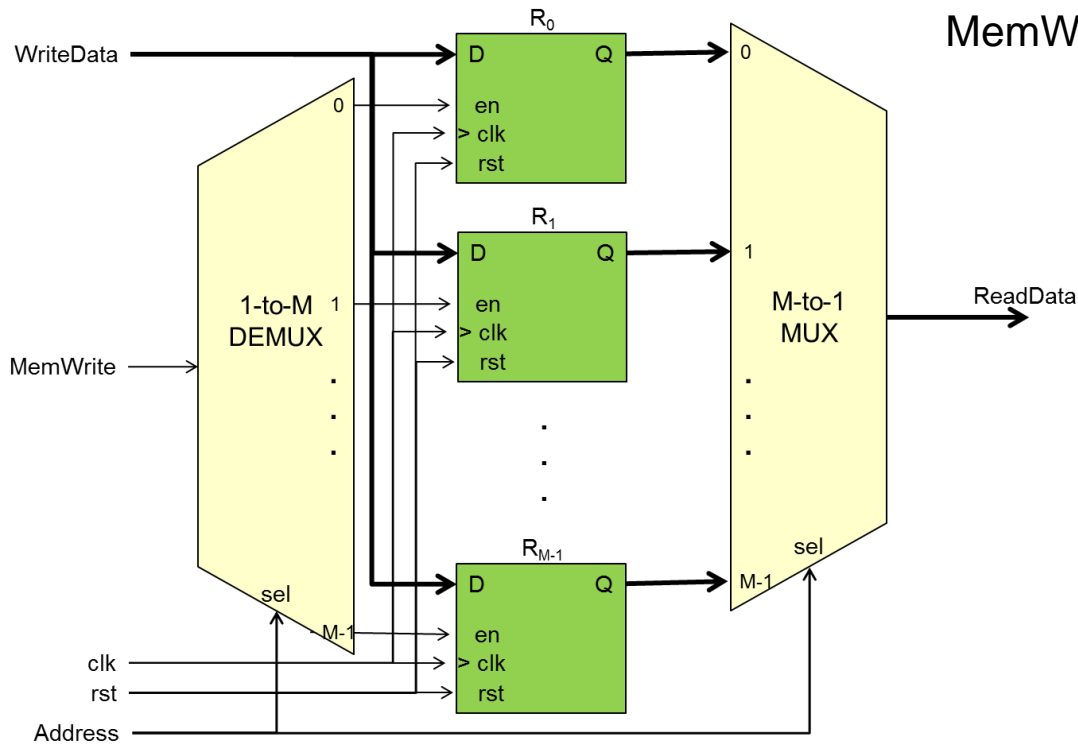


# MIPS Data Memory

## ❑ M 32-bit registers

- | One read port
- | One write port



# MIPS Data Memory

