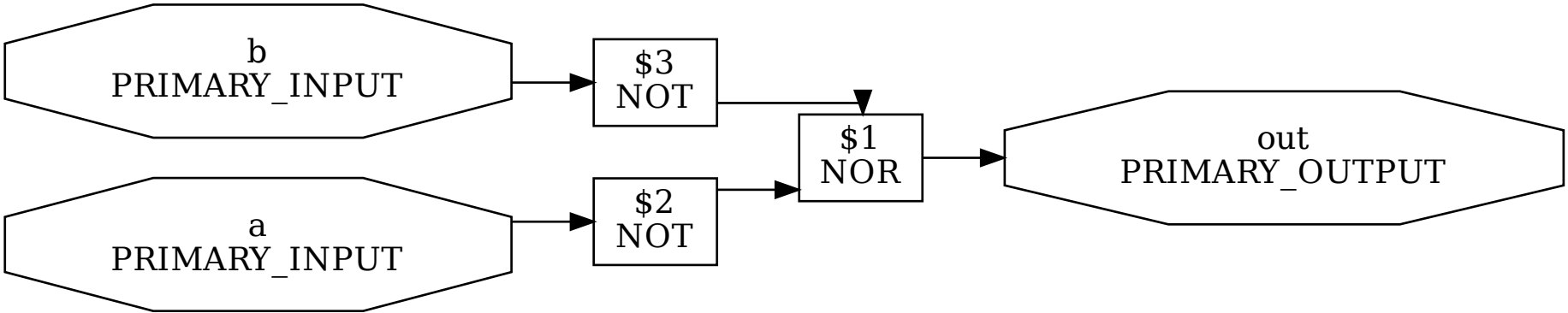
b
PRIMARY_INPUT
a
PRIMARY_INPUT
$3
NOT
$2
NOT
$1
NOR
out
PRIMARY_OUTPUT

and_gate