

Revision History:



Date: Revision: Comments:

Initial Revision
Additional Notes:

Important Notes about this Schematic:

DESIGN NOTE:
Design notes in gray text boxes give
information about design
requirements and descisions.

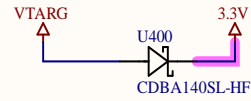
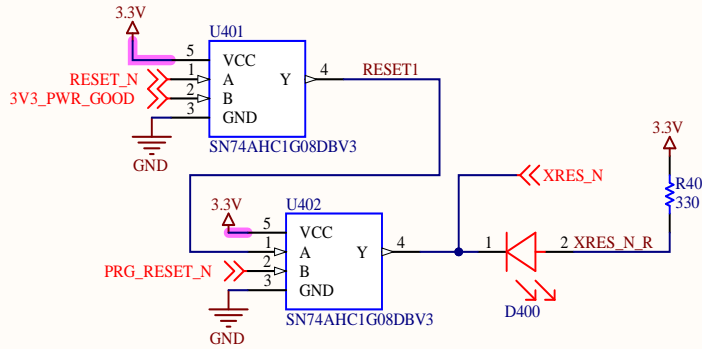
LAYOUT NOTE:
Layout notes in red text boxes give
information regarding layout
requirements.

Revision History and Table of Contents

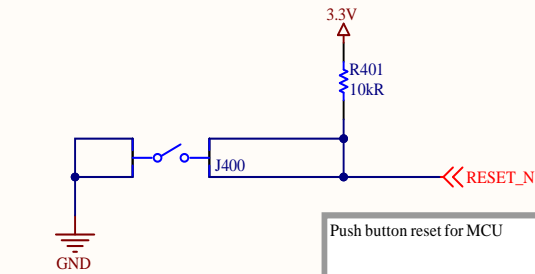


Title		
Size	Number	Revision
A		
Date: 8/11/2022		Sheet of
File: C:\Users\...\01-Revision_History_and_Content		8 of 8
Scale:		

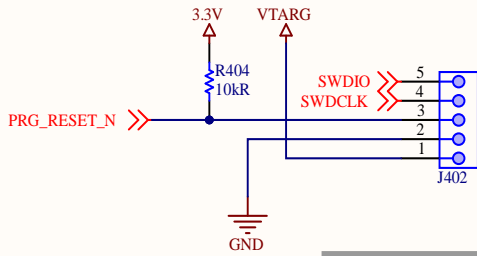
The MCU can be reset by 1 of 3 sources
 1. Programmer
 2. Push Button Reset
 3. 3.3V Voltage Monitor



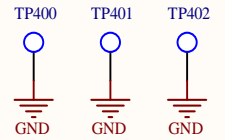
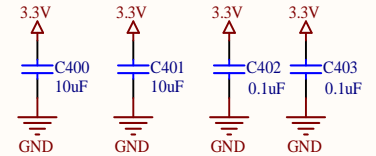
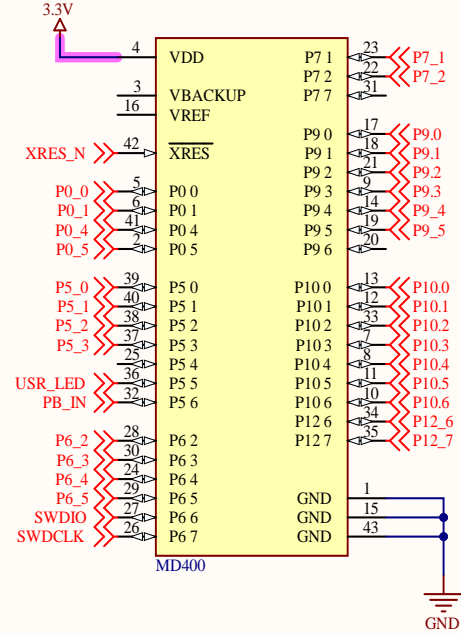
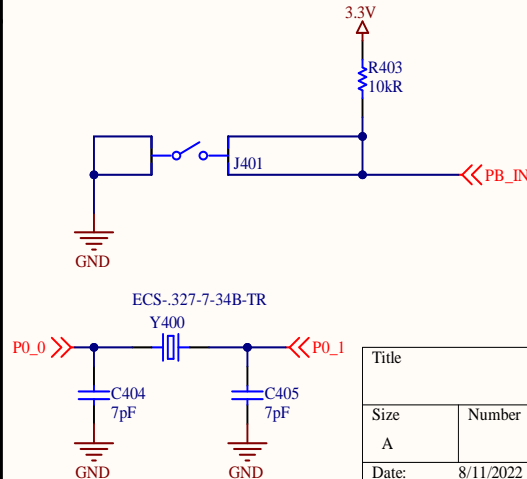
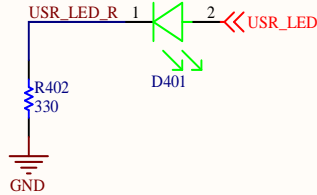
Diode ensures the the programmer target voltage is not connected directly to the 3.3V supply



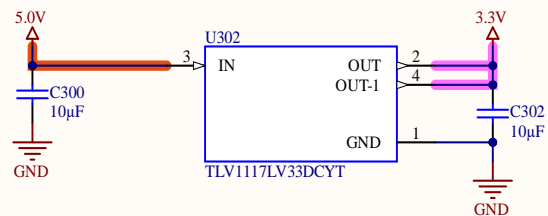
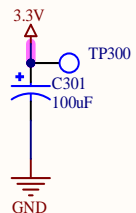
Push button reset for MCU



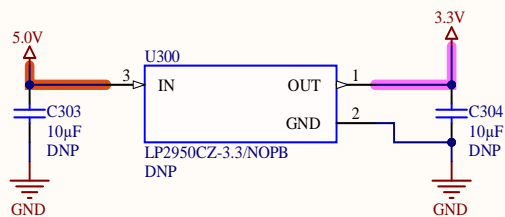
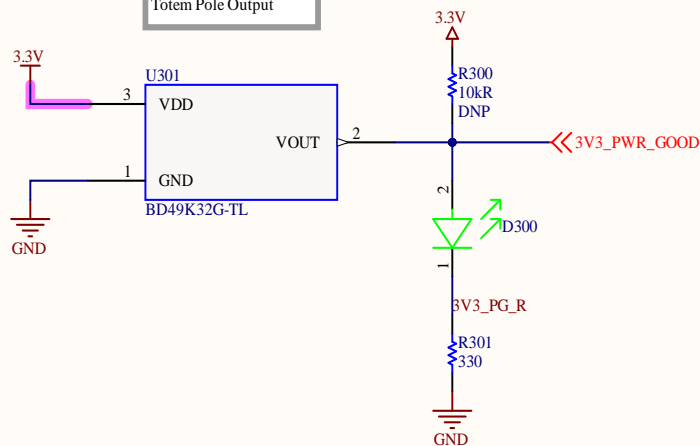
Header for the MiniProg4 Programmer.
 The programmer can be used to supply 3.3V to the MCU.



Title		
Size	Number	Revision
A		
Date:	8/11/2022	Sheet of
File:	C:\Users\...\04-MCU.SchDoc	Drawn By:



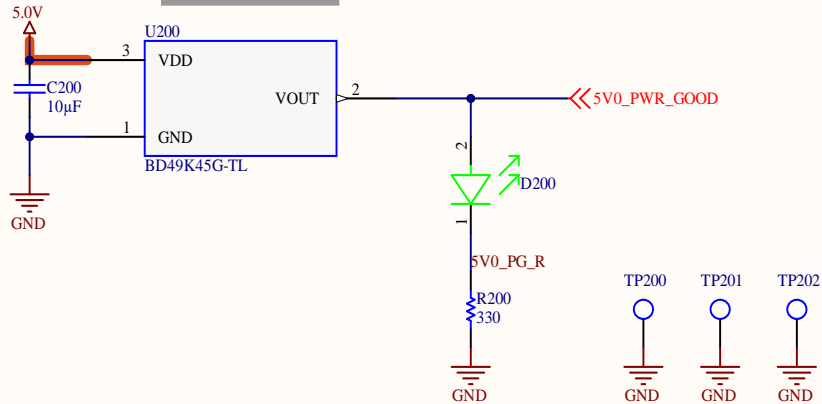
3.3V Voltage Monitor
3.2V Threshold
Totem Pole Output



Title		
Size	Number	Revision
A		
Date:	8/11/2022	Sheet of
File:	C:\Users\...\03-Power_3v3.SchDoc	Drawn By:



5.0V Voltage Monitor
4.5V Threshold
Totem Pole Output



Title		
Size	Number	Revision
A		
Date:	8/11/2022	Sheet of
File:	C:\Users\...\02-Power_5v0.SchDoc	Drawn By:

1

2

3

4



A

A

B

B

C

C

D

D



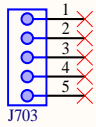
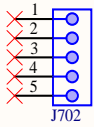
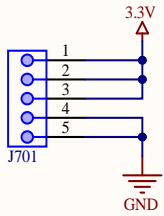
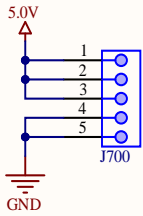
Title			
Size A	Number		Revision
Date:	8/11/2022		Sheet of
File:	C:\Users\...\06_Mechanicals.SchDoc		Drawn By:

1

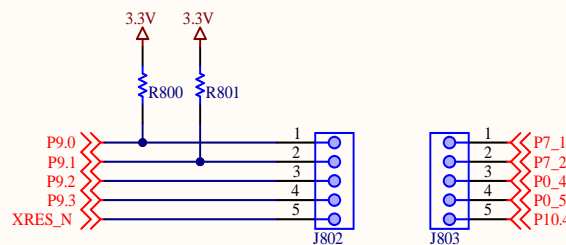
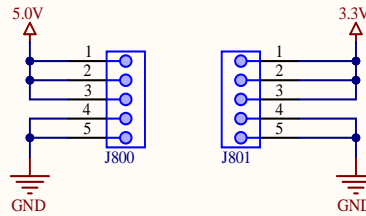
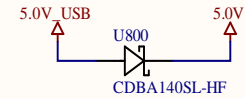
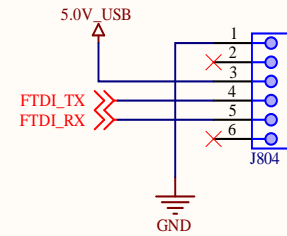
2

3

4

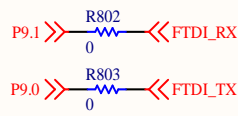


Title		
Size	Number	Revision
A		
Date:	8/11/2022	Sheet of
File:	C:\Users\...\07_Module_Power.SchDoc	Drawn By:

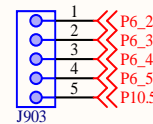
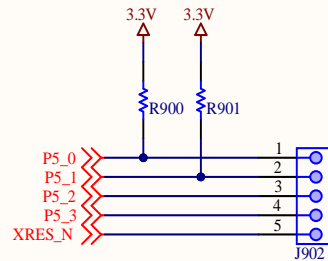
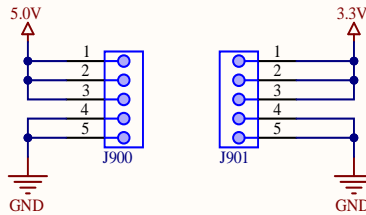


Pin	UART	SPI	I2C	PWM
P9.0	SCB2_RX	SCB2_MOSI	SCB2_SCL	tcpwm_0[4]
P9.1	SCB2_TX	SCB2_MISO	SCB2_SDA	!tcpwm_0[4]
P9.2		SCB2_SCLK		tcpwm_0[5]
P9.3		SCB2_SS0		!tcpwm_0[5]

Pin	PWM	Analog
P7.1	!tcpwm_0[4]	
P7.2	tcpwm_0[5]	
P0.4	tcpwm_0[2]	
P0.5	!tcpwm_0[2]	
P10.4	tcpwm_0[0]	SAMUX[4]



Title		
Size	Number	Revision
A		
Date:	8/11/2022	Sheet of
File:	C:\Users\...\08_Module_0.SchDoc	Drawn By:

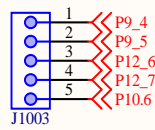
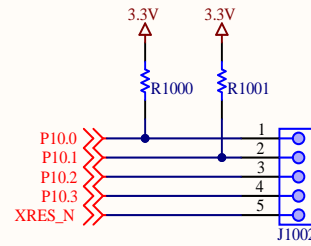
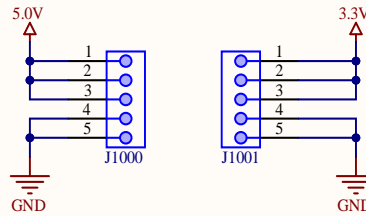


Pin	UART	SPI	I2C	PWM
P5_0	SCB5_RX	SCB5_MOSI	SCB5_SCL	tcpwm_1[4]
P5_1	SCB5_TX	SCB5_MISO	SCB5_SDA	!tcpwm_1[4]
P5_2		SCB5_SCLK		tcpwm_1[5]
P5_3		SCB5_SS0		!tcpwm_1[5]

Pin	PWM	Analog
P6_2	tcpwm_1[9]	
P6_3	!tcpwm_1[9]	
P6_4	tcpwm_1[10]	
P6_5	!tcpwm_1[10]	
P10_5	tcpwm_0[0]	SAMUX[5]



Title		
Size	Number	Revision
A		
Date:	8/11/2022	Sheet of
File:	C:\Users\...\09_Module_1.SchDoc	Drawn By:



Pin	UART	SPI	I2C	PWM
P10.0	SCB6_RX	SCB6_MOSI	SCB6_SCL	tcpwm_0[6]
P10.1	SCB6_TX	SCB6_MISO	SCB6_SDA	!tcpwm_0[6]
P10.2		SCB6_SCLK		tcpwm_0[7]
P10.3		SCB6_SS0		!tcpwm_0[7]

Pin	PWM	Analog
P9.4	tcpwm_1[0]	
P9.5	!tcpwm_1[0]	
P12.6	tcpwm_1[7]	
P12.7	!tcpwm_1[7]	
P10.6	tcpwm_1[2]	SAMUX[6]



Title		
Size	Number	Revision
A		
Date:	8/11/2022	Sheet of
File:	C:\Users\...\10_Module_2.SchDoc	Drawn By: