CAN

CAN Demo

- Clock Setup
 - HSE clock (missing in screenshots)
 - HCLK = 72 MHz
 - APB1 PCLK = 36 MHz
- CAN1 Basic config
 - Activate
 - Select RX & TX pins: PB8, PB9
 - Prescalar = 18
 - CAN time quanta = PCLK / Prescalar
 - = 18 / 36 M = 500 nsec
 - Time Quatas
 - Bit Seg 1 -- 2 Quanta
 - Bit Seg 2 -- 1 Quanta
 - SJW Seg -- 1 Quanta
 - Bit timing = 4 Quantas * Time Qunta = 2000 ns.
 - Baud Rate = 1 / Bit timing = 1 / 2000 ns = 500000 (bits per sec)
 - Mode = Loopback mode
 - Interrupt Enable = RX0 interrupt -- RX0 FIFO Pending message
- CAN Initialization (In main())
 - MX_CAN1_Init(...);
 - HAL_CAN_Start(...);
- CAN Receiving:
 - Acceptance Filter Config (In MX_CAN1_Init --> After CAN init)
 - HAL_CAN_ActivateNotification(...); (After HAL_CAN_Start());
 - Callback function: HAL_CAN_RxFifo0MsgPendingCallback(...);
- CAN Transmit (In main())
 - Put Msg metadata into TxHeader.

- Put Msg data in TxData -- array.
- Send message (into mailbox using HAL_CAN_AddTxMessage()).