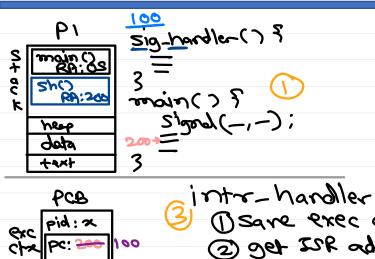


Embedded Operating Systems

Trainer: Nilesh Ghule





intr-hardler: 3 Osare exec ctr.

2) 20t ISB apply

3) CUL INR

(4) pid = scheduler();

@ sig_delivery (pid); dispertater (pid).

Sys_signal (sig, hand)

(1) get current process pcb.

@ write hard for addr in its sig handler that slot corresponding to sig num,

sig_delivery (Pid):

(1) in bop of diren beacers check pending but non-maked styrels.

Ofer each such signal:

@ create artificial stack frame on wer stack for that signal's

handler. Dithe seturn adds of the Stack found will be the addr from ELEC CLX (open beacers grang osuone after handler candeton).

(C) change PC in exec ct n of the process to the signal hand In addr (obtained from sig

a clear sig pending bit.



Sys_kill(pid, 59):

1) get pop of the process from process list whose p'id is given,

2) Set the bit corresponding to Elderg emerger in sig pending field of that pcb.



may 000000..0

pend 0 00000...0

Sig h.+bl



Thank you!

Nilesh Ghule <nilesh@sunbeaminfo.com>

