

CPU Modes : 0 (System / Kernel / Privilege...)

Kernel mode → all instruction can be execute
all register can be accessed

CPU mode : 1
user mode (unprivilege)

→ Special instruction are not allowed
eg. I/O, intr. control...
few reg are not accessible
eg. MMU register, timer, Pic.

→ When user code is running, mode = 1 & when kernel code is running, mode = 0.

→ mode is changed from 1 to 0, when an intr/exception arrives - mode change back to 1 when intr/exception handler returns.

→ During Sys call, this is done by swi intr

→ Actual CPU mode depends CPU arch.

→ ARM Cortex-A : 7 modes

svc, irq, fiq, undef, abt, system, user
privilege mode

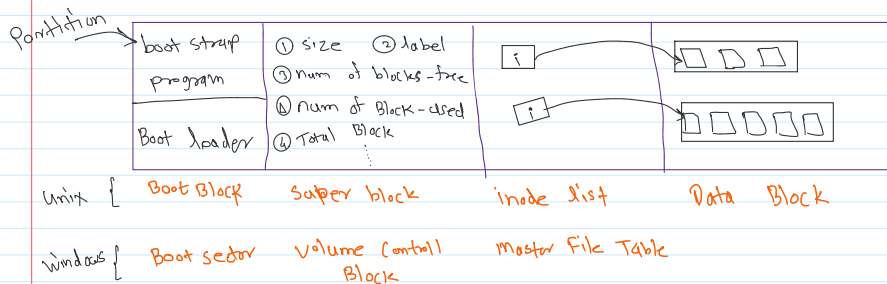
* If you want to make your own Sys call

1st

2nd

1. Download linux kernel source code
2. Implement your syscall in some .c file under kernel source tree (codes)
3. Create/modify makefile to compile your system code.
4. Make your syscall entry into arch specific syscall table.
5. Recompile the linux kernel & deploy it (in boot dir) also modify boot loader (grub) to make entry of your kernel.
6. Reboot system into your compiled kernel

1. Implement your syscall applⁿ wrapper using assembly lang or using syscall funcⁿ
2. Call your syscall api from your code i.e. `main()`



File system is created by
formatting tool/utility

→ windows : `format.exe`

→ linux : `mkfs`