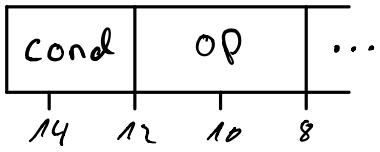
Instructions

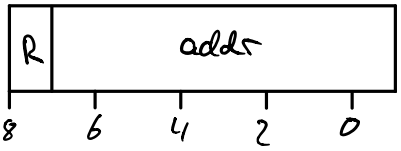


Conditions:

Condition	Cond	binary value	flags
No condition	-	000_2	-
Greater Than	GT	001_2	$\bar{N}$ and $\bar{Z}$
Less Than	LT	010_2	N
Greater Than or Equal	GTE	011_2	$\bar{N}$
less Than or Equal	LTE	100_2	N or Z
Equal	EQ	101_2	Z
Not Equal	NE	110_2	$\bar{Z}$

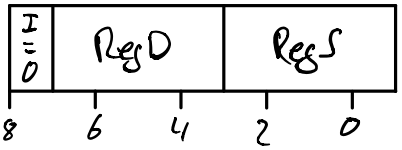
Operation:

- J $[0001_2]$

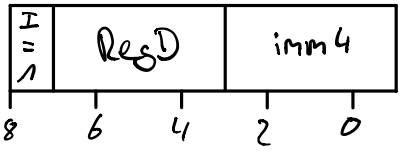


-> Moves addr to R15, r is reserved (later func. prolog?)

- MOV $[0011_2]$

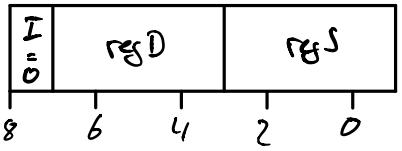


-> Moves value of register RegS to register RegD



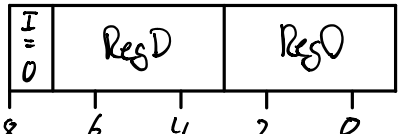
-> Moves imm4 to register RegD

- LDR $[0100_2]$ and STR $[0101_2]$

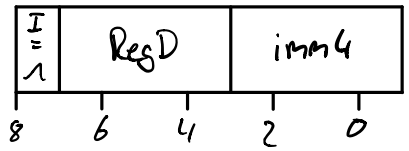


-> For LDR loads value at memory address addr to register R0
-> For STR writes value of register R0 to memory address addr

- ADD $[1000_2]$ and SUB $[1001_2]$

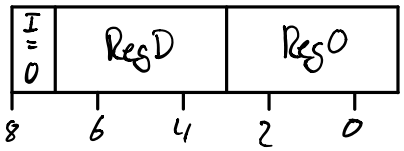


-> Adds to/Subtracts from register RegD value of register RegO.

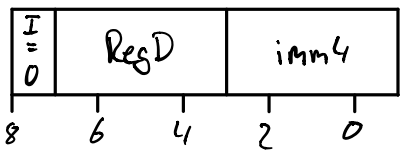


→ Adds to/Subtracts from register RegD value imm4

- LSL [1010₂] and LSR [1011₂]

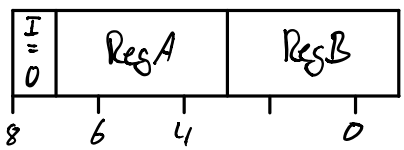


→ Shifts register RegD by the value of RegO to the left/right



→ Shifts register RegD by imm4 to the left/right

- AND [1100₂] and OR [1101₂]



- Performs bitwise comparison on the values of registers RegA and RegB and sets the Z flag according to whether they match (1) or not (0)