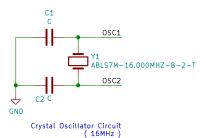
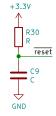


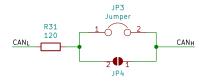
Slew Rate Control circuit.
The slew rate of CANH and CANL are determined by the resistance between the Rs pin and ground. Connect directly to ground for high speed mode, or limit the slew rate to help with EMI reduction







Power On Reset Circuit Needs to hold reset state for >2us after power up



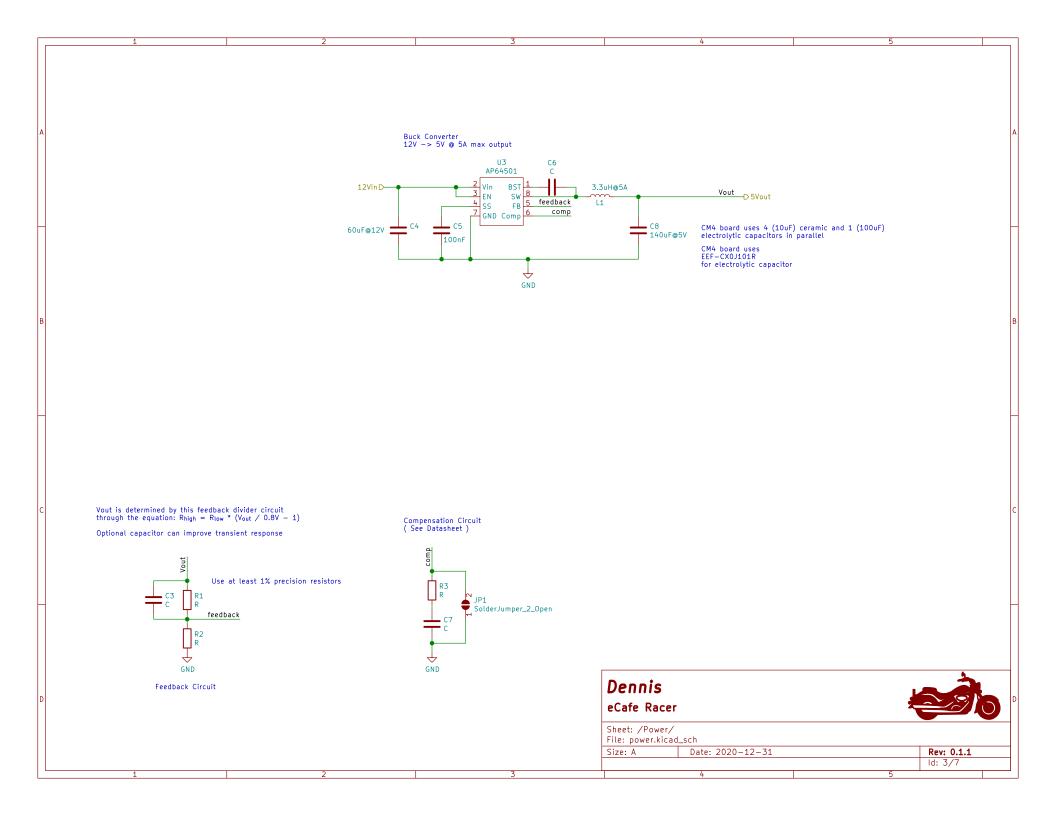
120 ohm termination resistor required by CAN bus. Can be configured with either jumper or solder bridge

## Dennis

## eCafe Racer

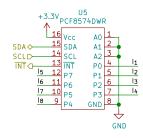
Sheet: /CAN Peripherals/ File: can\_peripherals.kicad\_sch

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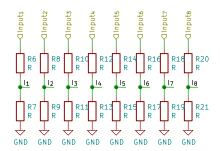


Thinking more and more that ethernet and power over ethernet might be worth it. Would be helpful mainly if we want to install this in a server rack as part of a dyno-room style electrical testing stand Module1A ComputeModule4-CM4 Hirose 2x DF40C-100DS-0.4V SD{DAT[0..3] CLK DET} SD{DAT[0..3] CLK DET} 2 GND GND J1 4 Ethernet\_Pair1\_P Ethernet\_Pair3\_P 3 Micro\_SD\_Card\_Det +3.3V 6 Ethernet\_Pair1\_N Ethernet\_Pair3\_N 5 GND 7 Ethernet\_Pair2\_N 9 8 GND 10 Ethernet\_PairO\_N DAT2 12 Ethernet\_Pair0\_P Ethernet\_Pair2\_P 11 DAT3/CD 14 GND Ethernet\_nLED3(3.3v)
Ethernet\_nLED2(3.3v)
Ethernet\_nLED1(3.3v)
Pl\_nLED\_Activity
23
GPI021
GPI020
GPI016
GPI012
GPI016
GPI017
GPI016
GPI017
GPI016
GPI017
GPI017
GPI017
GPI018
GPI025
GPI024
GPI024 GND 13 3 CMD Ethernet\_SYNC\_IN(1.8v) 4 +3.3V VDD 18 Ethernet\_SYNC\_OUT(1.8v) 5 CLK 20 EEPROM\_nWP 6 7 VSS 22 GND 24 GPI026 26 GPI019 R4 DAT0 8 DAT1 D GPIOOE 10 DET\_A 28 GPI013 -D GPIORS SD.DET 9 SHIELD DET\_B 30 GPI013 32 GND 34 GPI05 36 ID\_SD -D GPI0rcLK SD card detection circuit  $\,^{\,\,\text{R5}\,\,\,\text{R}}$ -D GPIOsclk -D GPIODATA 38 GPI011 40 GPI09 5 GPIO pins are used to drive a shift register controlling output on the board's 12V GPIO module. GND TODO: double check SD card circuit 42 GND 44 GPI010 GPIO TODO: select gpio pins to use 46 GPI022 GPI024 45 GPI023 GPI018 GPI015 51 48 GPI027 50 GPI017 52 GND 54 GPI04 GND 53 56 GPI03 GPI014 55 58 GPI02 SD.CLK SD\_CLK 57 60 GND GND 59 SD.DAT3 62 SD\_CMD SD\_DAT3 61 64 SD\_DAT5 SD.DAT0 SD\_DATO 63 66 GND GND 65 68 SD\_DAT4 SD\_DAT7 SD.DAT1 NB SD signals are only available SD\_DAT1 67 SD.DAT2 SD\_DAT2 69 72 SD\_DAT /
72 SD\_DAT 6
74 GND
+3.3V 76 Reserved
78 GPIO\_VREF(1.8v/3.3v\_Input) on modules without eMMC GND 71 SD\_VDD\_Override SD\_PWR\_ON +5v\_(Input)
73
75
77 +5V +5v\_(Input) 79 80 SCLO SCL 82 SDA0 +5v\_(Input) 81 SDA♦ 84 +3.3v\_(Output) 600mA Max +5v\_(Input) 83 +5v\_(Input) 85 +5v\_(Input) 87 86 +3.3v\_(Output) +5v\_(Input) 87 88 +1.8v\_(Output) 600mA Max 90 +1.8v\_(Output) WiFi\_nDisable 89 BT\_nDisable 91 nRPIBOOT 93 92 RUN\_PG 94 AnalogIP0 96 AnalogIP1 nPI\_LED\_PWR 95 98 GND Camera\_GPIO 97 100 nEXTRST Global\_EN 99 Dennis eCafe Racer GND Sheet: /ComputeModule\_A/ File: compute\_module\_A.kicad\_sch Size: A Date: 2020-12-31 Rev: 0.1.1 Id: 4/7

+3.3٧ NPIC6C596ADJ OUT\_CTL1 OUT\_CTL2 DS Q1 OUT\_CTL3 SHCPD SHCP Q2 OUT\_CTL4 STCPD STCP Q3 OUT\_CTL5 MRD MR Q4 12 OUT\_CTL6 13 OUT\_CTL7 14 OUT\_CTL8 OE D-ŌĒ Q5 Q7S Q6 GND Q7 GND



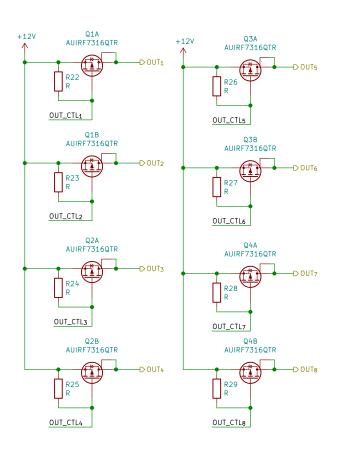
An IZC 10 expander is used to read the inputs instead of connecting to the pi's gpio pins directly. This is mainly done for insurance so that the pi's pins cannot be accidentally exposed to the 12V signal.



Voltage Divider Network (12V  $\rightarrow$  5V) Used to read 12V digital input signals from other parts of the bike Use integrated resistor network if possible (TODO: spec)

By far the most common use for these inputs will be to read the value of switches. Need to think more about the best way to achieve this, it is most likely not this.

It might be nice to combine Input and Output pins into a single circuit which can be configured in software to act like either, like the gpio modules on microprocessors. Perhaps a "switch detection" mode could be added as well, which measures the resistance to ground instead of voltage



## **Dennis** eCafe Racer

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