

1. **UFC** = **BY BUS POWER SWITCHING**  
**JPS** CLOSED, **JPS** OPEN, **JPS** OPEN  
**VSUS** POWERS BY **BUS** **UNTIL** EXT **PWR** (**WCC**) APPLIED.  
**THEN** **VSUS** IS **POWERED** BY **VCC**.  
(**EXTENDED/DEFAULT** OPERATION)
- 1.2. **JPS** OPEN, **JPS** CLOSED, **JPS** OPEN  
**VSUS** IS **SOLELY** **POWERED** BY **EXT PWR** (**WCC**).
- 1.3. **JPS** OPEN, **JPS** OPEN, **JPS** CLOSED  
**VSUS** IS **SOLELY** **POWERED** BY **RAW** **VSUS** **BUS**.  
**CAUTION:** **FALSE** **INDICATION** OF **EXT PWR** **VIA** **WCC** **IS**  
**NOT** **WARRANTED** **IF** **EXT PWR** **CONNECTED**.
- 1.4. **JPS** OPEN, **JPS** OPEN, **JPS** OPEN  
**NO** **BY** **POWER** **IS** **AVAILABLE** **IF** **UFC** **IS** **UFC**.  
**WARNING:** **UFC** **WILL** **NOT** **FUNCTION** **IN** **THIS** **STATE**.
2. **ENABLING** **OPDS/OPPOD** **TO** **ACTIVATE** **THE** **OPTION** **QUEING** **USE**  
**OPICE** **TO** **DRIVE** **THE** **MSDET** **WHICH** **TURNS** **ON** **CL/SD** **TO** **SHOW**

