

Modelling and Validation of Concurrent System

Computational Tree Logic

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Limitations of HML

The role of Logics in Program Verification

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TLA is a temporal Logic.

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Instead of aiming at stating and proving properties of programs, it aims at providing a logical account of the operational equivalence notion of process languages (observational congruence).

- Strong Bisimulation coincides with logical equivalence, considering strong modalities.
- Observational congruence coincides with logical equivalence for finite branch processes, considering weak modalities.

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There is no way of talking about something holding eventually or always in the future — formulæ in HML talk about a finite number of steps, the concrete number given by the depth of a formula.

Computational Tree Logic, CTL

A simple logic to express temporal properties of processes

In the early 1980s, Clarke, Emerson, Sistla and others developed a logic with a *next state* modality and built-in temporal connectives

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It features:

- propositional connectives
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- a temporal operator: *until*:
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The key idea

- Quantify over *paths* on the LTS
(instead of only over actions)
- Talk about the future

The *until* temporal operator

Like in HML, CTL formulæ are interpreted over LTSs.

A *path* on an LTS S

is a sequence of states, linked to each other by transitions.

If P is an initial state of S , then

$$P \xrightarrow{a_1} P_1 \xrightarrow{a_2} P_2 \dots$$

is a path of S if each triple $P_{i-1} \xrightarrow{a_i} P_i$ is in S 's transition relation

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Computational Tree

The acyclic unfolding of an LTS

Syntax of CTL

Consider $\mathcal{K} \subseteq \text{Act}$

The set \mathcal{F} of formulæ is inductively defined by the grammar

$$\varphi ::= \perp \mid \neg\varphi \mid (\varphi \wedge \varphi) \mid \langle \mathcal{K} \rangle \varphi \mid \text{E}(\varphi \text{ U } \psi) \mid \text{A}(\varphi \text{ U } \psi)$$

where the temporal operators:

- E means *there Exists* a path in the LTS
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Furthermore, some derived temporal operators are used often

- F means *sometime* in the Future
 $\text{F } \varphi = \top \text{ U } \varphi$
- G means *always* in the future
 $\text{G } \varphi = \neg(\top \text{ U } \neg\varphi)$

Semantics of CTL

The *satisfaction relation*:

$\models \subseteq \text{Proc} \times \mathcal{F}$ is defined as for HML

$P_0 \models E(\varphi \cup \psi)$ if for SOME path $P_0 \xrightarrow{a_1} P_1 \xrightarrow{a_2} \dots$ it is the case
that $\exists i. (i \geq 0 \wedge P_i \models \psi \wedge \forall j. (0 \leq j < i \wedge P_j \models \varphi))$

$P_0 \models A(\varphi \cup \psi)$ if for ALL paths $P_0 \xrightarrow{a_1} P_1 \xrightarrow{a_2} \dots$ it is the case
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More useful abbreviations

- $EF \varphi = E(\top \cup \varphi)$
- $AF \varphi = A(\top \cup \varphi)$
- $EG \varphi = \neg AF \neg \varphi$
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In CTL temporal operators are always prefixed by path quantifiers.

Properties of Distributed Systems in CTL

Strong Safety $AG \neg \text{BAD}$

Strong Liveness $AF \text{GOOD}$

Weak Safety $EG \neg \text{BAD}$

Weak Liveness $EF \text{GOOD}$

Assume two processes willing to access a critical region.

Absence of Deadlock

Every path is infinite: $AG \langle - \rangle T$

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Mutual exclusion

In no path are two processes simultaneously in the critical region
 $AG ([rel1] \perp \vee [rel2] \perp)$

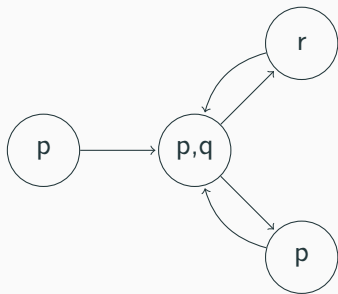
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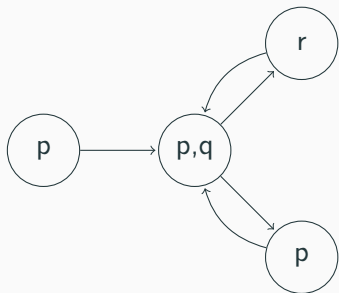
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Call P to the leftmost state, Q to the central one, R to the top-right one, and S to the bottom-right one.



- $P \models A(p \cup q)$ does not hold.
- $P \models EF r$ holds
- $P \models AF r$ does not hold.
- $P \models EF p$ holds
- $P \models AF(EG r \vee EG p)$ does not hold.

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Beyond the π -calculus

- Higher-order π allows to pass processes
- SPI adds encryption and decryption primitives
- ψ -calculi allows to have terms of an algebra instead of names

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Solutions

- CTL* lifts the alternation limitation
- The modal μ -calculus substitutes the temporal operators by fix-points (smallest and greatest)
It turns out it includes CTL*