[1] Boier, Katoen: Principles of model checking. MIT Press (chapters 1-3,5, and 6)
[2] Clarke, Grunberg, Poled: Midel checking. MIT Press this motes were based on

INTRODUCTION

elways relative Su is nowaday ubiquitous => Su correctuess is valueble

correct systems for information processing are more valuable than gold.

(H. Barendregt, 'The quest for correctness', in Images of SMC Research 1996

therac-25 > 6 deaths between 1985-1987 -Sw bys = less of lives

Therec-2> > 6 deaths between 1985-1982

Examples Arisame -5 exploded 365 after launch

Beggsge bandling system @ Denver zirport (\$ 1.1.10 x day x 9 months)

https://www.reuters.com/business/autos-transportation/us-probing-fatal-tesla-crash-that-killed-pedestrian-2021-09-03 https://www.tesladeaths.com/

/ testing) + "simple" = etefacts on checked + "simple" = pertial (when should we stop?)

| + Infinite state systems | - hazd' to time consuming | - interactive . Deductive ressuring

Borrowed from [1]

what is the system, AND specificationpropertiesDesign Process product or prototype buq(s) found Verification ' no bugs found

the system either doen something not expected - 2 hoes not do something expected

Exercise. Consider the 3 python functions essentially implenting Example 1.1 in [1]: old reset (): det dec (): det inc 0: while lost: while loop: while loop : if x == bound: if x (bound :

Take the temporal property

4 = Alweys 0 < x < 200

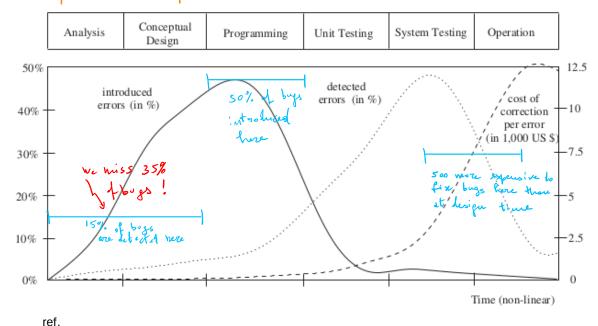
Does y hold if initially x == 0, loop == True, bound == 200 & inc. dec, and reset above execute concurrently?

Moder CHECKING, but let's dissect buys first We'll focus on

Empirical evidence shows that exerts showet distribute evenly in space (bugs tend to concentrate in few modules) and in

The sooner, the better!

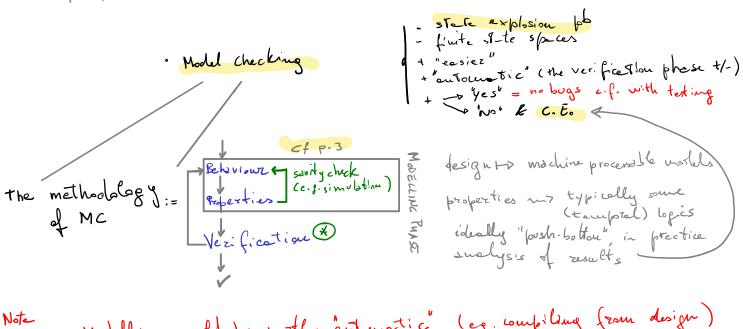
model-based verification
helps here



P. Liggesmeyer and M. Rothfelder and M. Rettelbach and T. Ackermann. Qualitätssicherung Software-basierter technischer Systeme. Informatik Spektrum, 21(5):249–258, 1998.

Quoting [1]

"In software and hardware design of complex systems, more time and effort are spent on verification than on construction. Techniques are sought to reduce and ease the verification efforts while increasing their coverage. Formal methods offer a large potential to obtain an early integration of verification in the design process, to provide more effective verification techniques, and to reduce the verification time."



Note Modelling could be pertly automatic (eg. compiling from design)
Verification is mainly automatic

Glowing at temporal logics

Note Temp-rol logics stem from phylosophy: model logics to reason about time in natural banguage!

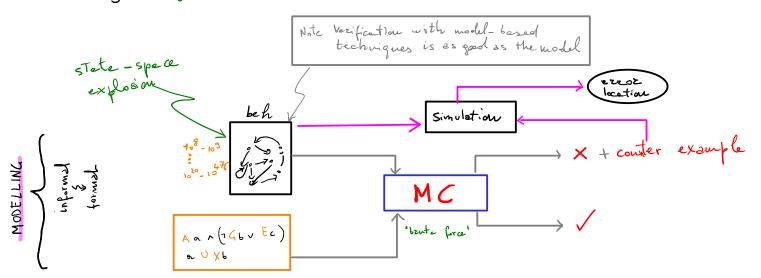
- · Designed to predicate on concurrent events · events ou ordered in time
- but time is not explicit

modelity Id

eg [(7(e x b)) =

. thread a writer 2 it will never happen that events @ end 6 occur at
the same time". = three b

Schemstically Cf Fig 1.4 [1]



Exercise. Consider the following python implementation of Example 1.1 in [1]:

det inc (): while emp:

if respond:

det dec (): while loop: if n 70: old uset (): while loop: if x == bound:

Does the (temporal) property

Alweys 0 5 x \$ 200

half if inc. dec, and reset above execute concoverently & initially x ==0, loop == True, and bound = co?

What we've going to see

- . Modelling (concurrent) systems
- . Temporal hopics
- · LTL
 . CTL*
 . CTL*
 . Fairness conditions of time
- . Hints on symbolic M.C.

What we're not looking at

- . Partial order reductions
- · µ-calculus
- . Abstraction techniques
- . Quantitative/performance analysis
- . Timed models

If I= p =) no behaviour

Rechable states