

eYSIP2017

TITLE OF THE PROJECT



Intern 1 Name

Intern 2 Name

Mentor Name

Duration of Internship: 22/05/2017 – 07/07/2017

2017, e-Yantra Publication

Project Name

Abstract

Give the brief introduction and overview of the project

Completion status

Give details for work/project completed successfully. If work is not complete, mention the details till which task is done.

1.1 Hardware parts

- List of hardware
- Detail of each hardware: [Datasheet, page 5](#), [Vendor link](#),
- Connection diagram

1.2 Software used

- List of software used
- Detail of software: version, [download link](#),
- Installation steps

1.3 Assembly of hardware

Circuit diagram and Steps of assembly of hardware with pictures for each step



1.4. SOFTWARE AND CODE

Circuit Diagram

Circuit schematic, simplified circuit diagram , block diagram of system

Step 1

Steps for assembling part 1

Step 2

Steps for assembling part 2

Step 3

Steps for assembling part 3

1.4 Software and Code

[Github link](#) for the repository of code

Brief explanation of various parts of code

1.5 Use and Demo

Final Setup Image

User Instruction for demonstration

[Youtube Link](#) of demonstration video

1.6 Future Work

What can be done to take this work ahead in future as projects.

1.7 Bug report and Challenges

Any issues in code and hardware.

Any failure or challenges faced during project

Bibliography

- [1] Ad Kamerman and Leo Monteban, *WaveLAN-II: A High-Performance Wireless LAN for the Unlicensed band*, 1997.

