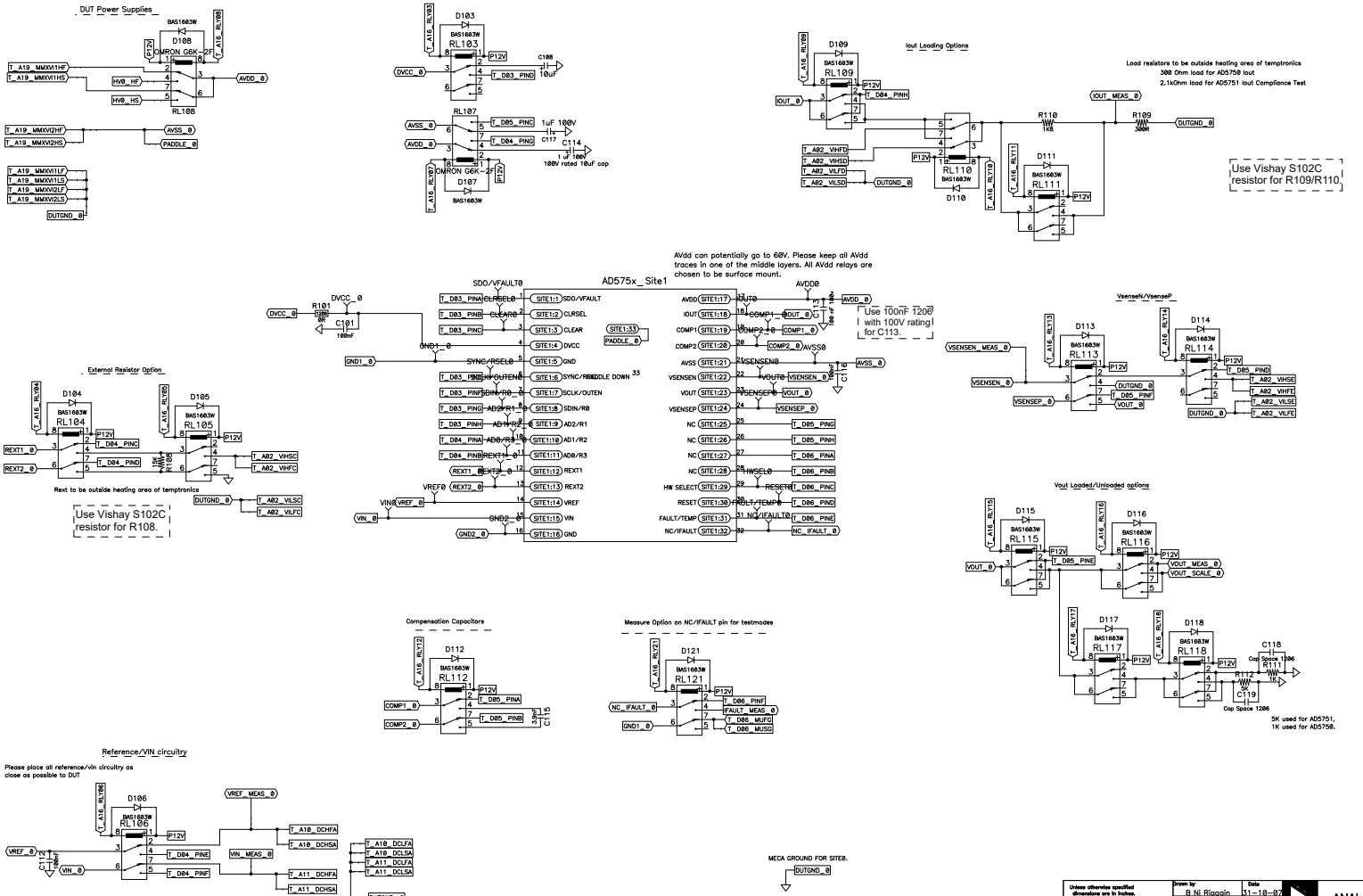
Revisions					
Symbol	Description	Drawn	Check	Approved	Date
A	Initial release per E.C.N. no.				
B	SEE ECR-097781				
C	SEE ECR-102241				

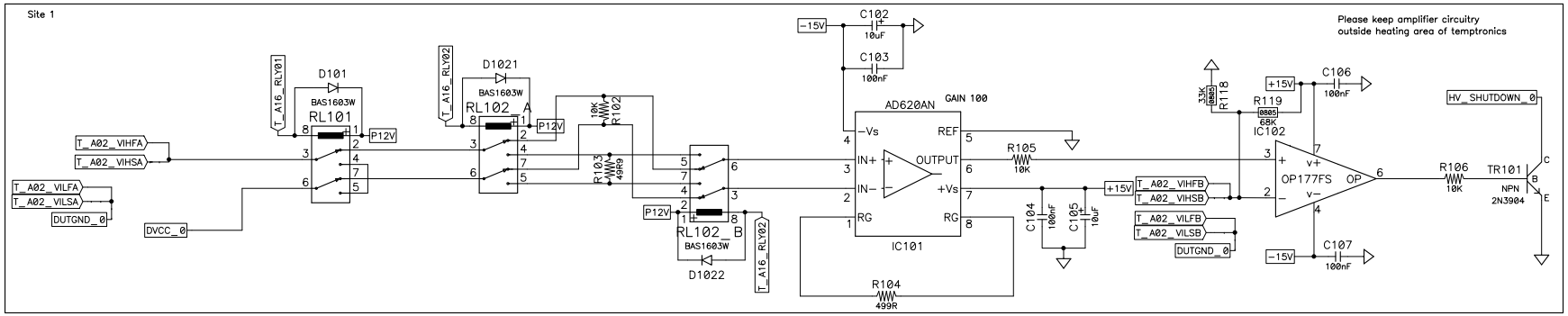


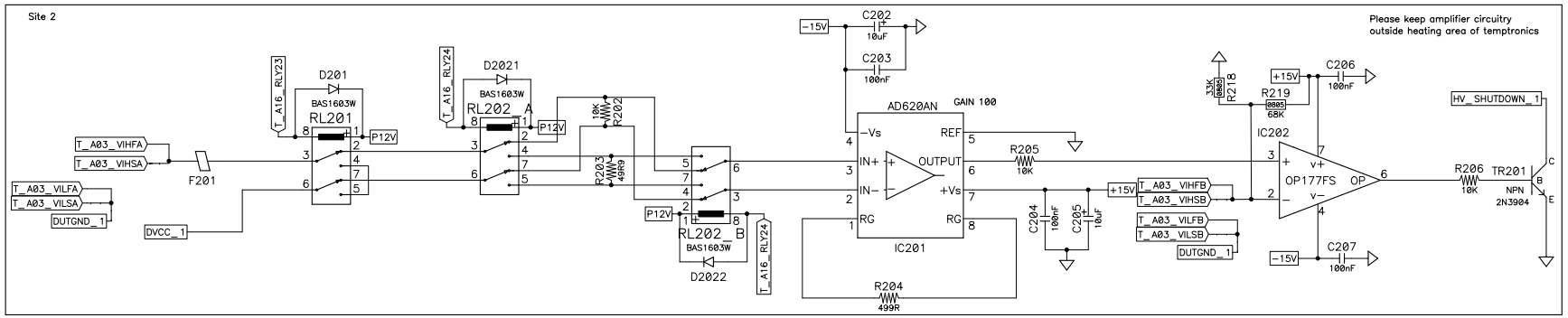
Unless otherwise specified dimensions are in inches. Tolerances: Decimals .XX+_ Fractions: .XXX+_ Angles:	Drawn by: B Ni Riagain	Date: 31-10-07	ANALOG DEVICES PHILS.
	Des. B Ni Riagain	31-10-07	AD5758 CTS5400 32Ld LFCSP FINAL TEST SPEC#: GTL58202
	Chkd1	31-10-07	
Litho Circuits	Job no. SL58202B	Chkd2 / Chkd3	
		Q.A. mgr. QA.ADLK 31-10-07	

This drawing is the property of ANALOG DEVICES GT .It is not be reproduced or copied in whole or in part, or used for furnishing information to others, or for any other purpose detrimental to the interests of ANALOG DEVICES. The equipment shown hereon may be protected by patents owned or controlled by ANALOG DEVICES GT

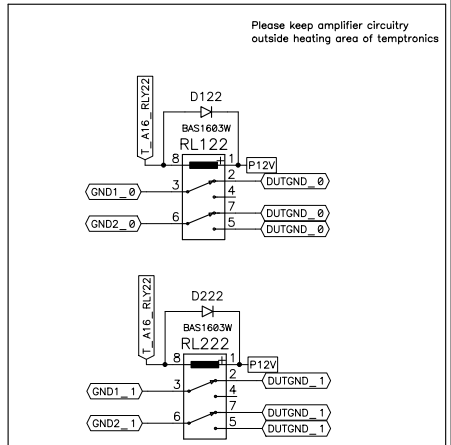
Size	Code Ident. no.	Drawing no.	Rev.
D	24355	02-035657 (WTD01297)	C
Scale	Model	Sh. 1 of 21	

CURRENT SENSE ON DVCC FOR SITES 0 & 1. THIS ENSURES THAT THE HIGH VOLTAGE IS ONLY ON WHILE DUT IS IN THE SOCKET.

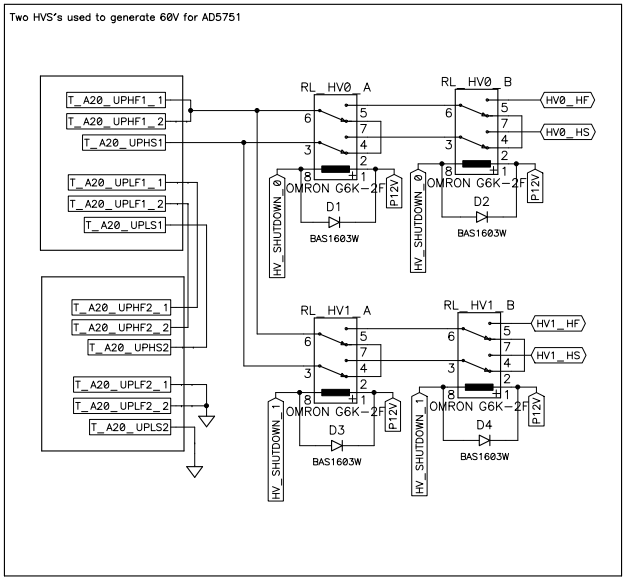




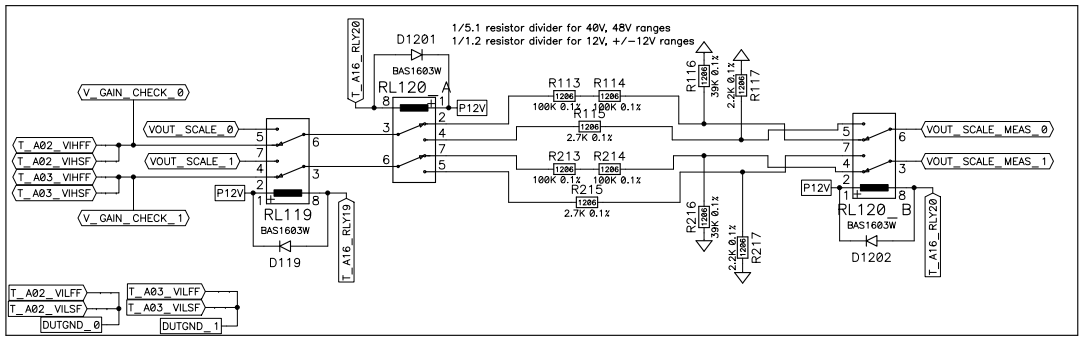
GROUND SENSE CIRCUITRY

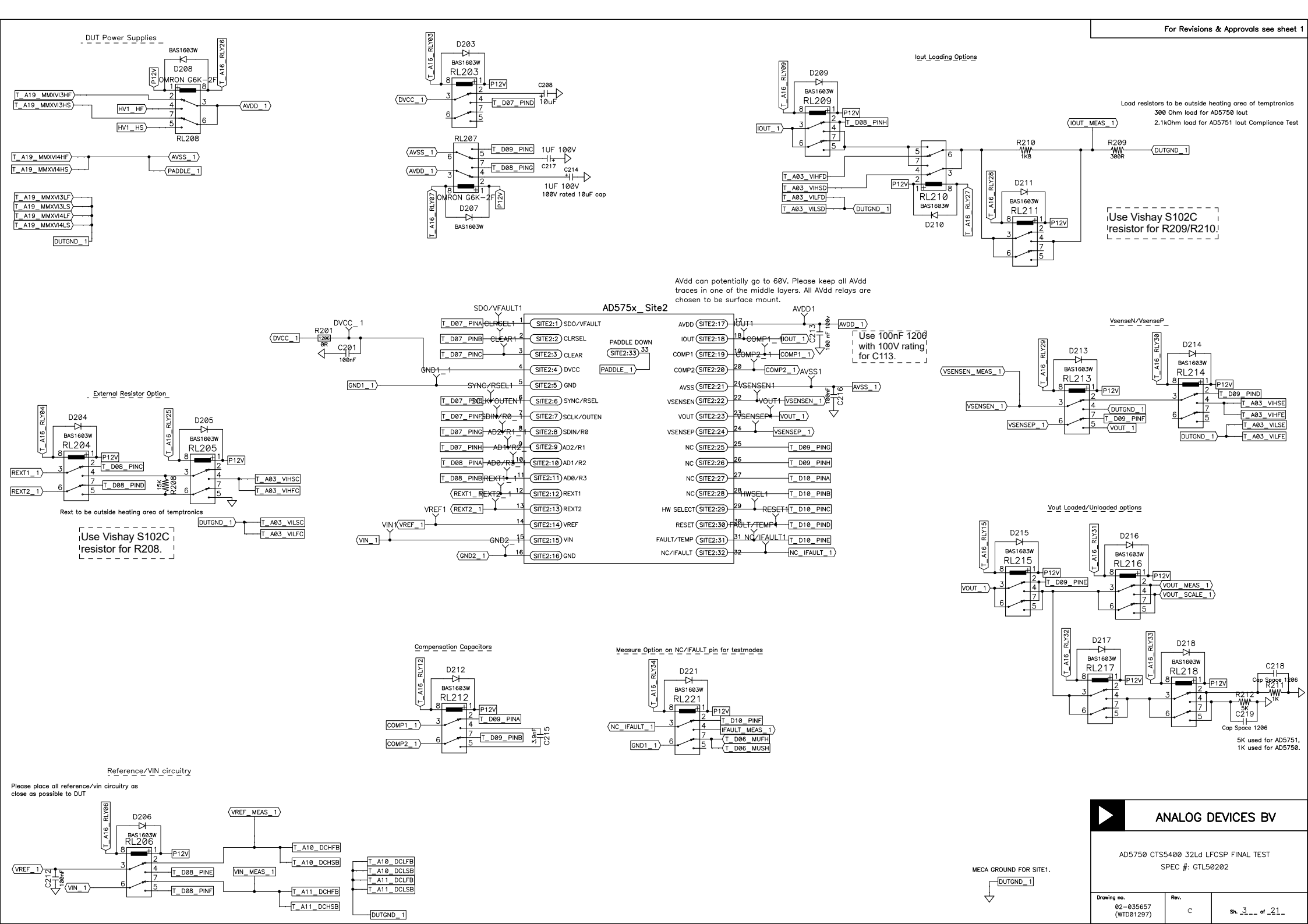


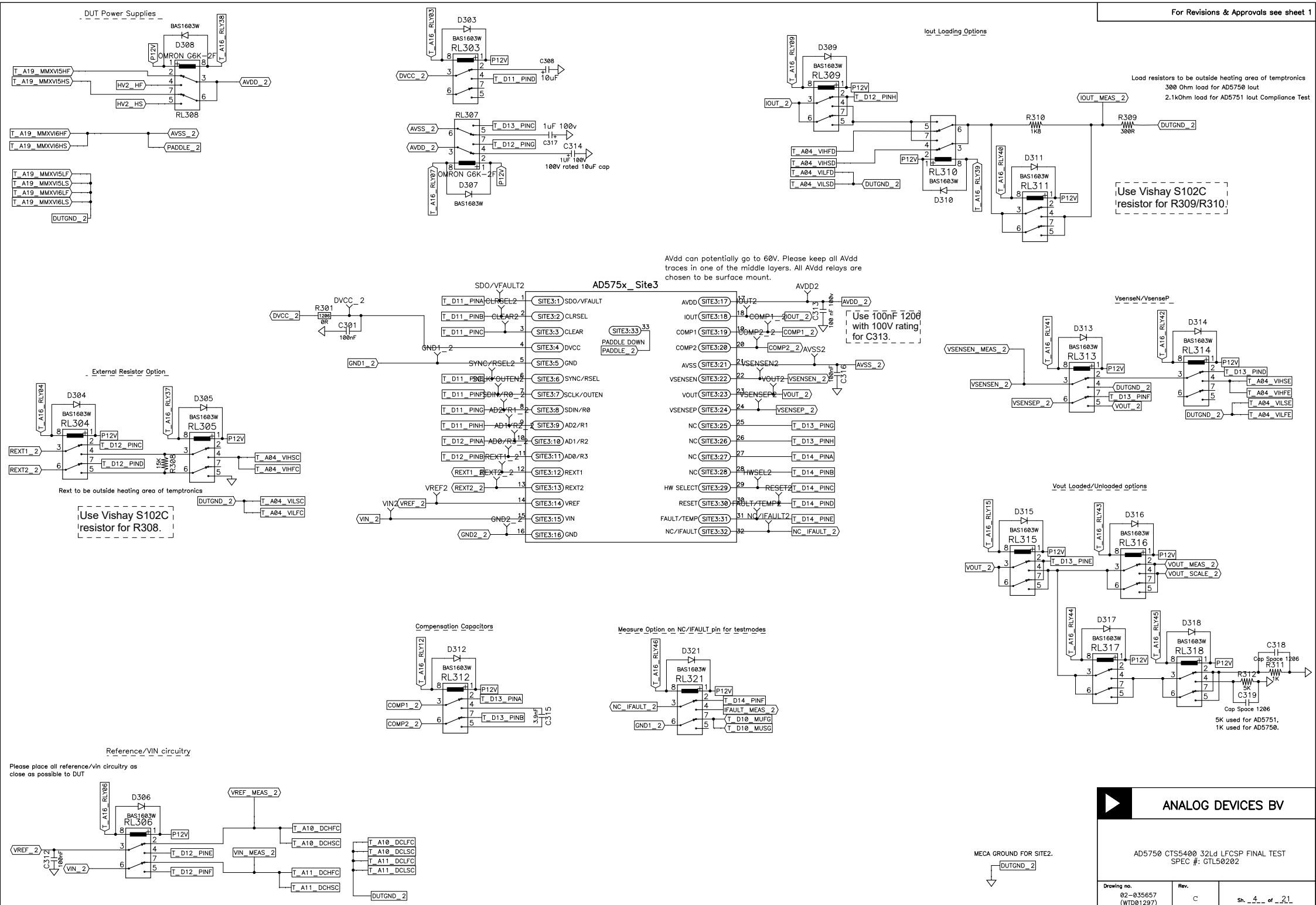
HIGH VOLTAGE POWER SUPPLY FOR SITES 0 & 1



VOUT SCALE DOWN CIRCUITS, SITES 0 & 1




DUT Power Supplies
D208
BAS1603W
OMRON G6K-2F
RL208
HV1_HF
HV1_HS
AVDD_1
AVSS_1
PADDLE_1
DUTGND_1
D203
RL203
C208
10uF
D207
RL207
C217
1UF 100V
C214
1UF 100V
100V rated 10uF cap
Iout Loading Options
D209
RL209
D210
RL210
D211
RL211
R210
1K8
R209
300R
IOUT_MEAS_1
Load resistors to be outside heating area of temptronics
300 Ohm load for AD5750 Iout
2.1kOhm load for AD5751 Iout Compliance Test
Use Vishay S102C resistor for R209/R210.
AVdd can potentially go to 60V. Please keep all AVdd traces in one of the middle layers. All AVdd relays are chosen to be surface mount.
AD575x_Site2
SDO/VFAULT1
AVDD1
PADDLE DOWN
Use 100nF 1206 with 100V rating for C113.
VsenseN/VsenseP
D213
RL213
D214
RL214
External Resistor Option
D204
RL204
D205
RL205
Rext to be outside heating area of temptronics
Use Vishay S102C resistor for R208.
Vout Loaded/Unloaded options
D215
RL215
D216
RL216
D217
RL217
D218
RL218
5K used for AD5751,
1K used for AD5750.
Compensation Capacitors
D212
RL212
Measure Option on NC/IFAULT pin for testmodes
D221
RL221
Reference/VIN circuitry
Please place all reference/vin circuitry as close as possible to DUT
D206
RL206
MECA GROUND FOR SITE1.
ANALOG DEVICES BV
AD5750 CTS5400 32Ld LFCSP FINAL TEST
SPEC #: GTL50202
Drawing no. 02-035657 (WTD01297)
Rev. C
Sh. 3 of 21

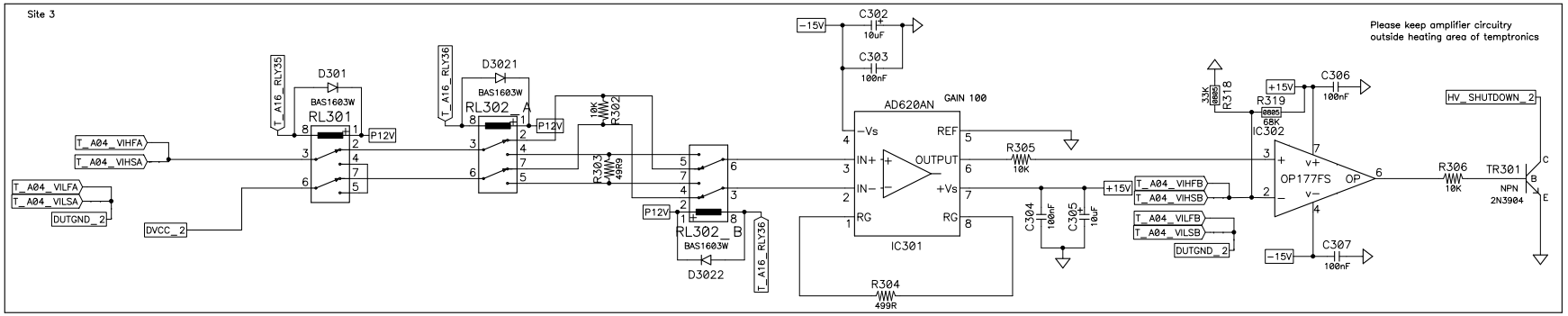


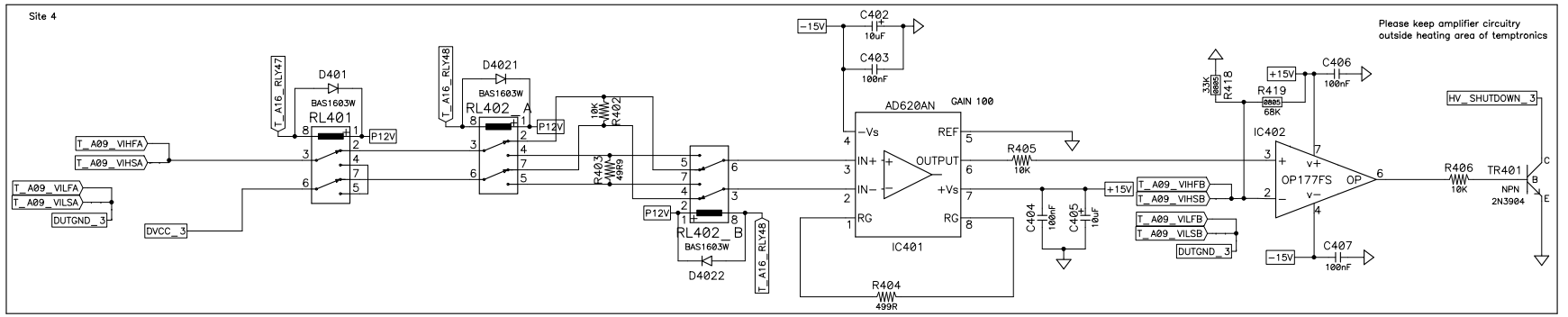
ANALOG DEVICES BV

AD5750 CTS5400 32Ld LFCSP FINAL TEST
SPEC #: GTL50202

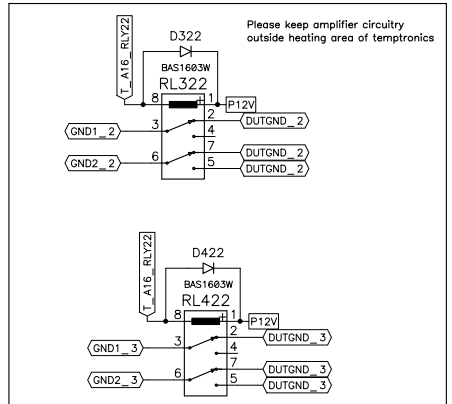
Drawing no.	Rev.	Sh. 4 of 21
02-035657 (WTD01297)	C	

CURRENT SENSE ON DVCC FOR SITES 2 & 3. THIS ENSURES THAT THE HIGH VOLTAGE IS ONLY ON WHILE DUT IS IN THE SOCKET.

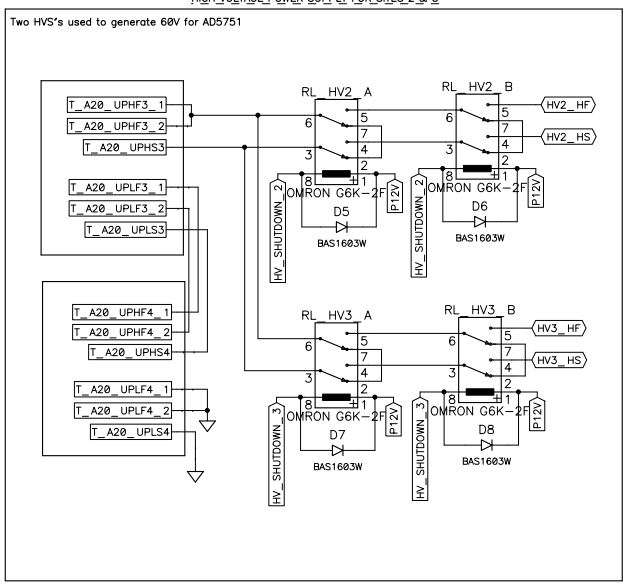




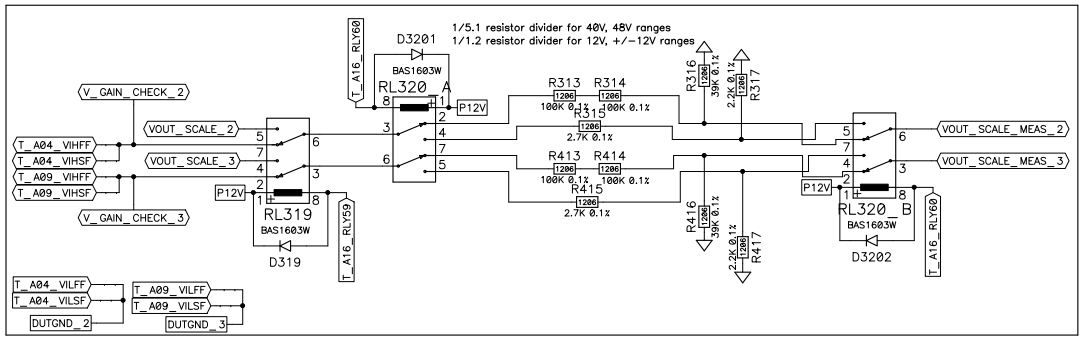
GROUND SENSE CIRCUITRY



HIGH VOLTAGE POWER SUPPLY FOR SITES 2 & 3



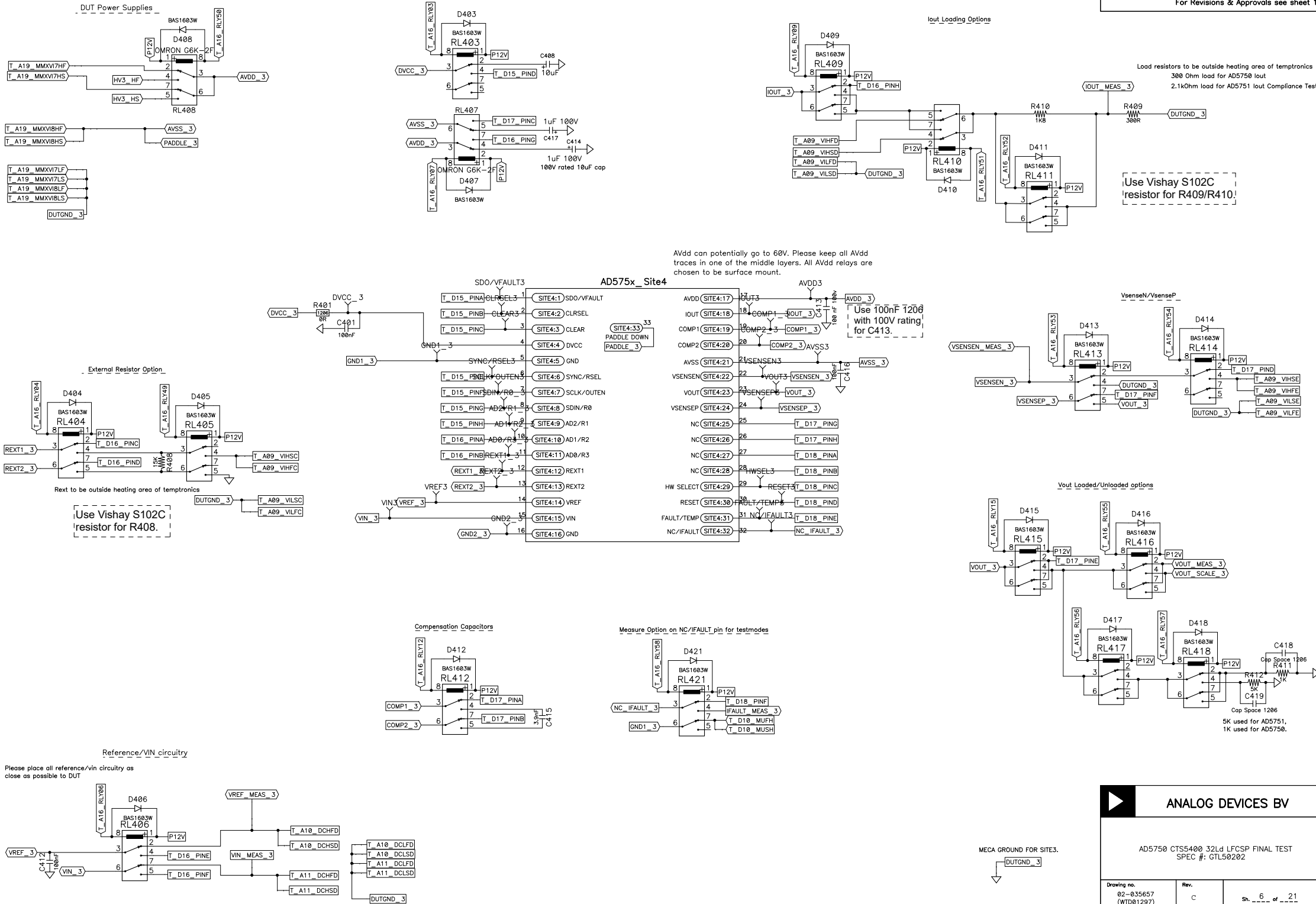
VOUT SCALE DOWN CIRCUITS, SITES 2 & 3



ANALOG DEVICES BV

AD5750 CTS5400 32Ld LFCSP FINAL TEST
SPEC #: GTL50202

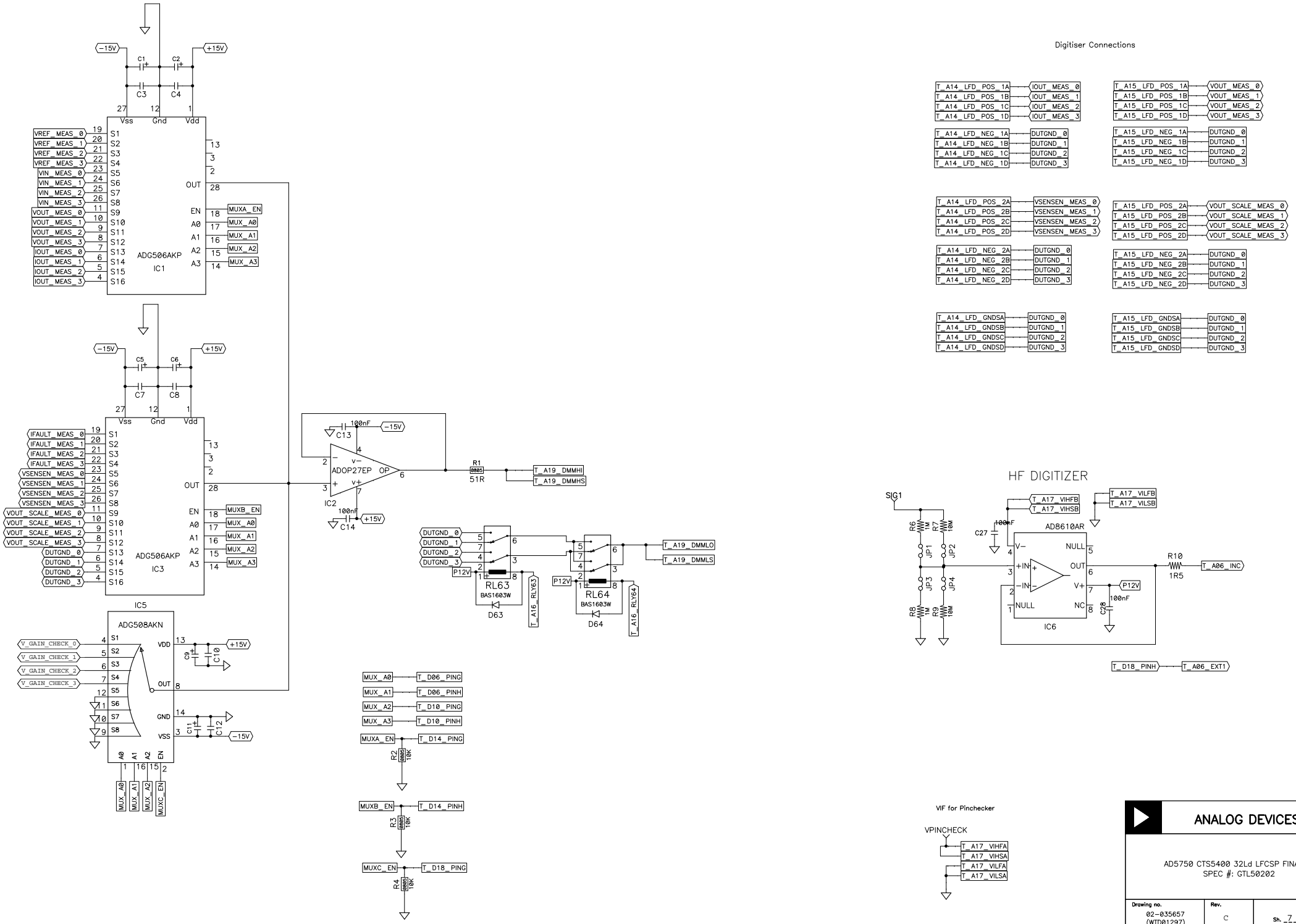
Drawing no.	Rev.	Sh. 5 of 21
02-035657 (WTD01297)	C	



ANALOG DEVICES BV

AD5750 CTS5400 32Ld LFCSP FINAL TEST
SPEC #: GTL50202

Drawing no.	Rev.	Sh. 6 of 21
02-035657 (WTD01297)	C	

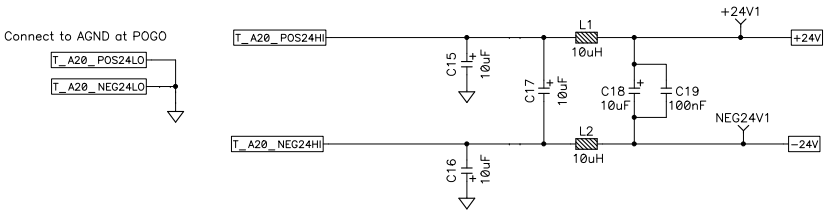


ANALOG DEVICES BV

AD5750 CTS5400 32Ld LFCSP FINAL TEST
SPEC #: GTL50202

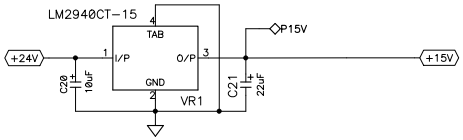
Drawing no.	Rev.	Sh. 7 of 21
02-035657 (WTD01297)	C	

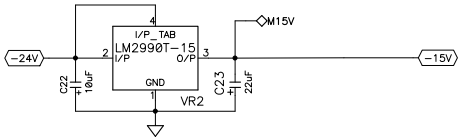
+/−24V Supplies



Please place all regulator as far away as possible from DUT

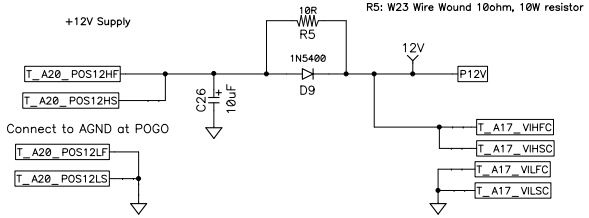
Power Supplies





Relay Checker

R5: W23 Wound 10ohm, 10W resistor



ANALOG DEVICES BV

AD5750 CTS5400 32Ld LFCSP FINAL TEST
SPEC #: GTL50202

Drawing no.	Rev.	Sh.
02−035657 (WTD01297)	C	8 of 21

TEST HEAD ANALOG SLOT ASSIGNMENTS			
SLOT	RESOURCE NAME	CHANNELS	CTS MODEL NO.
A02	V/I TYPE F	6	CTS-4215
A03	V/I TYPE F	6	CTS-4215
A04	V/I TYPE F	6	CTS-4215
A05	LF SOURCE	2	CTS-4220
A06	HF DIGITIZER	2	CTS-4206-3
A07	HF DIGITIZER	2	CTS-4206-3
A08	HF SOURCE		CTS-4221
A09	V/I TYPE F	6	CTS-4215
A10	DC SOURCE	4	CTS-4219
A11	DC SOURCE	4	CTS-4219
A12	RAMP SOURCE	1	CTS-4224
A13	LF SOURSE	2	CTS-4218
A14	LF DIGITIZER	4	CTS-4223
A15	LF DIGITIZER	4	CTS-4223
A16	RELAY DRIVERS	64	CTS-4214
A17	V/I TYPE F	6	CTS-4215
A18	V/I TYPE F	6	CTS-4215
A19	MEASURE MATRIX	N/A	CTS-4240
A20	USER POWER CARD	N/A	CTS-4216

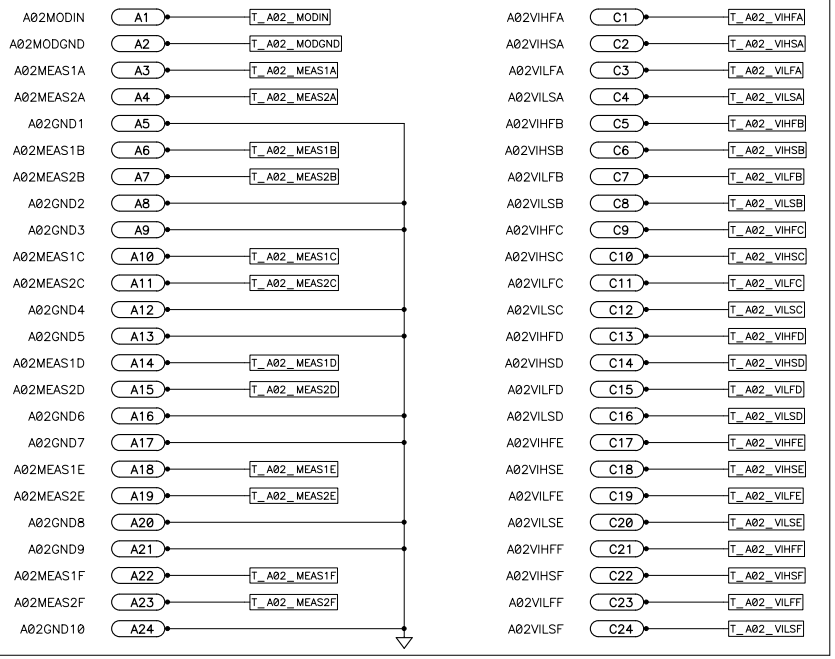
TEST HEAD DIGITAL SLOT ASSIGNMENTS			
SLOT	RESOURCE NAME	CHANNELS	CTS MODEL NO.
D02	CLOCK MATRIX B	2	CTS-4209-1
D03	PINCARD D	8	CTS-4102
D04	PINCARD D	8	CTS-4102
D05	PINCARD D	8	CTS-4102
D06	PINCARD D	8	CTS-4102
D07	PINCARD D	8	CTS-4102
D08	PINCARD D	8	CTS-4102
D09	PINCARD D	8	CTS-4102
D10	PINCARD D	8	CTS-4102
D11	PINCARD D	8	CTS-4102
D12	PINCARD D	8	CTS-4102
D13	PINCARD D	8	CTS-4102
D14	PINCARD D	8	CTS-4102
D15	PINCARD D	8	CTS-4102
D16	PINCARD D	8	CTS-4102
D17	PINCARD D	8	CTS-4102
D18	PINCARD D	8	CTS-4102
D19	TRIGGER PINCARD	8	CTS-4103
D20	DIG SUPPORT C	N/A	CTS-4011

ANALOG DEVICES BV

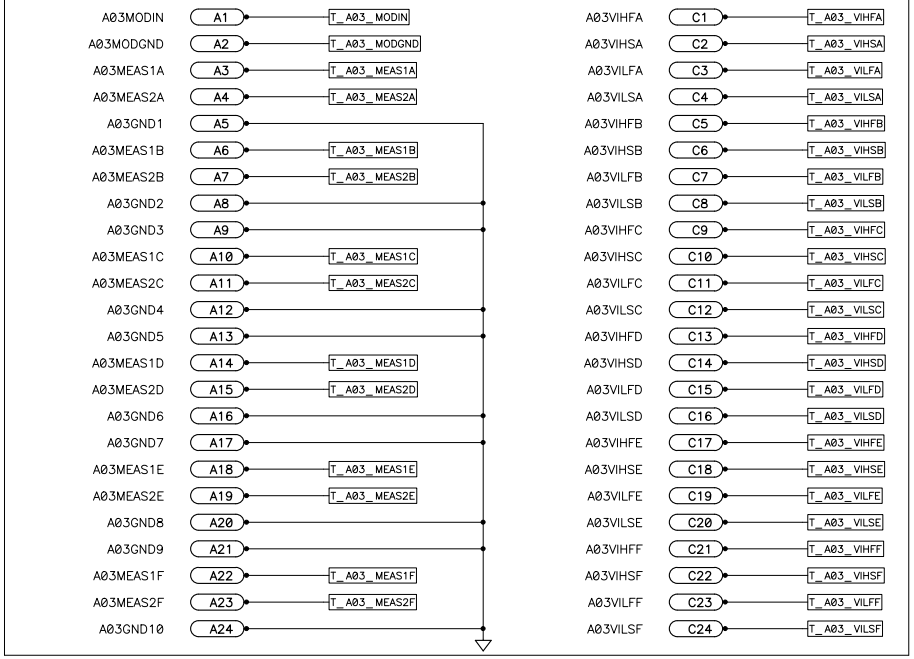
AD5750 CTS5400 32Ld LFCSP FINAL TEST
SPEC #: GTL50202
HEX V/I

Drawing no.	Rev.	
02-035657 (WTD01297)	C	Sh. 9 of 21

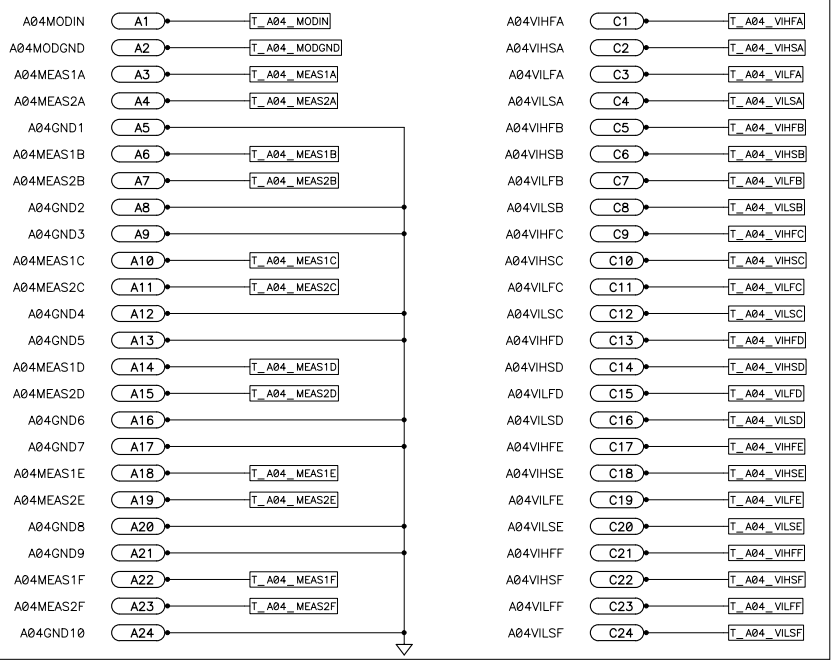
ASLOT2 – HEX V/I



ASLOT3 – HEX V/I



ASLOT4 – HEX V/I

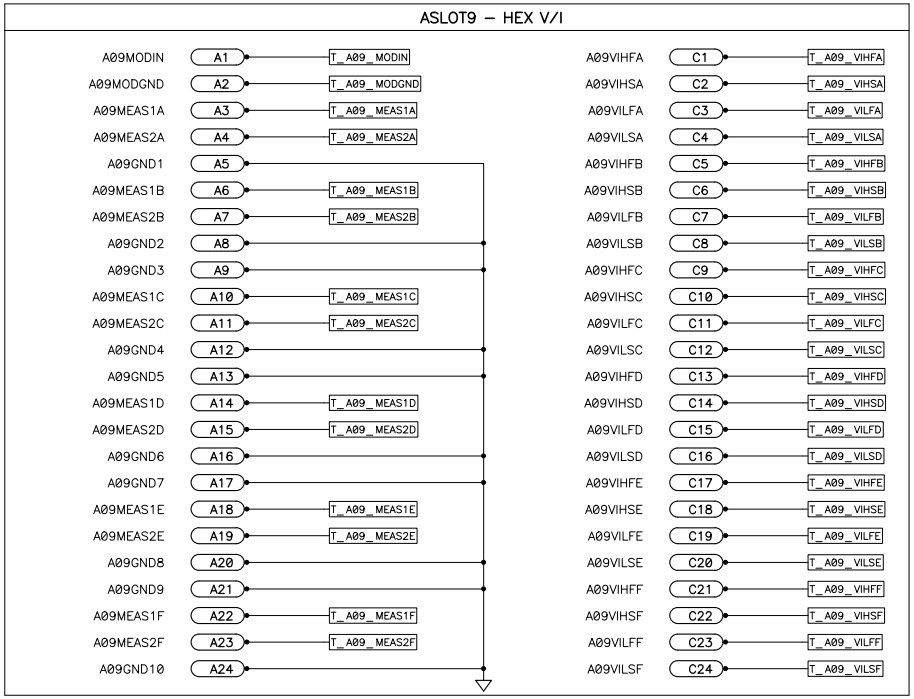


ANALOG DEVICES BV

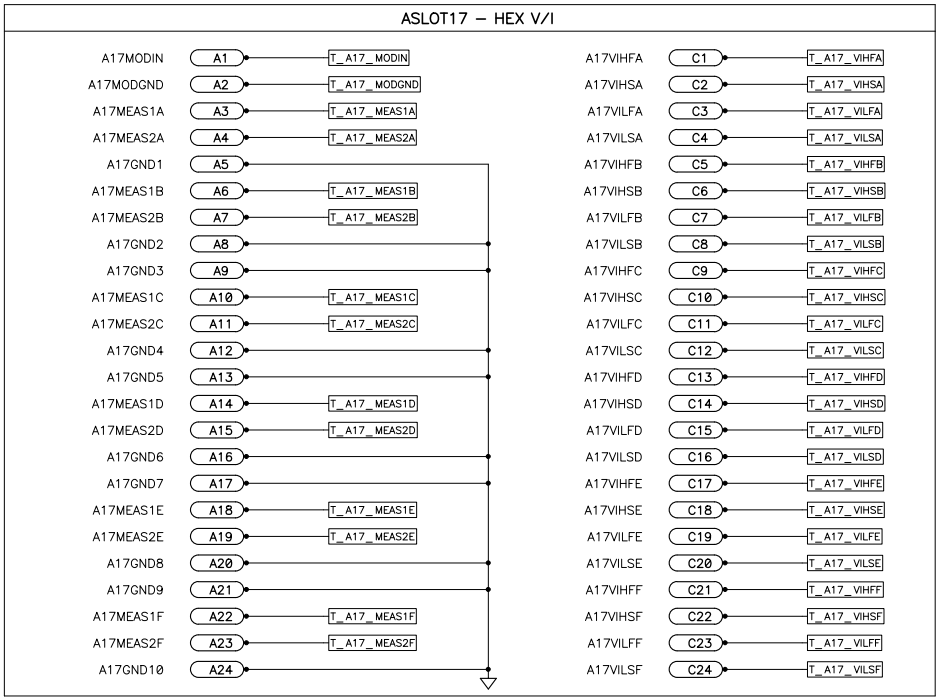
AD5750 CTS5400 32Ld LFCSP FINAL TEST
SPEC #: GTL50202
HEX V/I

Drawing no.	Rev.	Sh.
02-035657 (WTD01297)	C	10 of 21

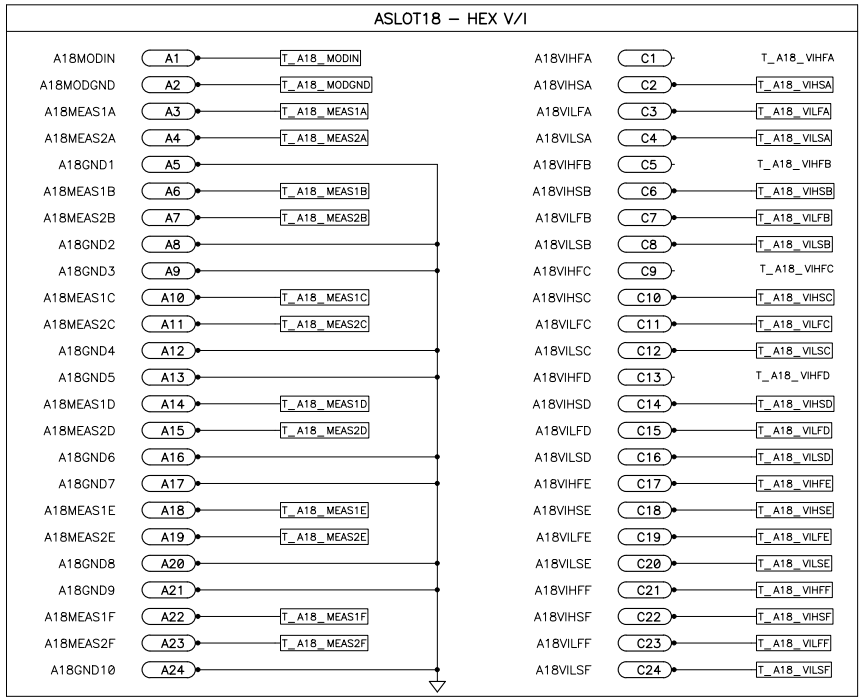
ASLOT9 – HEX V/I



ASLOT17 – HEX V/I



ASLOT18 – HEX V/I

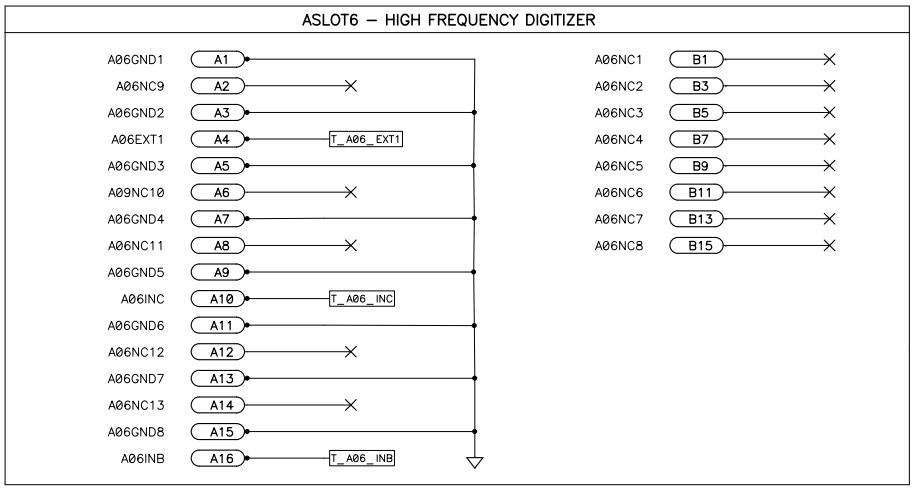


ANALOG DEVICES BV

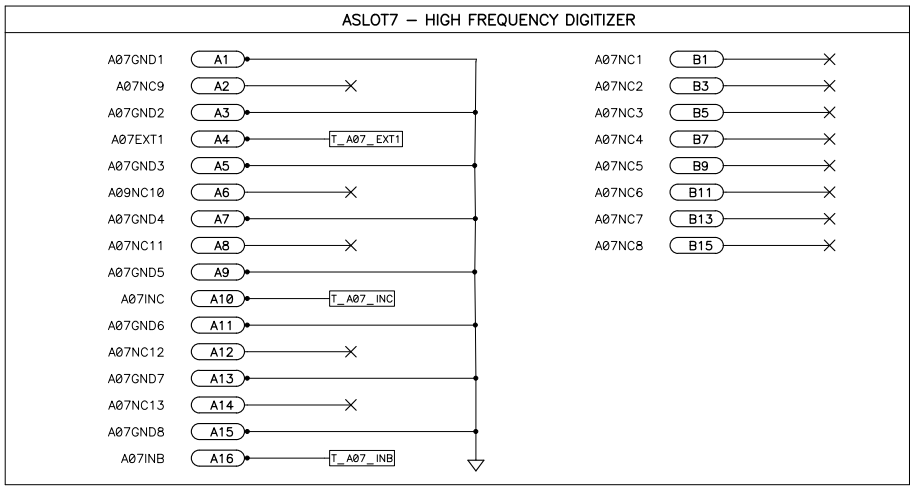
AD5750 CTS5400 32Ld LFCSP FINAL TEST
SPEC #: GTL50202
HEX V/I

Drawing no.	Rev.	Sh. ____ of ____
02-035657 (WTD01297)	C	11 of 21

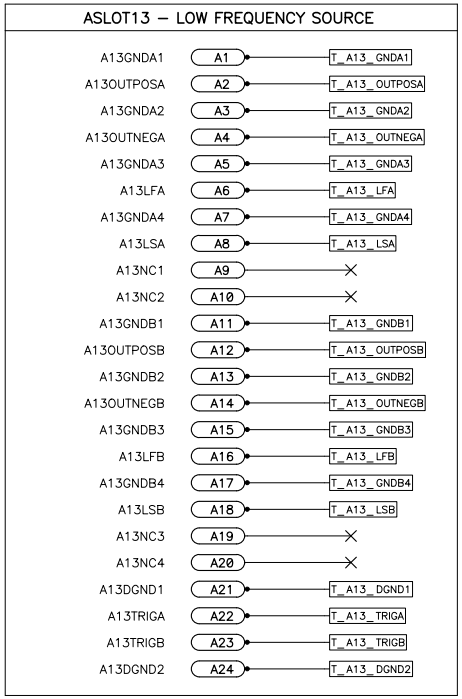
ASLOT6 – HIGH FREQUENCY DIGITIZER



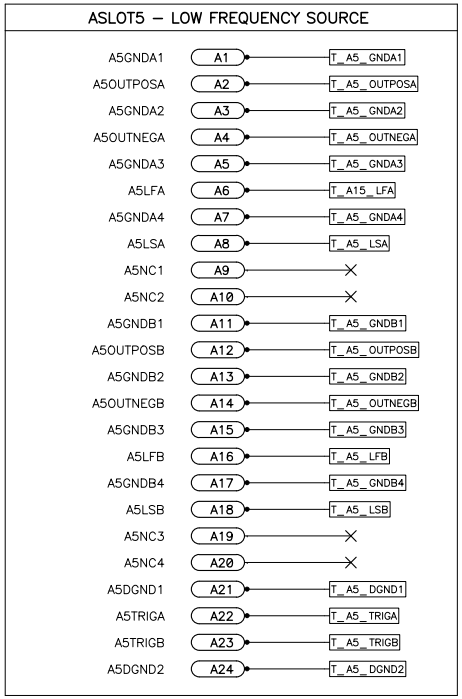
ASLOT7 – HIGH FREQUENCY DIGITIZER



ASLOT13 – LOW FREQUENCY SOURCE



ASLOT5 – LOW FREQUENCY SOURCE



ANALOG DEVICES BV

AD5750 CTS5400 32Ld LFCSP FINAL TEST
SPEC #: GTL50202
HIGH FREQUENCY DIGITIZER
LOW FREQUENCY AWG

Drawing no.	Rev.	Sh. ____ of ____
02-035657 (WTD01297)	C	12 of 21

ASLOT14 – LOW FREQUENCY DIGITIZER (CHANNELS A, B, C, and D = INPUTS 1, 2, 3, and 4)

A1	T_A14_LFD_POS_1A	B1		C1	T_A14_LFD_POS_3A
A2	T_A14_LFD_NEG_1A	B2	T_A14_LFD_GNDSA	C2	T_A14_LFD_NEG_3A
A3	T_A14_LFD_POS_2A	B3		C3	T_A14_LFD_POS_4A
A4	T_A14_LFD_NEG_2A	B4		C4	T_A14_LFD_NEG_4A
A5	T_A14_LFD_POS_1B	B5		C5	T_A14_LFD_POS_3B
A6	T_A14_LFD_NEG_1B	B6	T_A14_LFD_GNDSB	C6	T_A14_LFD_NEG_3B
A7	T_A14_LFD_POS_2B	B7		C7	T_A14_LFD_POS_4B
A8	T_A14_LFD_NEG_2B	B8		C8	T_A14_LFD_NEG_4B
A9	T_A14_LFD_POS_1C	B9		C9	T_A14_LFD_POS_3C
A10	T_A14_LFD_NEG_1C	B10	T_A14_LFD_GNDSC	C10	T_A14_LFD_NEG_3C
A11	T_A14_LFD_POS_2C	B11		C11	T_A14_LFD_POS_4C
A12	T_A14_LFD_NEG_2C	B12		C12	T_A14_LFD_NEG_4C
A13	T_A14_LFD_POS_1D	B13		C13	T_A14_LFD_POS_3D
A14	T_A14_LFD_NEG_1D	B14	T_A14_LFD_GNDSD	C14	T_A14_LFD_NEG_3D
A15	T_A14_LFD_POS_2D	B15		C15	T_A14_LFD_POS_4D
A16	T_A14_LFD_NEG_2D	B16		C16	T_A14_LFD_NEG_4D
A17		B17		C17	
A18		B18		C18	
A19		B19		C19	
A20		B20		C20	
A21		B21		C21	
A22		B22		C22	
A23		B23		C23	
A24		B24		C24	

ASLOT15 – LOW FREQUENCY DIGITIZER (CHANNELS A, B, C, and D = INPUTS 1, 2, 3, and 4)

A1	T_A15_LFD_POS_1A	B1		C1	T_A15_LFD_POS_3A
A2	T_A15_LFD_NEG_1A	B2	T_A15_LFD_GNDSA	C2	T_A15_LFD_NEG_3A
A3	T_A15_LFD_POS_2A	B3		C3	T_A15_LFD_POS_4A
A4	T_A15_LFD_NEG_2A	B4		C4	T_A15_LFD_NEG_4A
A5	T_A15_LFD_POS_1B	B5		C5	T_A15_LFD_POS_3B
A6	T_A15_LFD_NEG_1B	B6	T_A15_LFD_GNDSB	C6	T_A15_LFD_NEG_3B
A7	T_A15_LFD_POS_2B	B7		C7	T_A15_LFD_POS_4B
A8	T_A15_LFD_NEG_2B	B8		C8	T_A15_LFD_NEG_4B
A9	T_A15_LFD_POS_1C	B9		C9	T_A15_LFD_POS_3C
A10	T_A15_LFD_NEG_1C	B10	T_A15_LFD_GNDSC	C10	T_A15_LFD_NEG_3C
A11	T_A15_LFD_POS_2C	B11		C11	T_A15_LFD_POS_4C
A12	T_A15_LFD_NEG_2C	B12		C12	T_A15_LFD_NEG_4C
A13	T_A15_LFD_POS_1D	B13		C13	T_A15_LFD_POS_3D
A14	T_A15_LFD_NEG_1D	B14	T_A15_LFD_GNDSD	C14	T_A15_LFD_NEG_3D
A15	T_A15_LFD_POS_2D	B15		C15	T_A15_LFD_POS_4D
A16	T_A15_LFD_NEG_2D	B16		C16	T_A15_LFD_NEG_4D
A17		B17		C17	
A18		B18		C18	
A19		B19		C19	
A20		B20		C20	
A21		B21		C21	
A22		B22		C22	
A23		B23		C23	
A24		B24		C24	

ASLOT8 – HIGH FREQUENCY SOURCE

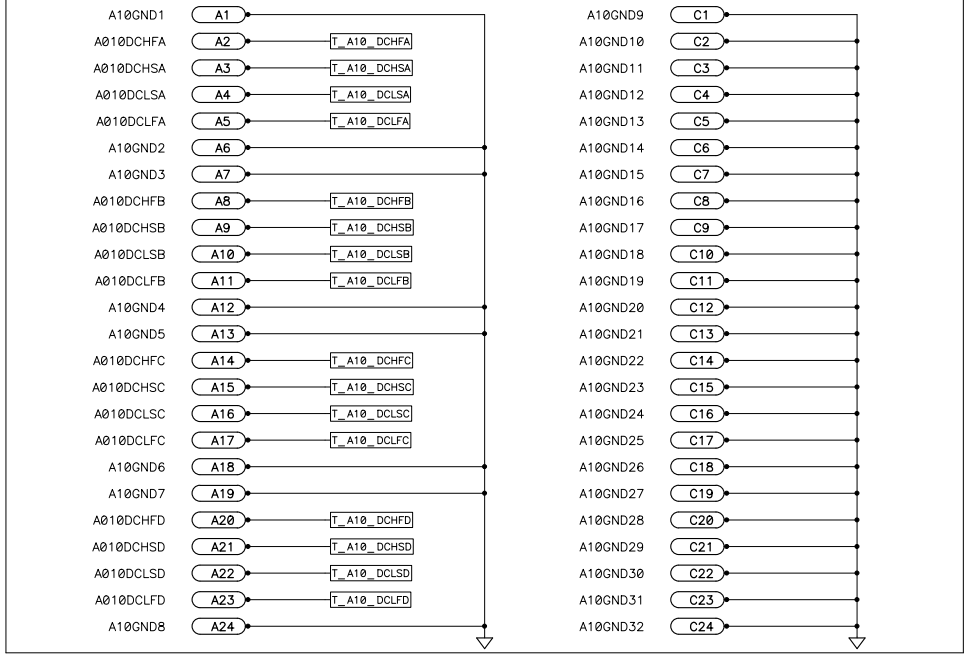
A08CHA	CHA	T_A08_CHA
A08CHA GND	AS1	T_A08_CHA_GND
A08CHA GND	AS2	T_A08_CHA_GND
A08CHA GND	AS3	T_A08_CHA_GND
A08CHA GND	AS4	T_A08_CHA_GND
A08CHB	CHB	T_A08_CHB
A08CHB GND	BS1	T_A08_CHB_GND
A08CHB GND	BS2	T_A08_CHB_GND
A08CHB GND	BS3	T_A08_CHB_GND
A08CHB GND	BS4	T_A08_CHB_GND
A08CHC	CHC	T_A08_CHC
A08CHC GND	CS1	T_A08_CHC_GND
A08CHC GND	CS2	T_A08_CHC_GND
A08CHC GND	CS3	T_A08_CHC_GND
A08CHC GND	CS4	T_A08_CHC_GND
A08CHD	CHD	T_A08_CHD
A08CHD GND	DS1	T_A08_CHD_GND
A08CHD GND	DS2	T_A08_CHD_GND
A08CHD GND	DS3	T_A08_CHD_GND
A08CHD GND	DS4	T_A08_CHD_GND
A08CHE	CHE	T_A08_CHE
A08CHE GND	ES1	T_A08_CHE_GND
A08CHE GND	ES2	T_A08_CHE_GND
A08CHE GND	ES3	T_A08_CHE_GND
A08CHE GND	ES4	T_A08_CHE_GND
A08CHF	CHF	T_A08_CHF
A08CHF GND	FS1	T_A08_CHF_GND
A08CHF GND	FS2	T_A08_CHF_GND
A08CHF GND	FS3	T_A08_CHF_GND
A08CHF GND	FS4	T_A08_CHF_GND

ANALOG DEVICES BV

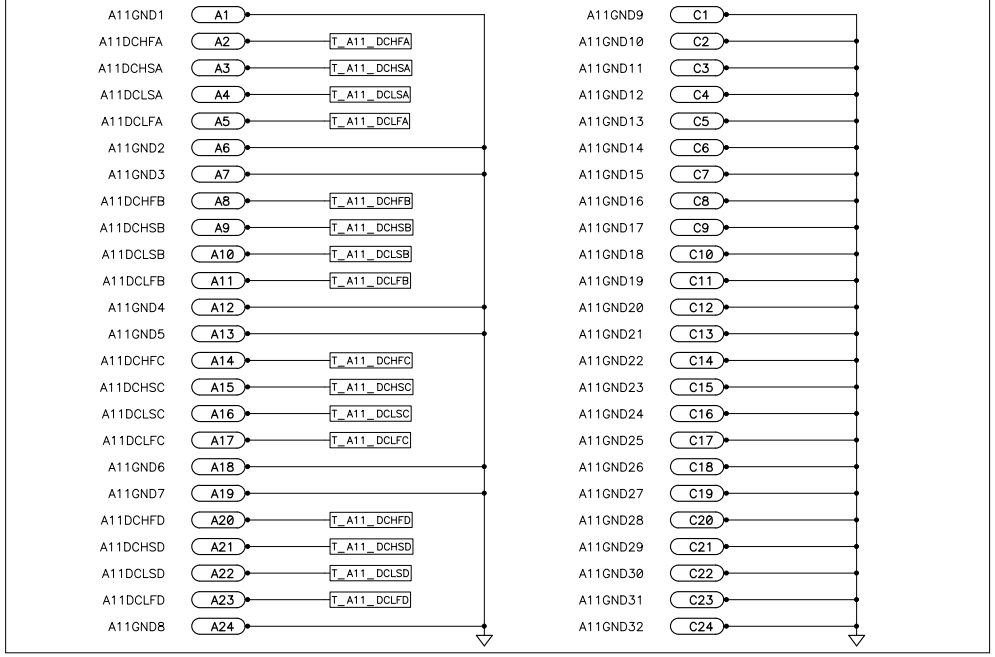
AD5750 CTS5400 32Ld LFCSP FINAL TEST
SPEC #: GTL50202
LOW FREQUENCY DIGITIZER
HIGH FREQUENCY AWG

Drawing no.	Rev.	
02-035657 (WTD01297)	C	Sh. 13 of 21

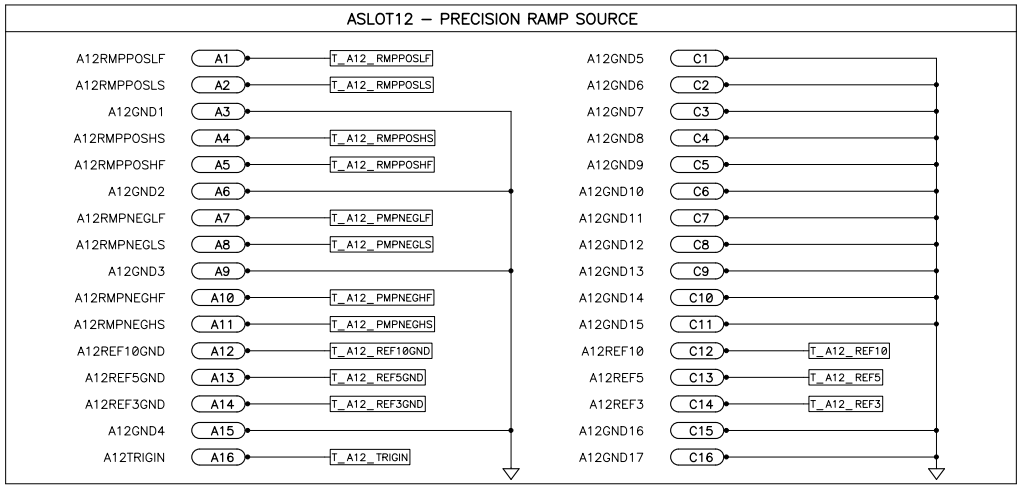
ASLOT10 – QUAD PRECISION DC SOURCE



ASLOT11 – QUAD PRECISION DC SOURCE



ASLOT12 – PRECISION RAMP SOURCE



ANALOG DEVICES BV

AD5750 CTS5400 32Ld LFCSP FINAL TEST
SPEC #: GTL50202

QUAD PRECISION DC SOURCE
PRECISION RAMP SOURCE

Drawing no.	Rev.	
02-035657 (WTD01297)	C	Sh. 14 of 21

ASLOT19 – ANALOG MATRIX

A19MMXVI1HF	A1	T_A19_MMXVI1HF	A19BUSAH1	B1	T_A19_BUSAH1	A19MMXVI3HF	C1	T_A19_MMXVI3HF
A19MMXVI1HS	A2	T_A19_MMXVI1HS	A19BUSAL1	B2	T_A19_BUSAL1	A19MMXVI3HS	C2	T_A19_MMXVI3HS
A19MMXVI1LF	A3	T_A19_MMXVI1LF	A19BUSAH2	B3	T_A19_BUSAH2	A19MMXVI3LF	C3	T_A19_MMXVI3LF
A19MMXVI1LS	A4	T_A19_MMXVI1LS	A19BUSAL2	B4	T_A19_BUSAL2	A19MMXVI3LS	C4	T_A19_MMXVI3LS
A19MMXVI2HF	A5	T_A19_MMXVI2HF	A19BUSAH3	B5	T_A19_BUSAH3	A19MMXVI4HF	C5	T_A19_MMXVI4HF
A19MMXVI2HS	A6	T_A19_MMXVI2HS	A19BUSAL3	B6	T_A19_BUSAL3	A19MMXVI4HS	C6	T_A19_MMXVI4HS
A19MMXVI2LF	A7	T_A19_MMXVI2LF	A19BUSAH4	B7	T_A19_BUSAH4	A19MMXVI4LF	C7	T_A19_MMXVI4LF
A19MMXVI2LS	A8	T_A19_MMXVI2LS	A19BUSAL4	B8	T_A19_BUSAL4	A19MMXVI4LS	C8	T_A19_MMXVI4LS
A19BUSAH5	A9	T_A19_BUSAH5	A19GND1	B9		A19BUSAH7	C9	T_A19_BUSAH7
A19BUSAL5	A10	T_A19_BUSAL5	A19DMMHI	B10	T_A19_DMMHI	A19BUSAL7	C10	T_A19_BUSAL7
A19BUSAH6	A11	T_A19_BUSAH6	A19DMMLO	B11	T_A19_DMMLO	A19BUSAH8	C11	T_A19_BUSAH8
A19BUSAL6	A12	T_A19_BUSAL6	A19DMMI	B12	T_A19_DMMI	A19BUSAL8	C12	T_A19_BUSAL8
A19BUSBH5	A13	T_A19_BUSBH5	A19GND2	B13		A19BUSBH7	C13	T_A19_BUSBH7
A19BUSBL5	A14	T_A19_BUSBL5	A19DMMHS	B14	T_A19_DMMHS	A19BUSBL7	C14	T_A19_BUSBL7
A19BUSBH6	A15	T_A19_BUSBH6	A19DMMLS	B15	T_A19_DMMLS	A19BUSBH8	C15	T_A19_BUSBH8
A19BUSBL6	A16	T_A19_BUSBL6	A19GND3	B16		A19BUSBL8	C16	T_A19_BUSBL8
A19MMXVI5HF	A17	T_A19_MMXVI5HF	A19BUSBH1	B17	T_A19_BUSBH1	A19MMXVI7HF	C17	T_A19_MMXVI7HF
A19MMXVI5HS	A18	T_A19_MMXVI5HS	A19BUSBL1	B18	T_A19_BUSBL1	A19MMXVI7HS	C18	T_A19_MMXVI7HS
A19MMXVI5LF	A19	T_A19_MMXVI5LF	A19BUSBH2	B19	T_A19_BUSBH2	A19MMXVI7LF	C19	T_A19_MMXVI7LF
A19MMXVI5LS	A20	T_A19_MMXVI5LS	A19BUSBL2	B20	T_A19_BUSBL2	A19MMXVI7LS	C20	T_A19_MMXVI7LS
A19MMXVI6HF	A21	T_A19_MMXVI6HF	A19BUSBH3	B21	T_A19_BUSBH3	A19MMXVI8HF	C21	T_A19_MMXVI8HF
A19MMXVI6HS	A22	T_A19_MMXVI6HS	A19BUSBL3	B22	T_A19_BUSBL3	A19MMXV8HS	C22	T_A19_MMXVI8HS
A19MMXVI6LF	A23	T_A19_MMXVI6LF	A19BUSBH4	B23	T_A19_BUSBH4	A19MMXVI8LF	C23	T_A19_MMXVI8LF
A19MMXVI6LS	A24	T_A19_MMXVI6LS	A19BUSBL4	B24	T_A19_BUSBL4	A19MMXVI8LS	C24	T_A19_MMXVI8LS

ASLOT20 – USER POWER SUPPLIES

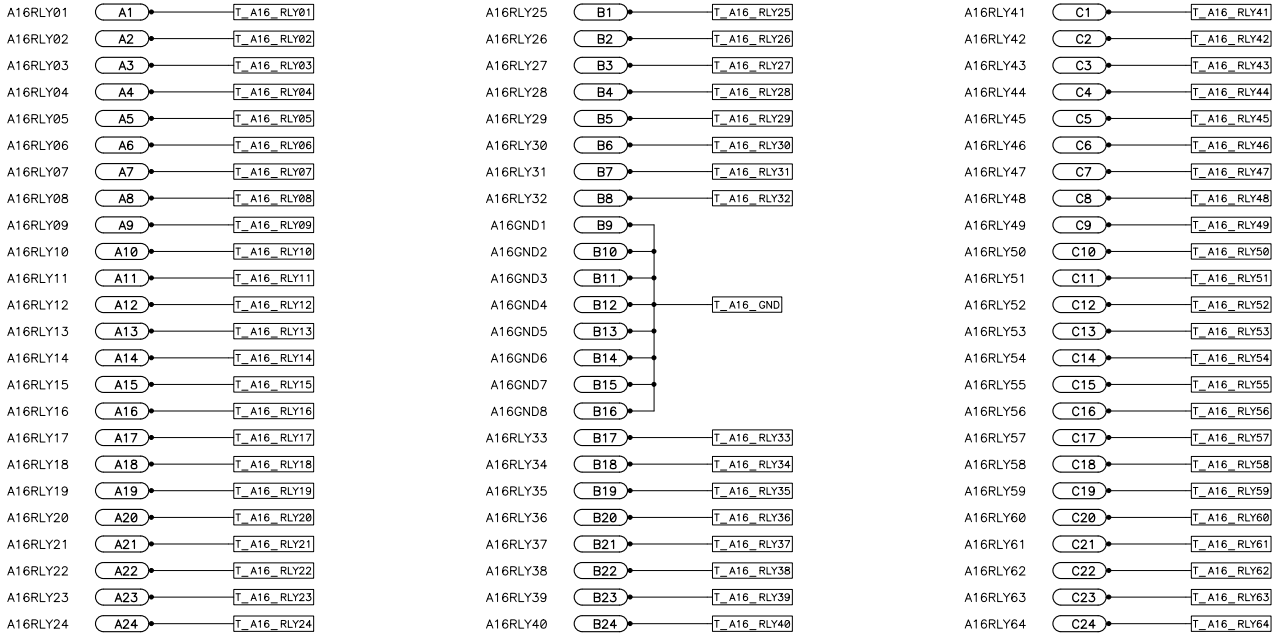
A20NC1	A1		A20NEG24LO	B1	T_A20_NEG24LO	A20NEG24HI	C1	T_A20_NEG24HI
A20UPLS1	A2	T_A20_UPLS1	A20UPLF1_1	B2	T_A20_UPLF1_1	A20UPLF1_2	C2	T_A20_UPLF1_2
A20UPHS1	A3	T_A20_UPHS1	A20UPHF1_1	B3	T_A20_UPHF1_1	A20UPHF1_2	C3	T_A20_UPHF1_2
A20UPLS2	A4	T_A20_UPLS2	A20UPLF2_1	B4	T_A20_UPLF2_1	A20UPLF2_2	C4	T_A20_UPLF2_2
A20UPHS2	A5	T_A20_UPHS2	A20UPHF2_1	B5	T_A20_UPHF2_1	A20UPHF2_2	C5	T_A20_UPHF2_2
A20UPLS3	A6	T_A20_UPLS3	A20UPLF3_1	B6	T_A20_UPLF3_1	A20UPLF3_2	C6	T_A20_UPLF3_2
A20UPHS3	A7	T_A20_UPHS3	A20UPHF3_1	B7	T_A20_UPHF3_1	A20UPHF3_2	C7	T_A20_UPHF3_2
A20UPLS4	A8	T_A20_UPLS4	A20UPLF4_1	B8	T_A20_UPLF4_1	A20UPLF4_2	C8	T_A20_UPLF4_2
A20UPHS4	A9	T_A20_UPHS4	A20UPHF4_1	B9	T_A20_UPHF4_1	A20UPHF4_2	C9	T_A20_UPHF4_2
A20NC2	A10	T_A20_POS12LS	A20POS12LO_1	B10	T_A20_POS12LF	A20POS12LO_2	C10	T_A20_POS12LF
A20NC3	A11	T_A20_POS12HS	A20POS12HI_1	B11	T_A20_POS12HF	A20POS12HI_2	C11	T_A20_POS12HF
A20NC4	A12	T_A20_NEG15LS	A20NEG15LO_1	B12	T_A20_NEG15LF	A20NEG15LO_2	C12	T_A20_NEG15LF
A20NC5	A13	T_A20_NEG15HS	A20NEG15HI_1	B13	T_A20_NEG15HF	A20NEG15HI_2	C13	T_A20_NEG15HF
A20NC6	A14	T_A20_POS15LS	A20POS15LO_1	B14	T_A20_POS15LF	A20POS15LO_2	C14	T_A20_POS15LF
A20NC7	A15	T_A20_POS15HS	A20POS15HI_1	B15	T_A20_POS15HF	A20POS15HI_2	C15	T_A20_POS15HF
A20NC8	A16	T_A20_NEG5LS	A20NEG5LO_1	B16	T_A20_NEG5LF	A20NEG5LO_2	C16	T_A20_NEG5LF
A20NC9	A17	T_A20_NEG5HS	A20NEG5HI_1	B17	T_A20_NEG5HF	A20NEG5HI_2	C17	T_A20_NEG5HF
A20NC10	A18	T_A20_POS5LS	A20POS5LO_1	B18	T_A20_POS5LF	A20POS5LO_2	C18	T_A20_POS5LF
A20NC11	A19	T_A20_POS5HS	A20POS5HI_1	B19	T_A20_POS5HF	A20POS5HI_2	C19	T_A20_POS5HF
A20NC12	A20		A20POS3V3LO_1	B20	T_A20_POS3V3LF	A20POS3V3LO3	C20	T_A20_POS3V3LF
A20NC13	A21	T_A20_POS3V3LS	A20POS3V3LO_2	B21	T_A20_POS3V3LF	A20INTRLKHI	C21	
A20NC14	A22	T_A20_POS3V3HS	A20POS3V3HI_1	B22	T_A20_POS3V3HF	A20INTRLKLO	C22	
A20NC15	A23		A20POS3V3HI_2	B23	T_A20_POS3V3HF	A20POS3V3HI3	C23	T_A20_POS3V3HF
A20NC16	A24		A20POS24LO	B24	T_A20_POS24LO	A20POS24HI	C24	T_A20_POS24HI

ANALOG DEVICES BV

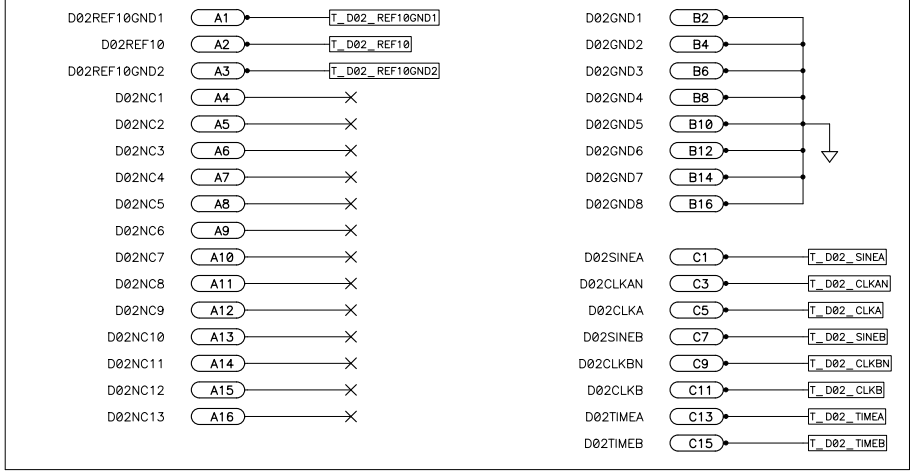
AD5750 CTS5400 32Ld LFCSP FINAL TEST
SPEC #: GTL50202
ANALOG MATRIX
USER POWER SUPPLIES

Drawing no.	Rev.	Sh. 15 of 21
02-035657 (WTD01297)	C	

ASLOT16 – RELAY DRIVERS



DSLOT2 – CLOCK MATRIX

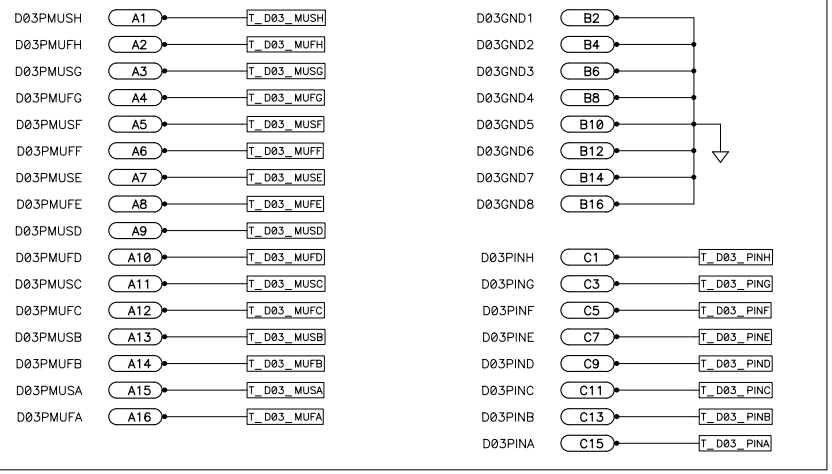


ANALOG DEVICES BV

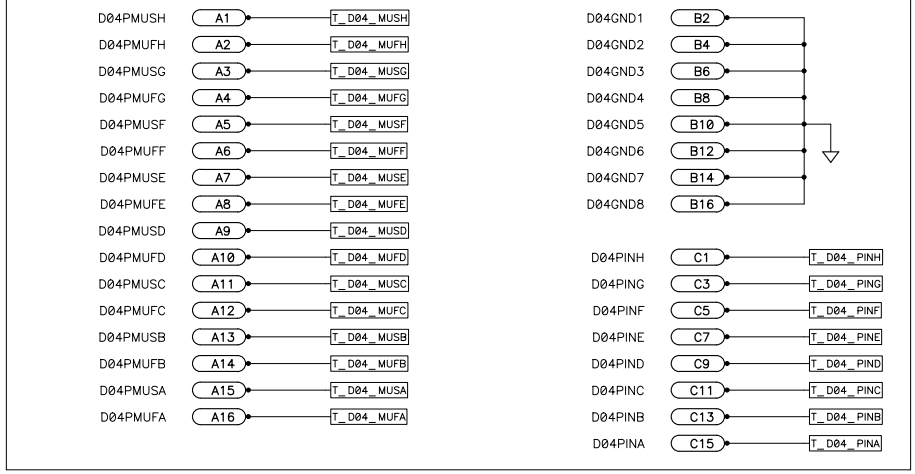
AD5750 CTS5400 32Ld LFCSP FINAL TEST
SPEC #: GTL50202
RELAY DRIVERS
CLOCK MATRIX

Drawing no.	Rev.	Sh. _16_ of _21_
02-035657 (WTD01297)	C	

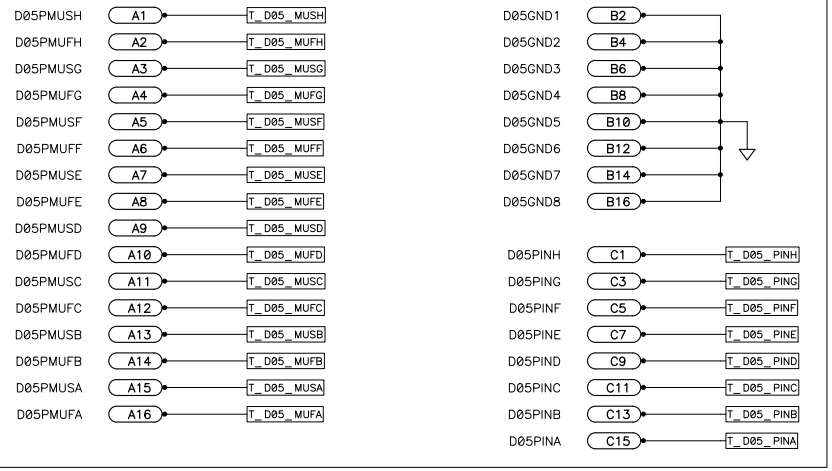
DSLOT3 – DIGITAL PIN DRIVERS



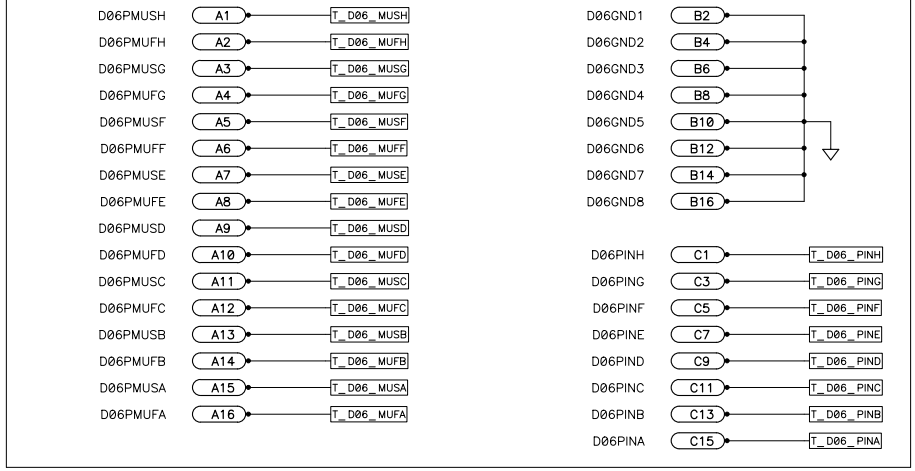
DSLOT4 – DIGITAL PIN DRIVERS



DSLOT5 – DIGITAL PIN DRIVERS



DSLOT6 – DIGITAL PIN DRIVERS

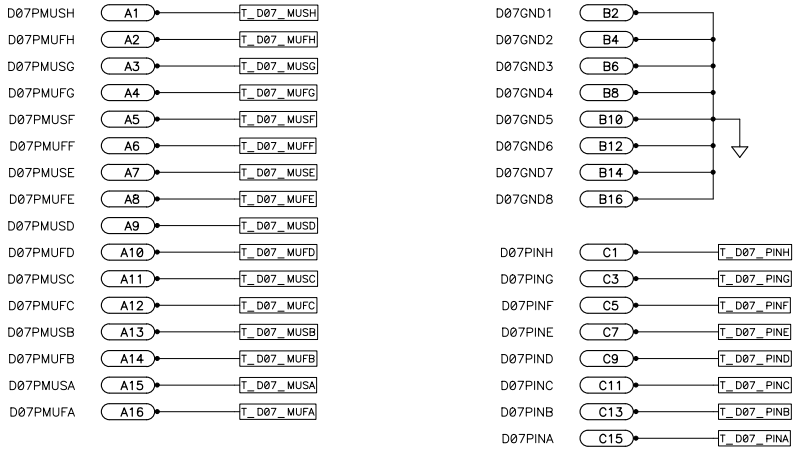


ANALOG DEVICES BV

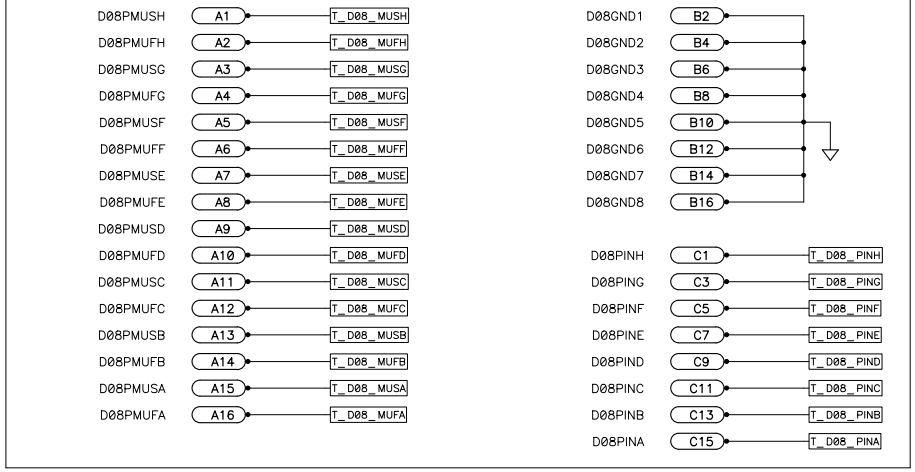
AD5750 CTS5400 32Ld LFCSP FINAL TEST
SPEC #: GTL50202
DIGITAL PIN DRIVERS

Drawing no.	Rev.	
02-035657 (WTD01297)	C	Sh. 17 of 21

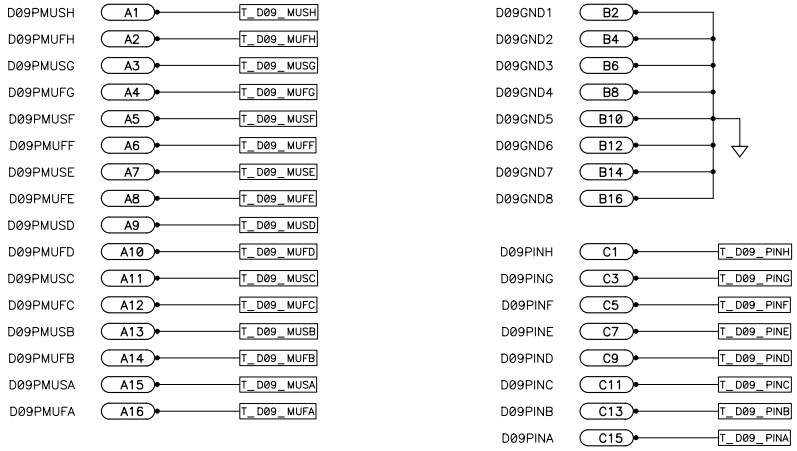
DSLOT7 – DIGITAL PIN DRIVERS



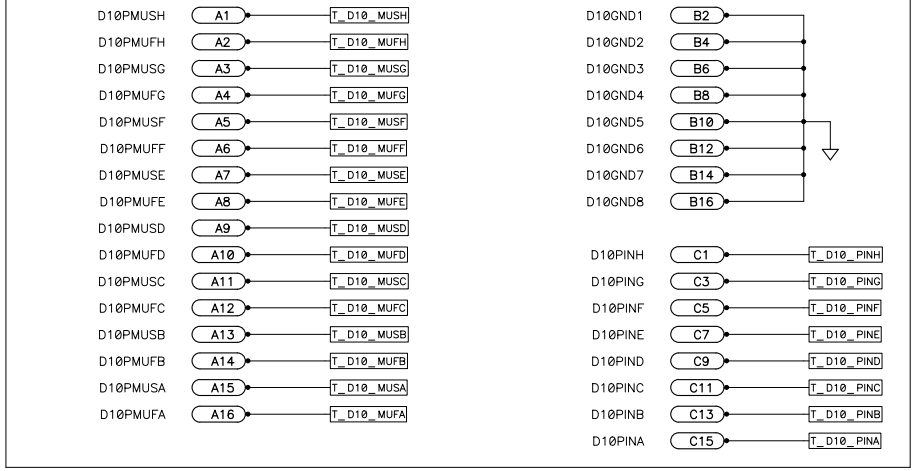
DSLOT8 – DIGITAL PIN DRIVERS



DSLOT9 – DIGITAL PIN DRIVERS



DSLOT10 – DIGITAL PIN DRIVERS

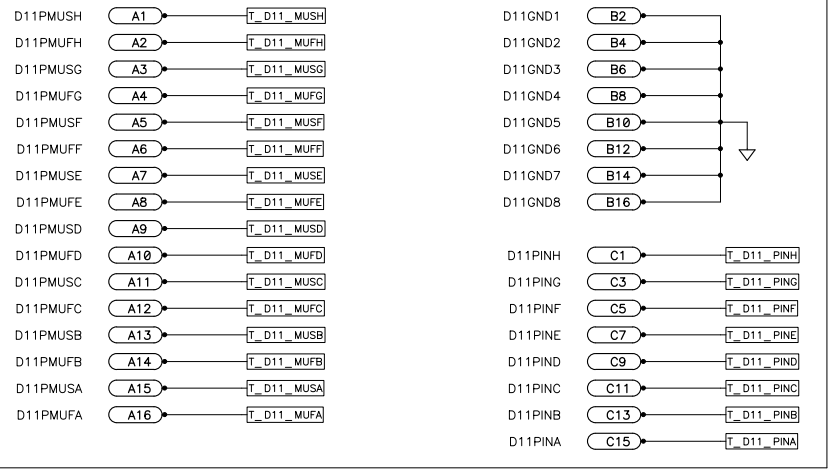


ANALOG DEVICES BV

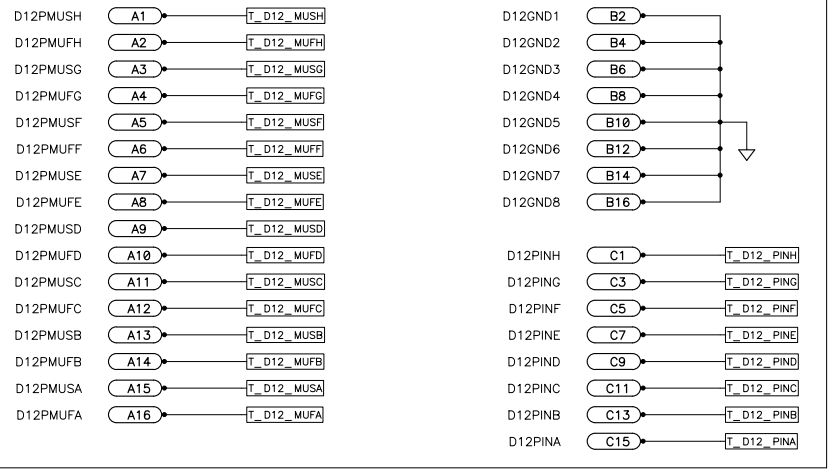
AD5750 CTS5400 32Ld LFCSP FINAL TEST
SPEC #: GTL50202
DIGITAL PIN DRIVERS

Drawing no.	Rev.	
WTD01297	C	Sh. 18 of 21

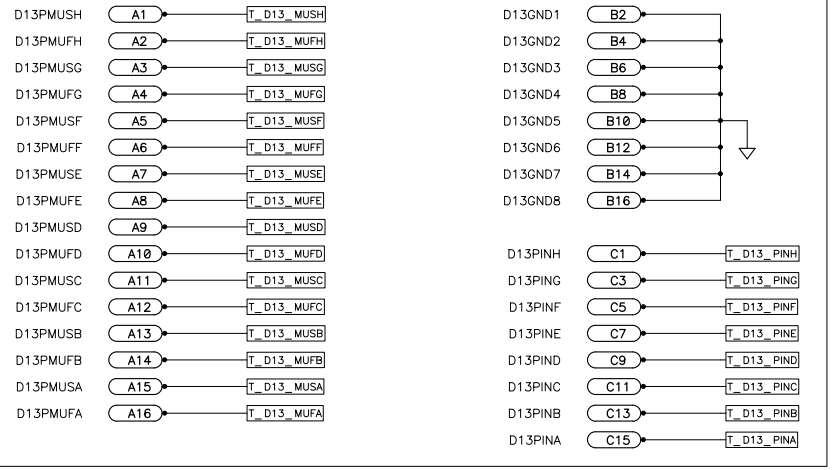
DSLOT11 – DIGITAL PIN DRIVERS



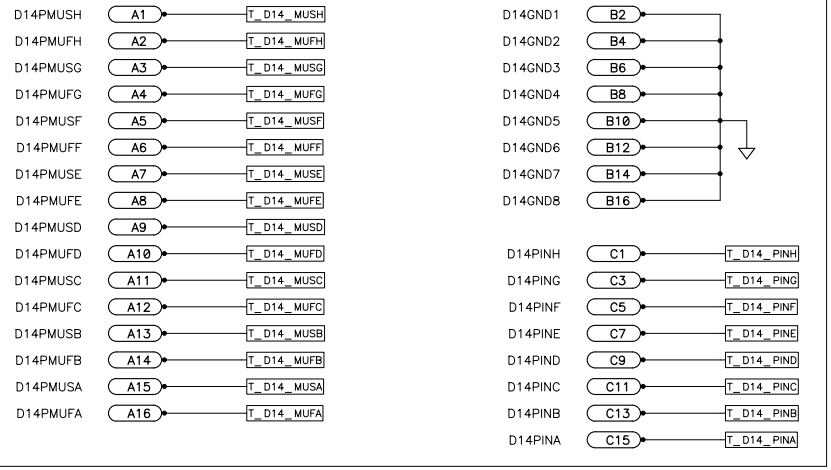
DSLOT12 – DIGITAL PIN DRIVERS



DSLOT13 – DIGITAL PIN DRIVERS



DSLOT14 – DIGITAL PIN DRIVERS

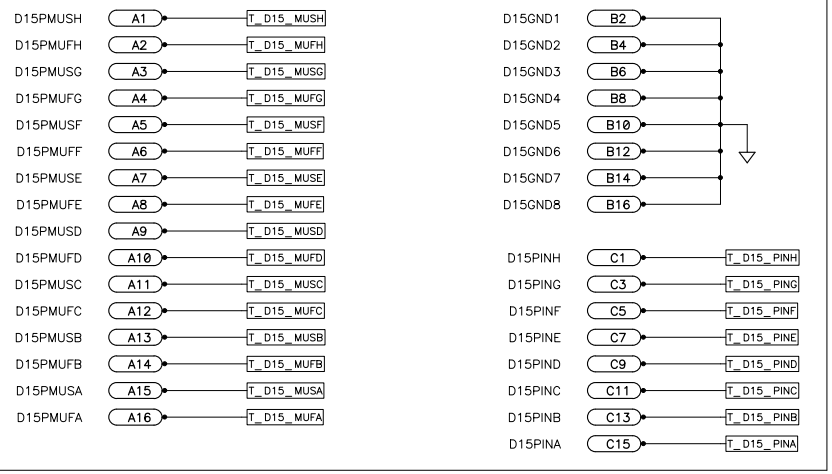


ANALOG DEVICES BV

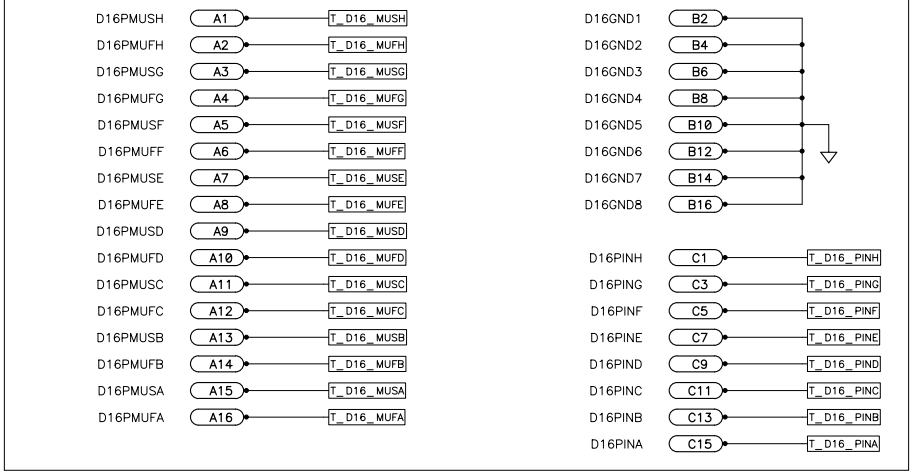
AD5750 CTS5400 32Ld LFCSP FINAL TEST
SPEC #: GTL50202
DIGITAL PIN DRIVERS

Drawing no.	Rev.	Sh. 19 of 21
02-035657 (WTD01297)	C	

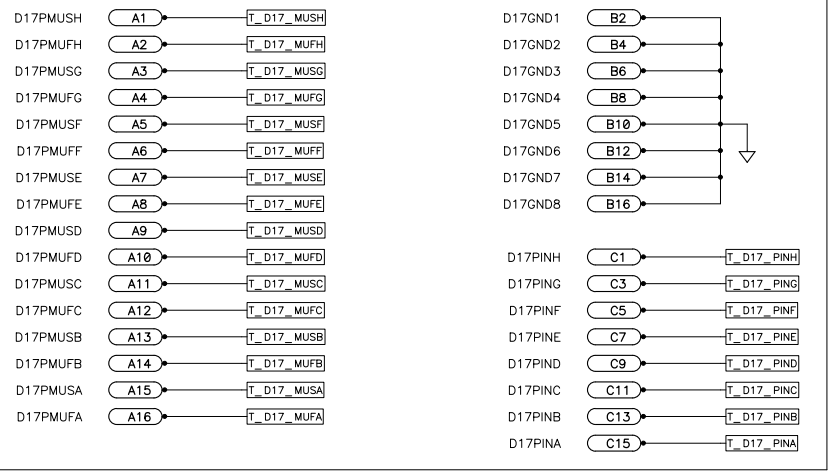
DSLOT15 – DIGITAL PIN DRIVERS



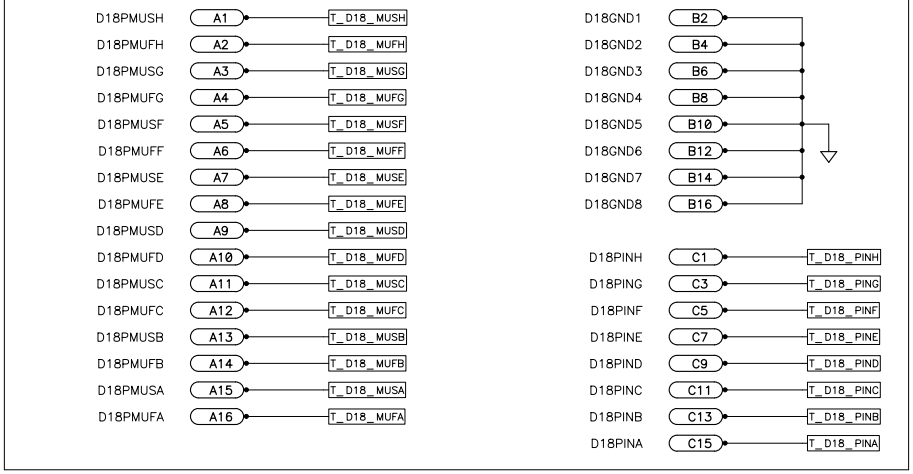
DSLOT16 – DIGITAL PIN DRIVERS



DSLOT17 – DIGITAL PIN DRIVERS



DSLOT18 – DIGITAL PIN DRIVERS



ANALOG DEVICES BV

AD5750 CTS5400 32Ld LFCSP FINAL TEST
SPEC #: GTL50202
DIGITAL PIN DRIVERS

Drawing no.	Rev.	
02-035657 (WTD01297)	C	Sh. 20 of 21

DSLOT19 – TRIGGER RESOURCE

D19PMUSH	A1	T_D19_PMUSH
D19PMUFH	A2	T_D19_PMUFH
D19PMUSG	A3	T_D19_PMUSG
D19PMUFG	A4	T_D19_PMUFG
D19PMUSF	A5	T_D19_PMUSF
D19PMUFF	A6	T_D19_PMUFF
D19PMUSE	A7	T_D19_PMUSE
D19PMUFE	A8	T_D19_PMUFE
D19PMUSD	A9	T_D19_PMUSD
D19PMUFD	A10	T_D19_PMUFD
D19PMUSC	A11	T_D19_PMUSC
D19PMUFC	A12	T_D19_PMUFC
D19PMUSB	A13	T_D19_PMUSB
D19PMUFB	A14	T_D19_PMUFB
D19PMUSA	A15	T_D19_PMUSA
D19PMUFA	A16	T_D19_PMUFA

D19GND1	B2
D19GND2	B4
D19GND3	B6
D19GND4	B8
D19GND5	B10
D19GND6	B12
D19GND7	B14
D19GND8	B16

DSLOT20 – DIGITAL SUPPORT CARD

D20DAT0	A1	T_D20_DAT00
D20DAT1	A2	T_D20_DAT01
D20GND1	A3	
D20DAT2	A4	T_D20_DAT02
D20DAT3	A5	T_D20_DAT03
D20GND2	A6	
D20DAT4	A7	T_D20_DAT04
D20DAT5	A8	T_D20_DAT05
D20GND3	A9	
D20DAT6	A10	T_D20_DAT06
D20DAT7	A11	T_D20_DAT07
D20GND4	A12	
D20DAT8	A13	T_D20_DAT08
D20DAT9	A14	T_D20_DAT09
D20GND5	A15	
D20DAT10	A16	T_D20_DAT10
D20DAT11	A17	T_D20_DAT11
D20GND6	A18	
D20DAT12	A19	T_D20_DAT12
D20DAT13	A20	T_D20_DAT13
D20GND7	A21	
D20DAT14	A22	T_D20_DAT14
D20DAT15	A23	T_D20_DAT15
D20GND8	A24	

D20DAT16	B1	T_D20_DAT16
D20DAT17	B2	T_D20_DAT17
D20GND9	B3	
D20DAT18	B4	T_D20_DAT18
D20DAT19	B5	T_D20_DAT19
D20GND10	B6	
D20DAT20	B7	T_D20_DAT20
D20DAT21	B8	T_D20_DAT21
D20GND11	B9	
D20DAT22	B10	T_D20_DAT22
D20DAT23	B11	T_D20_DAT23
D20GND12	B12	
D20DAT24	B13	T_D20_DAT24
D20DAT25	B14	T_D20_DAT25
D20GND13	B15	
D20DAT26	B16	T_D20_DAT26
D20DAT27	B17	T_D20_DAT27
D20GND14	B18	
D20DAT28	B19	T_D20_DAT28
D20DAT29	B20	T_D20_DAT29
D20GND15	B21	
D20DAT30	B22	T_D20_DAT30
D20DAT31	B23	T_D20_DAT31
D20GND16	B24	

D20DB00	C1	T_D20_DB00
D20DB01	C2	T_D20_DB01
D20DB02	C3	T_D20_DB02
D20DB03	C4	T_D20_DB03
D20DB04	C5	T_D20_DB04
D20DB05	C6	T_D20_DB05
D20DB06	C7	T_D20_DB06
D20DB07	C8	T_D20_DB07
D20DB08	C9	T_D20_DB08
D20DB09	C10	T_D20_DB09
D20DB10	C11	T_D20_DB10
D20DB11	C12	T_D20_DB11
D20DB12	C13	T_D20_DB12
D20DB13	C14	T_D20_DB13
D20DB14	C15	T_D20_DB14
D20DB15	C16	T_D20_DB15
D20USRSTRBN	C17	T_D20_USRSTRBN
D20USRRDN	C18	T_D20_USRRDN
D20REG0N	C19	T_D20_REG0N
D20REG1N	C20	T_D20_REG1N
D20REG2N	C21	T_D20_REG2N
D20REG3N	C22	T_D20_REG3N
D20REG4N	C23	T_D20_REG4N
D20REG5N	C24	T_D20_REG5N

ANALOG DEVICES BV

AD5750 CTS5400 32Ld LFCSP FINAL TEST
SPEC #: GTL50202
TRIGGER RESOURCES
DIGITAL SUPPORT CARD

Drawing no.	Rev.	Sh. 21 of 21
02-035657 (WTD01297)	C	