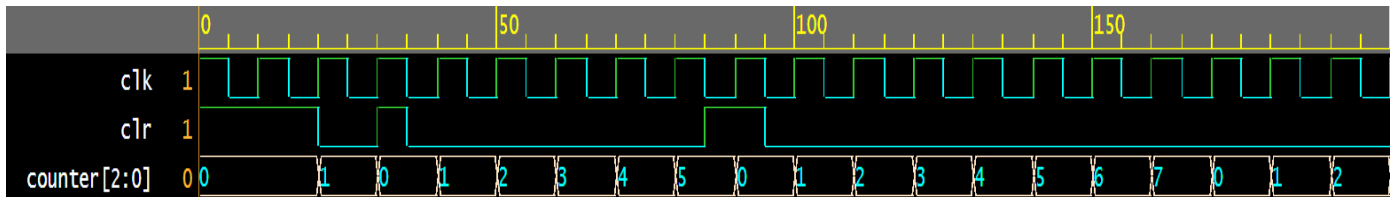


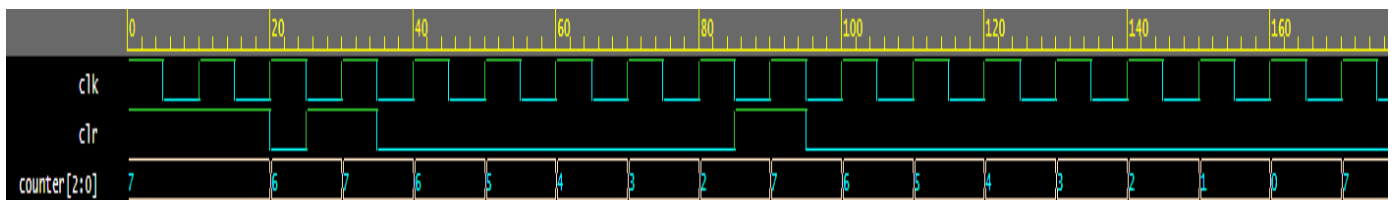
REPORTE TAREA 1- IEE2753

EDUARDO ANAIS

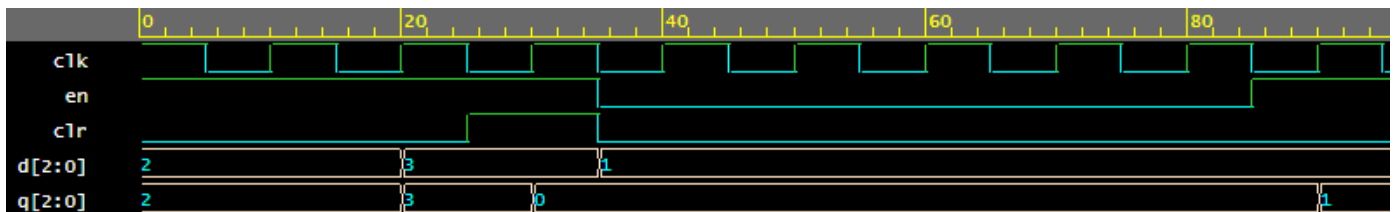
COUNTER UP:



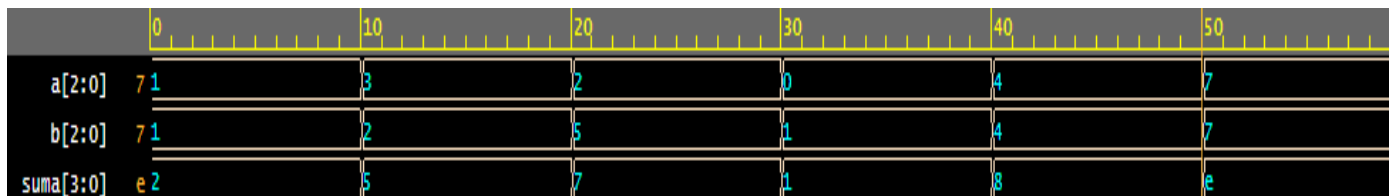
COUNTER DOWN:



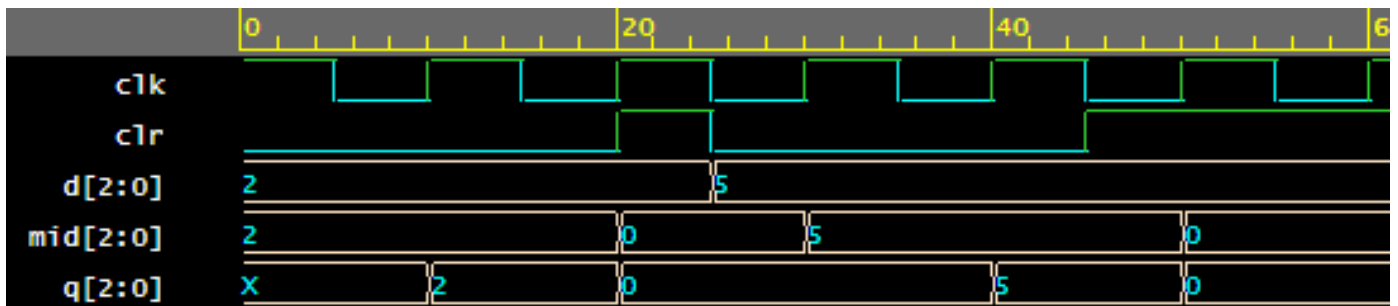
REGISTER:



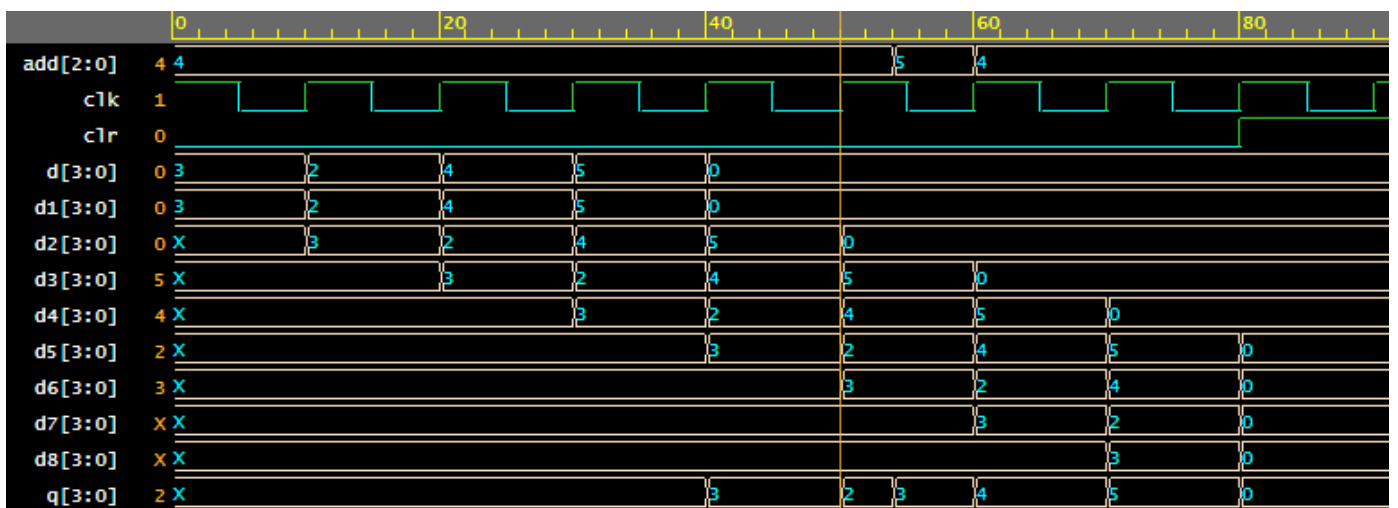
ADDER:



DELAY2: el registro mid se utilizó dentro del módulo, no es I/O



ASR8: los dx's corresponden a los registros internos del módulo



ROM8:

