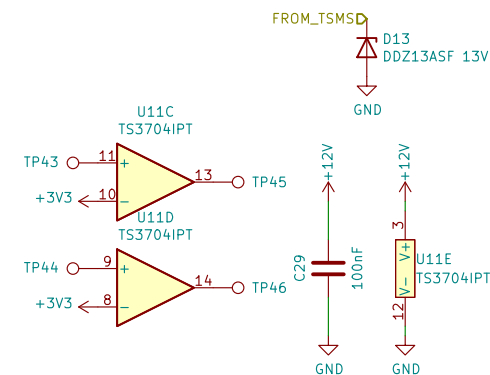
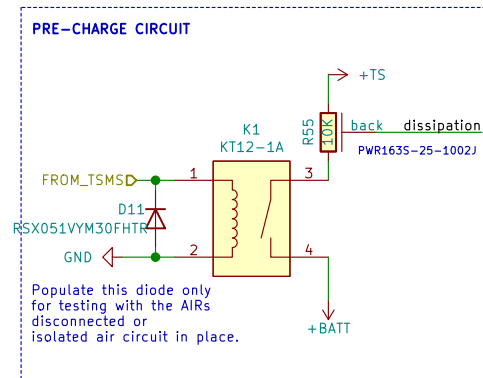
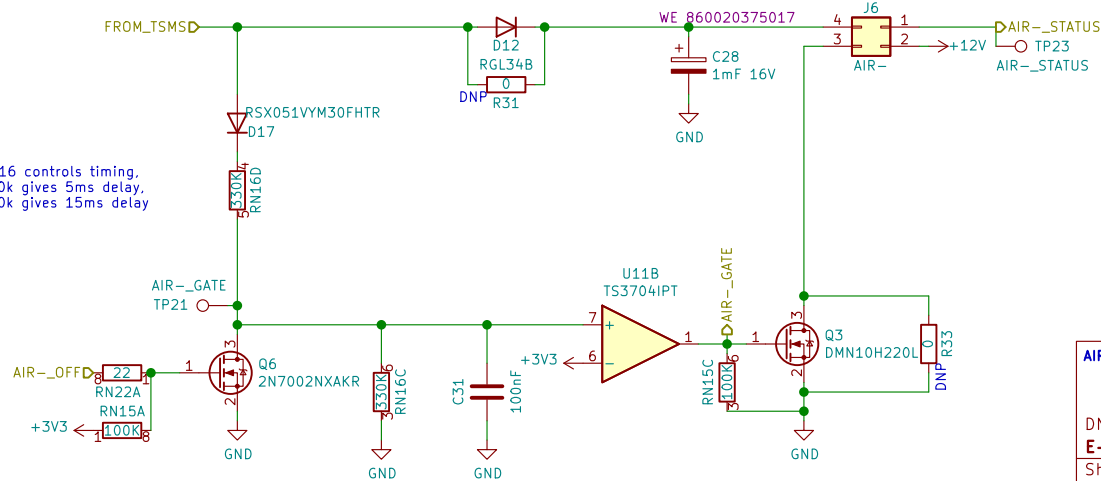


Fallback configuration:  
place only:

R30  
R31  
R33  
U11  
RN15  
RN16  
RN22  
Q7  
C37  
Jumper:  
JP4 23  
JP5 23

RN16 controls timing,  
100k gives 5ms delay,  
330k gives 15ms delay



#### AIR CONTROL CIRCUIT

DNP: Do Not Place, optional

E-Agle TRT

Sheet: /AIRs/

File: AIRs.sch

**Title: Fenice BMS – MainBoard**

Size: A4 Date: 2021-05-15

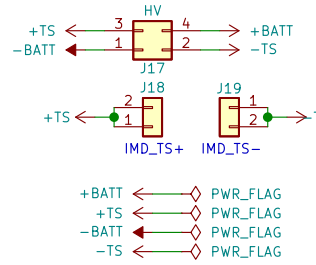
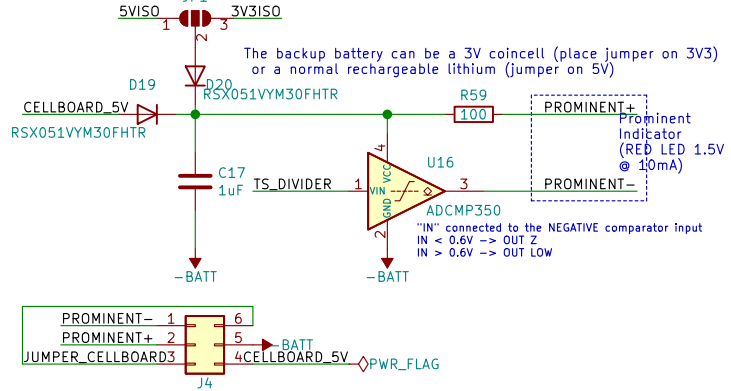
KiCad E.D.A. kicad 5.1.10

The AIRs have internal  
suppression that caps at 55V,  
don't put flyback diodes on the  
FROM\_TSMS line that clips it lower.

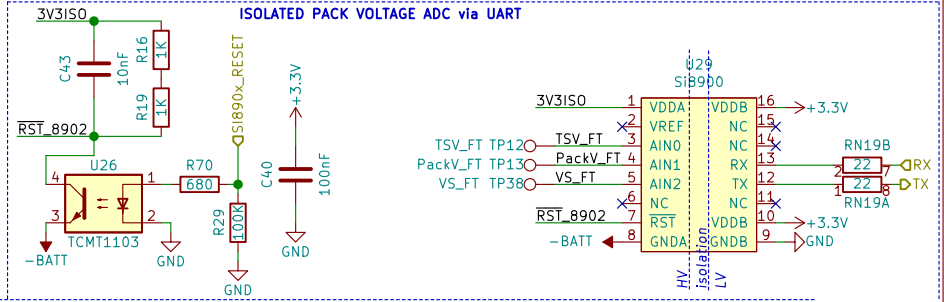
Rev: v1.1

Id: 4/6

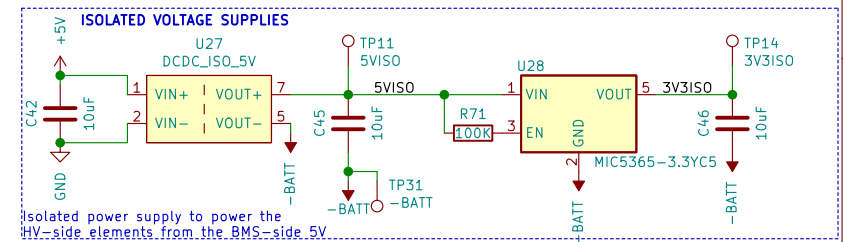
## PROMINENT INDICATOR



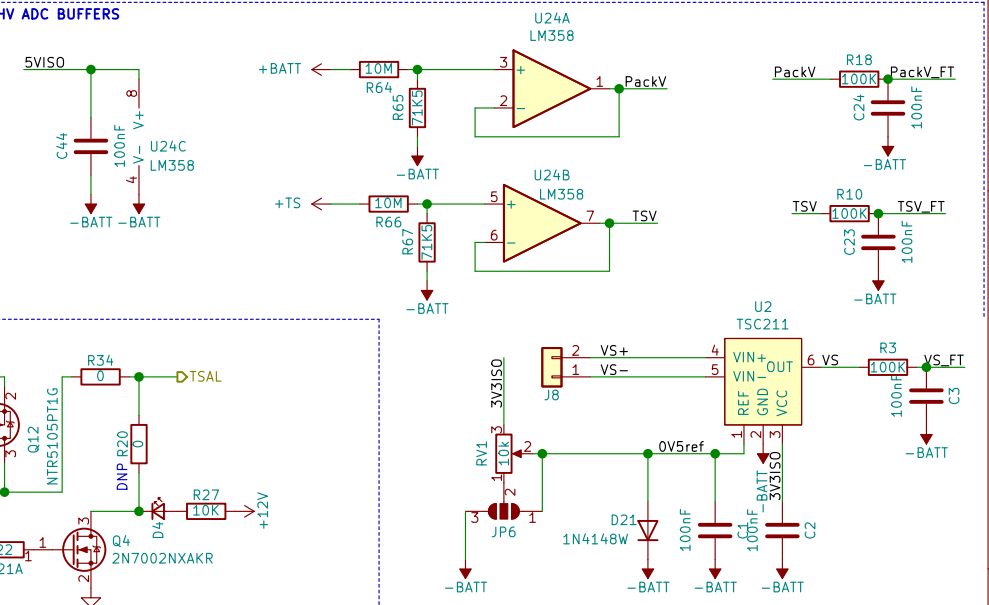
## ISOLATED PACK VOLTAGE ADC via UART



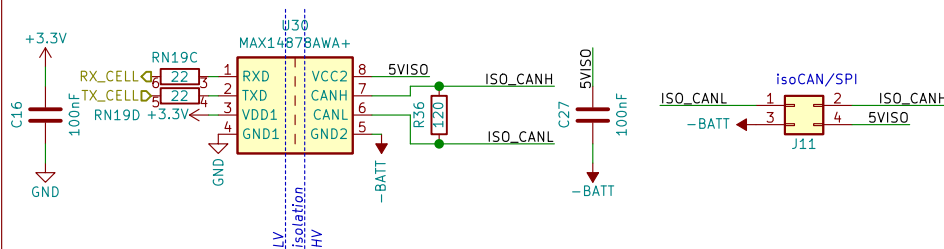
## ISOLATED VOLTAGE SUPPLIES



## HV ADC BUFFERS



## isoCAN INTERFACE



R20: place to invert TSAL logic (remove R34 in this case)

DNP: Do Not Place, optional

E-Agle TRT

Sheet: /HV/

File: HVSide.sch

Title: Fenice BMS - MainBoard

Size: A4

Date: 2021-05-15

KiCad E.D.A. kicad 5.1.10

Rev: v1.1

Id: 5/6

