

Critic: Designing a Sound Static Analyzer for Chisel

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I. INTRODUCTION

Static analysis for hardware designs is a useful augmentation to the traditional testbench validation approach commonly undertaken by hardware designers. High assurance settings such as flight systems, defense, and medicine impose strong behavioral requirements on systems, and static analysis is one way to meet such requirements. By emphasizing soundness in the design of a static analyzer, it is possible to provide provable guarantees about systems being analyzed.

II. CONCLUSION

The conclusion goes here.

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REFERENCES

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