## EARNEST P VEKARIYA

AMRUT PALACE, A-401;

giriraj main road;

Behind bus stand;

Junagadh 362001,

Gujarat

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**OBJECTIVE** Worked on microcontroller based project.

Want to design various emmbedded system which will enable automization of various machine, save energy sources, increase precision and accuracy in work and also devolp various safety equipment

EDUCATION

| EDUCATION |        |                        |                    |           |                |
|-----------|--------|------------------------|--------------------|-----------|----------------|
|           | Degree | School                 | University         | year      | Pass           |
|           |        |                        |                    |           | Percentage     |
|           | B.tech | Carmel Convent school, | Dharmsingh Desai   | 2014-2018 | 7.84 SPI       |
|           | E.C.E  | $_{ m Junagadh}$       | University, Nadiad |           | (1st,2nd year) |

### **PROJECTS**

- 1. Robot for PIZZA Delivery Theme in e-YANTRA'15
- 2. Devolp a game in c
- 3. Atmega32 and 8051 base projects.
- 4. Project based on digital electronics

# TRAINING & INTERNSHIP

• e-YANTRA Summer Internship -2016

### RESEARCH PUBLICATION

1.

# TECHNICAL SKILL

- $\bullet\,$  programming language
  - C language
  - embedded C
  - assembly language
  - C++

- knowledge of micro controller:
  - -8051
  - Avr atmega32
  - atmel ATMEGA2560
- knowledge of designing circuit of digital elactronics

### SOFT SKILL

- 1. team work
- 2. management

## EXTRA -CURRICULAR ACTIVITIES

- chess
- swimming
- $\bullet$  travelling

## CO-CURRICULAR ACTIVITIES

- 1. project based on microcontroller
- 2. participating in technical events

#### **Personal Details**

Father's name: Popatlal Bhadabhai Vekariya Mother's name: Ashaben Popatlal Vekariya

Sex: MALE

Date of birth: SEPTEMBER 28, 1996

Nationality: INDIAN Marital status: SINGLE

#### Reference