ELET 4780 – Senior Project 1 – FALL 2015

To: Elias Kougianos

From: George Dunson

CC: Chris Harris

Date: 11/22/2015

Re: Senior Design Weekly Report – TEAM 1

# Overview

This week we concentrated our focus on getting the first version of our custom PCB started. There are a lot of things to consider when designing a board and we tried to address some of the main issues in our project without letting scope creep get the best of us. We realize that we will most likely need to make some changes to the board in regards to additional features. We will address the need for additional hardware while we are debugging the first rev of the board.

# Completed Tasks

* Designed v1 of the custom PCB

# Ongoing Tasks

* Firmware development
* Custom PCB revisions and debugging

# Next Week’s Tasks

* Order PCBs from fab house
* Order the enclosure
* Write final report

# Interaction with the other team members

* Spent 3 hours on Friday and 4 hours on Saturday discussing and designing the PCB
* Got together each day in spare time between classes to brainstorm about how the program flow will look and discus what we had learned about the Edison during our independent research.