

---

MODULE *OneUpdateMeta*

---

This module is a formal specification of 1-Update,  
a hybrid invalidate and update cache coherence protocol that appears in *PACT'21*.  
This spec actually includes two variants of 1-Update one in which acks for updates  
are gathered directly by the directory (the main variant discussed in the paper);  
and another variant where acks for updates are gathered by the writer itself.  
Setting the constant *ENABLE\_DIR\_ACKS* TRUE or FALSE verifies either variant accordingly.

EXTENDS     *Integers, FiniteSets*

CONSTANTS    *CORES*,  
                  *MAX\_WRITES*,  
                  *WRITE\_TO\_UPDATE*,     *I.e., number of write on which we will trigger the Update*  
                  *ENABLE\_DIR\_ACKS*        - If TRUE: update acks are gathered by the directory.  
  - If FALSE: update acks are gathered by the writer.

VARIABLES    variable prefixes – > *g*:global, *d*: directory, *c*: cache/core | *VECTORS* indexed by *cache/core\_id*  
                  GLOBAL variables  
                  *Msgs*,  
                  *gBcstMsg*,  
                  *gBcstMsgRcvrs*,  
                  Dir variables  
                  *dOwner*,  
                  *dSharers*,     No sharers/owner: *.readers = {} / .owner = 0*  
                  *dReqPending*,  
                  *dState*,  
                  *dRcvAcks*,  
                  Cache/core variables  
                  *cState*,  
                  *cRcvAcks*,  
                  data is a monotonically increasing int to check correctness invariants  
                  *cData*,  
                  *mData*     Memory data

*vars*  $\triangleq$   $\langle dOwner, dSharers, dReqPending, dState, dRcvAcks,$   
                  *cState, cRcvAcks, cData,*  
                  *mData, Msgs, gBcstMsg, gBcstMsgRcvrs \rangle*

Helper Definitions

*EMPTY\_OWNER*      $\triangleq$  0

Assumptions

ASSUME *Cardinality(CORES)* > 0     assume atleast 1 cache  
ASSUME *MAX\_WRITES* > *WRITE\_TO\_UPDATE*     ensure we always have enough writes to trigger an update  
ASSUME *EMPTY\_OWNER*  $\notin$  *CORES*     id used for *EMPTY\_OWNER* should not be used to identify a CORE  
ASSUME *ENABLE\_DIR\_ACKS*  $\in$  {TRUE, FALSE}

---

Useful Unchanged shortcuts

<i>unchanged_g</i>	$\triangleq$ UNCHANGED $\langle gBcstMsg, gBcstMsgRcvrs \rangle$
<i>unchanged_m</i>	$\triangleq$ UNCHANGED $\langle mData \rangle$
<i>unchanged_c</i>	$\triangleq$ UNCHANGED $\langle cState, cRcvAcks, cData \rangle$
<i>unchanged_d</i>	$\triangleq$ UNCHANGED $\langle dOwner, dSharers, dReqPending, dState, dRcvAcks \rangle$
<i>unchanged_dm</i>	$\triangleq$ <i>unchanged_d</i> $\wedge$ <i>unchanged_m</i>
<i>unchanged_cm</i>	$\triangleq$ <i>unchanged_c</i> $\wedge$ <i>unchanged_m</i>
<i>unchanged_cd</i>	$\triangleq$ <i>unchanged_c</i> $\wedge$ <i>unchanged_d</i>
<i>unchanged_mcd</i>	$\triangleq$ <i>unchanged_c</i> $\wedge$ <i>unchanged_d</i> $\wedge$ <i>unchanged_m</i>
<i>unchanged_gm</i>	$\triangleq$ <i>unchanged_g</i> $\wedge$ <i>unchanged_m</i>
<i>unchanged_gmc</i>	$\triangleq$ <i>unchanged_c</i> $\wedge$ <i>unchanged_gm</i>
<i>unchanged_gmd</i>	$\triangleq$ <i>unchanged_d</i> $\wedge$ <i>unchanged_gm</i>
<i>unchanged_Msgs</i>	$\triangleq$ UNCHANGED $\langle Msgs \rangle$
<i>unchanged_mMsgs</i>	$\triangleq$ <i>unchanged_Msgs</i> $\wedge$ <i>unchanged_m</i>
<i>unchanged_cMsgs</i>	$\triangleq$ <i>unchanged_Msgs</i> $\wedge$ <i>unchanged_c</i>
<i>unchanged_dMsgs</i>	$\triangleq$ <i>unchanged_Msgs</i> $\wedge$ <i>unchanged_d</i>
<i>unchanged_dmMsgs</i>	$\triangleq$ <i>unchanged_Msgs</i> $\wedge$ <i>unchanged_dm</i>
<i>unchanged_cmMsgs</i>	$\triangleq$ <i>unchanged_Msgs</i> $\wedge$ <i>unchanged_cm</i>
<i>unchanged_cdMsgs</i>	$\triangleq$ <i>unchanged_Msgs</i> $\wedge$ <i>unchanged_cd</i>
<i>unchanged_mcdMsgs</i>	$\triangleq$ <i>unchanged_Msgs</i> $\wedge$ <i>unchanged_mcd</i>
<i>unchanged_gMsgs</i>	$\triangleq$ <i>unchanged_g</i> $\wedge$ <i>unchanged_Msgs</i>
<i>unchanged_gmMsgs</i>	$\triangleq$ <i>unchanged_g</i> $\wedge$ <i>unchanged_mMsgs</i>
<i>unchanged_gcMsgs</i>	$\triangleq$ <i>unchanged_g</i> $\wedge$ <i>unchanged_cMsgs</i>
<i>unchanged_gdMsgs</i>	$\triangleq$ <i>unchanged_g</i> $\wedge$ <i>unchanged_dMsgs</i>
<i>unchanged_gdmMsgs</i>	$\triangleq$ <i>unchanged_g</i> $\wedge$ <i>unchanged_dmMsgs</i>
<i>unchanged_gcmMsgs</i>	$\triangleq$ <i>unchanged_g</i> $\wedge$ <i>unchanged_cmMsgs</i>
<i>unchanged_gcdMsgs</i>	$\triangleq$ <i>unchanged_g</i> $\wedge$ <i>unchanged_cdMsgs</i>
<i>unchanged_gmcdMsgs</i>	$\triangleq$ <i>unchanged_g</i> $\wedge$ <i>unchanged_mcdMsgs</i>

---

Type definitions

<i>Type_binary</i>	$\triangleq$ 0 .. 1
<i>Type_Data</i>	$\triangleq$ 0 .. MAX_WRITES
<i>Type_State</i>	$\triangleq$ { "M", "O", "E", "S", "I" } all nodes start from <i>I</i>

Msgs send by requester

<i>Type_rMsg</i>	$\triangleq$
	[ <i>type</i> : { "GetS", "GetM" }, <i>sender</i> : CORES]

<i>Type_uMsg</i>	$\triangleq$
	[ <i>type</i> : { "Upd" },
	<i>data</i> : Type_Data,
	<i>sender</i> : CORES,
	<i>receiver</i> : CORES]

Msgs send by directory

$$\begin{aligned}
Type\_iMsg &\triangleq \\
&[type : \{ "DInv" \}, \quad sender : CORES, \quad \text{initial sender (i.e., requester)} \\
&\quad receiver : CORES]
\end{aligned}$$

$$\begin{aligned}
Type\_dMsg &\triangleq Type\_iMsg \cup \\
&[type : \{ "Fwd-GetM", "Fwd-GetS" \}, \quad sender : CORES, \quad \text{initial sender (i.e., requester)} \\
&\quad receiver : CORES]
\end{aligned}$$

$$\begin{aligned}
&\text{Msgs send by sharer} \\
Type\_sMsg &\triangleq \\
&[type : \{ "SAck, UAck" \}, \quad sender : CORES, \\
&\quad receiver : CORES] \\
&\cup \\
&[type : \{ "SData", \\
&\quad "SAckData" \}, \quad data : Type\_Data, \\
&\quad sender : CORES, \\
&\quad receiver : CORES]
\end{aligned}$$

$$\begin{aligned}
Type\_bcastMsg &\triangleq Type\_uMsg \cup Type\_iMsg \\
Type\_msg &\triangleq Type\_sMsg \\
&\cup Type\_rMsg \\
&\cup Type\_uMsg \\
&\cup Type\_iMsg \\
&\cup Type\_dMsg \\
&\cup Type\_sMsg
\end{aligned}$$

---

Type check and initialization

$$\begin{aligned}
ATypeOK &\triangleq \quad \text{The type correctness invariant} \\
&\quad \text{GLOBAL variables} \\
&\quad \wedge Msgs \subseteq Type\_msg \\
&\quad \wedge gBcstMsg \in Type\_bcastMsg \\
&\quad \wedge gBcstMsgRcvrs \subseteq CORES \\
&\quad \text{Directory/memory variables} \\
&\quad \wedge dOwner \in CORES \\
&\quad \wedge dSharers \subseteq CORES \\
&\quad \wedge dRcvAcks \subseteq CORES \\
&\quad \wedge dReqPending \in Type\_binary \\
&\quad \wedge dState \in Type\_State \\
&\quad \wedge cState \in Type\_State \\
&\quad \wedge mData \in Type\_Data \\
&\quad \text{Core/cache variables} \\
&\quad \wedge \forall n \in CORES : cData[n] \in Type\_Data \\
&\quad \wedge \forall n \in CORES : cState[n] \in Type\_State
\end{aligned}$$

$$\wedge \forall n \in CORES : cRcvAcks[n] \subseteq (CORES \setminus \{n\})$$

$$\begin{aligned}
AInit &\triangleq \text{The initial predicate} \\
&\quad \text{GLOBAL variables} \\
&\quad \wedge Msgs = \{\} \\
&\quad \wedge gBcstMsg = \{\} \\
&\quad \wedge gBcstMsgRcvrs = \{\} \\
&\quad \text{Directory/memory variables} \\
&\quad \wedge mData = 0 \\
&\quad \wedge dState = \text{"I"} \\
&\quad \wedge dOwner = EMPTY\_OWNER \\
&\quad \wedge dSharers = \{\} \\
&\quad \wedge dRcvAcks = \{\} \\
&\quad \wedge dReqPending = 0 \\
&\quad \text{Core/cache variables} \\
&\quad \wedge cData = [n \in CORES \mapsto 0] \\
&\quad \wedge cRcvAcks = [n \in CORES \mapsto \{\}] \\
&\quad \wedge cState = [n \in CORES \mapsto \text{"I"}]
\end{aligned}$$

---


$$\begin{aligned}
&\text{TODO may add sanity check that } m \text{ already exists in the set before delivering it} \\
deliver\_Msg(m) &\triangleq Msgs' = Msgs \setminus \{m\}
\end{aligned}$$

$$\begin{aligned}
&\text{TODO may add all messages to one set from which we do not remove msgs for debugging (w/ a global counter)} \\
\_send\_Msg(m) &\triangleq Msgs' = Msgs \cup \{m\}
\end{aligned}$$

$$\begin{aligned}
\_send\_Msg\_with\_data(\_type, \_sender, \_receiver, \_data) &\triangleq \\
\_send\_Msg([type &\mapsto \_type, \\
&\quad data \mapsto \_data, \\
&\quad sender \mapsto \_sender, \\
&\quad receiver \mapsto \_receiver])
\end{aligned}$$

$$\begin{aligned}
\_send\_Msg\_simple(\_type, \_requester, \_receiver) &\triangleq \\
\_send\_Msg([type &\mapsto \_type, \\
&\quad sender \mapsto \_requester, \\
&\quad receiver \mapsto \_receiver])
\end{aligned}$$

$$\_resp\_Msg(m, new\_m) \triangleq Msgs' = (Msgs \setminus \{m\}) \cup \{new\_m\}$$

$$\begin{aligned}
\_resp\_Msg\_simple(m, \_type) &\triangleq \\
\_resp\_Msg(m, [type &\mapsto \_type, \\
&\quad sender \mapsto m.receiver, \\
&\quad receiver \mapsto m.sender])
\end{aligned}$$

$$\begin{aligned}
\_resp\_Msg\_with\_data(m, \_type) &\triangleq \\
\_resp\_Msg(m, [type &\mapsto \_type, \\
&\quad data \mapsto cData[m.receiver], \\
&\quad sender \mapsto m.receiver,
\end{aligned}$$

$$\begin{aligned}
& receiver \mapsto m.sender]) \\
resp\_UAck(m) & \triangleq \_resp\_Msg\_simple(m, "UAck") \\
resp\_SAck(m) & \triangleq \_resp\_Msg\_simple(m, "SAck") \\
resp\_SData(m) & \triangleq \_resp\_Msg\_with\_data(m, "SData") \\
resp\_SDataAck(m) & \triangleq \_resp\_Msg\_with\_data(m, "SDataAck") \\
\\
ucst\_FwdGetM(\_requester, \_receiver) & \triangleq \\
& \_send\_Msg\_simple("Fwd-GetM", \_requester, \_receiver) \\
ucst\_FwdGetS(\_requester, \_receiver) & \triangleq \\
& \_send\_Msg\_simple("Fwd-GetS", \_requester, \_receiver) \\
\\
\_bcst\_msg(\_requester, \_receivers, \_msg) & \triangleq \\
& LET rcver \triangleq CHOOSE x \in \_receivers : TRUEIN \\
& \quad \wedge gBcstMsgRcvrs' = \_receivers \setminus \{rcver\} \\
& \quad \wedge gBcstMsg' = \{[\_msg \text{ EXCEPT } !.receiver = rcver]\} \\
bcst\_DInv(\_requester, \_receivers) & \triangleq \\
& \_bcst\_msg(\_requester, \_receivers, \\
& \quad [type \mapsto "DInv", \\
& \quad sender \mapsto \_requester, \\
& \quad receiver \mapsto 0]) \\
bcst\_Upd(\_requester, \_receivers, \_data) & \triangleq \\
& \_bcst\_msg(\_requester, \_receivers, \\
& \quad [type \mapsto "Upd", \\
& \quad data \mapsto \_data, \\
& \quad sender \mapsto \_requester, \\
& \quad receiver \mapsto 0]) \\
\\
rcv\_unicast(m, receiver, type) & \triangleq \\
& \wedge m.type = type \\
& \wedge m.receiver = receiver \\
\\
rcv\_UAck(m, receiver) & \triangleq rcv\_unicast(m, receiver, "UAck") \\
rcv\_SAck(m, receiver) & \triangleq rcv\_unicast(m, receiver, "SAck") \\
rcv\_SData(m, receiver) & \triangleq rcv\_unicast(m, receiver, "SData") \\
rcv\_SDataAck(m, receiver) & \triangleq rcv\_unicast(m, receiver, "SDataAck") \\
\\
rcv\_Upd(m, receiver) & \triangleq rcv\_unicast(m, receiver, "Upd") \\
rcv\_DInv(m, receiver) & \triangleq rcv\_unicast(m, receiver, "DInv") \\
\\
rcv\_FwdGetM(m, receiver) & \triangleq rcv\_unicast(m, receiver, "Fwd-GetM") \\
rcv\_FwdGetS(m, receiver) & \triangleq rcv\_unicast(m, receiver, "Fwd-GetS")
\end{aligned}$$


---

Helper functions

$$\begin{aligned}
is\_M(n) &\triangleq cState[n] = \text{"M"} \\
is\_O(n) &\triangleq cState[n] = \text{"O"} \\
is\_E(n) &\triangleq cState[n] = \text{"E"} \\
is\_S(n) &\triangleq cState[n] = \text{"S"} \\
is\_I(n) &\triangleq cState[n] = \text{"I"}
\end{aligned}$$

$$\begin{aligned}
dir\_rcvd\_acks\_from\_set(set) &\triangleq set \subseteq dRcvAcks \\
rcvd\_acks\_from\_set(n, set) &\triangleq set \subseteq cRcvAcks[n] \\
rcvd\_all\_sharer\_acks(n) &\triangleq (dSharers \setminus \{n\}) \subseteq cRcvAcks[n] \\
has\_valid\_data(n) &\triangleq \neg is\_I(n) \\
set\_next\_data\_value(n) &\triangleq cData' = [cData \text{ EXCEPT } ![n] = cData[n] + 1] \\
has\_not\_reached\_final\_value &\triangleq \forall n \in CORES : cData[n] < MAX\_WRITES + 1
\end{aligned}$$

todo check the correctness of the following

$$\begin{aligned}
is\_sharer(n) &\triangleq \neg is\_I(n) \\
is\_exclusive(n) &\triangleq is\_M(n) \vee is\_E(n) \\
is\_owner(n) &\triangleq is\_O(n) \vee is\_M(n)
\end{aligned}$$

$$\begin{aligned}
upd\_core\_data(n, \_data) &\triangleq cData' = [cData \text{ EXCEPT } ![n] = \_data] \\
rd\_mem\_data(n) &\triangleq upd\_core\_data(n, mData) \\
upd\_mem\_data(n) &\triangleq mData' = cData[n]
\end{aligned}$$

$$\begin{aligned}
Min(S) &\triangleq \text{CHOOSE } x \in S : \\
&\quad \forall y \in S \setminus \{x\} : \\
&\quad y > x
\end{aligned}$$

---

Protocol Invariants:

memory data consistency invariant

$$\begin{aligned}
MEM\_DATA\_CONSISTENT &\triangleq \\
&\quad \vee \exists n \in CORES : is\_owner(n) \\
&\quad \vee \forall n \in CORES : cData[n] \leq mData
\end{aligned}$$

All valid core/cache data are consistent

$$\begin{aligned}
CORE\_DATA\_CONSISTENT &\triangleq \\
&\quad \forall o, k \in CORES : \vee \neg is\_I(o) \\
&\quad \quad \vee \wedge cData[o] \geq mData \\
&\quad \quad \wedge cData[o] \geq cData[k]
\end{aligned}$$

There is always at most one owner

$$\begin{aligned}
AT\_MOST\_ONE\_OWNER &\triangleq \\
&\quad \forall n, m \in CORES : \vee m = n \\
&\quad \quad \vee \neg is\_owner(n) \\
&\quad \quad \vee \neg is\_owner(m)
\end{aligned}$$

$$\begin{aligned}
IF\_EXCLUSIVE\_REST\_INV &\triangleq \\
&\vee \neg \exists n \in CORES : is\_exclusive(n) \\
&\vee \forall n \in CORES : \vee is\_I(n) \\
&\quad \vee is\_exclusive(n)
\end{aligned}$$

$$\begin{aligned}
CONSISTENT\_OWNER &\triangleq \\
&\forall n \in CORES : \vee dOwner = EMPTY\_OWNER \\
&\quad \vee dReqPending = 1 \\
&\quad \vee cState[dOwner] = dState
\end{aligned}$$

Directory correctly indicates owner and sharers

$$\begin{aligned}
CONSISTENT\_DIRECTORY\_OWNER &\triangleq \\
&\forall n \in CORES : \vee dOwner = n \\
&\quad \vee \neg is\_owner(n)
\end{aligned}$$

$$\begin{aligned}
CONSISTENT\_DIRECTORY\_SHARERS &\triangleq \\
&\forall k \in CORES : \vee is\_I(k) \\
&\quad \vee k \in dSharers
\end{aligned}$$

The owner and readers are always correctly reflected by any valid sharing vectors

$$INVARIANTS \triangleq$$

$$\begin{aligned}
&\wedge MEM\_DATA\_CONSISTENT \\
&\wedge CORE\_DATA\_CONSISTENT \\
&\wedge AT\_MOST\_ONE\_OWNER \\
&\wedge IF\_EXCLUSIVE\_REST\_INV \\
&\wedge CONSISTENT\_OWNER \\
&\wedge CONSISTENT\_DIRECTORY\_OWNER \\
&\wedge CONSISTENT\_DIRECTORY\_SHARERS
\end{aligned}$$