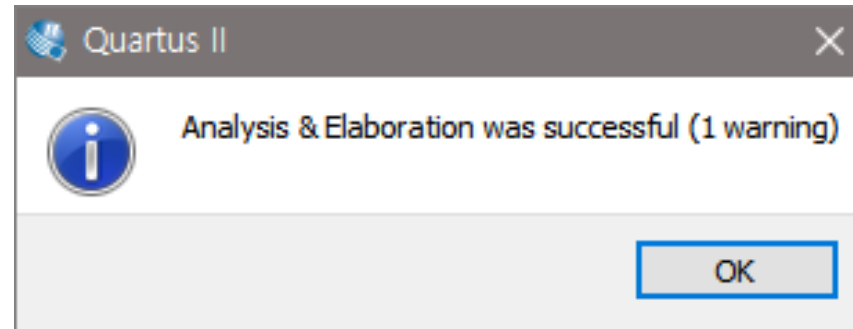


# 논리회로 및 실험

3주차

# 지난주 마지막 내용

**Processing > Start > Start Analysis & Elaboration**



# Assignment > Device



Device

Select the family and device you want to target for compilation.  
You can install additional device support with the Install Devices command on the Tools menu.

Device family

Family: Cyclone IV E

Devices: All

Target device

☐ Auto device selected by the Fitter

☒ Specific device selected in 'Available devices' list

☐ Other: n/a

Show in 'Available devices' list

Package: Any

Pin count: Any

Speed grade: Any

Name filter:

☒ Show advanced devices ☐ HardCopy compatible only

Device and Pin Options...

Available devices:

Name	Core Voltage	LEs	User I/Os	Memory Bits	Embedded multiplier 9-bit elements
EP4CE22F17C8L	1.0V	22320	154	608256	132
EP4CE22F17C9L	1.0V	22320	154	608256	132
EP4CE22F17I7	1.2V	22320	154	608256	132
EP4CE22F17I8L	1.0V	22320	154	608256	132
EP4CE22U14I7	1.2V	22320	154	608256	132
EP4CE30F19A7	1.2V	28848	193	608256	132
EP4CE30F23A7	1.2V	28848	329	608256	132
EP4CE30F23C6	1.2V	28848	329	608256	132
EP4CE30F23C7	1.2V	28848	329	608256	132
EP4CE30F23C8	1.2V	28848	329	608256	132

Migration compatibility

Migration Devices...

0 migration devices selected

Companion device

HardCopy:

☒ Limit DSP & RAM to HardCopy device resources

Buy Software OK Cancel Help

Click

Device and Pin Options - COUNT\_8BIT

Category:

- General
- Configuration
- Programming Files
- Unused Pins
- Dual-Purpose Pins
- Capacitive Loading
- Board Trace Model
- I/O Timing
- Voltage
- Pin Placement
- Error Detection CRC
- CVP Settings
- Partial Reconfiguration

Unused Pins

Specify device-wide options for reserving all unused pins on the device. To reserve individual dual-purpose configuration pins, go to the Dual-Purpose Pins tab. To reserve other pins individually, use the Assignment Editor.

Reserve all unused pins: As input tri-stated with weak pull-up

As input tri-stated  
As input tri-stated with bus-hold circuitry  
As input tri-stated with weak pull-up  
As output driving an unspecified signal  
As output driving ground

Description:

Reserves all unused pins on the target device in one of 5 states: as inputs that are tri-stated, as outputs that drive ground, as outputs that drive an unspecified signal, as input tri-stated with bus-hold, or as input tri-stated with weak pull-up.

Reset

OK Cancel Help

Select

Click

# Assignment > Pin Planner

Pin Planner - C:/Users/YeongHyeon/Desktop/CH01/COUNT\_8BIT - COUNT\_8BIT

File Edit View Processing Tools Window Help Search altera.com

Report not available

Groups Report

Tasks

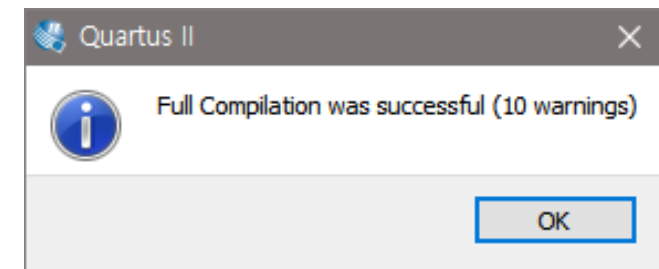
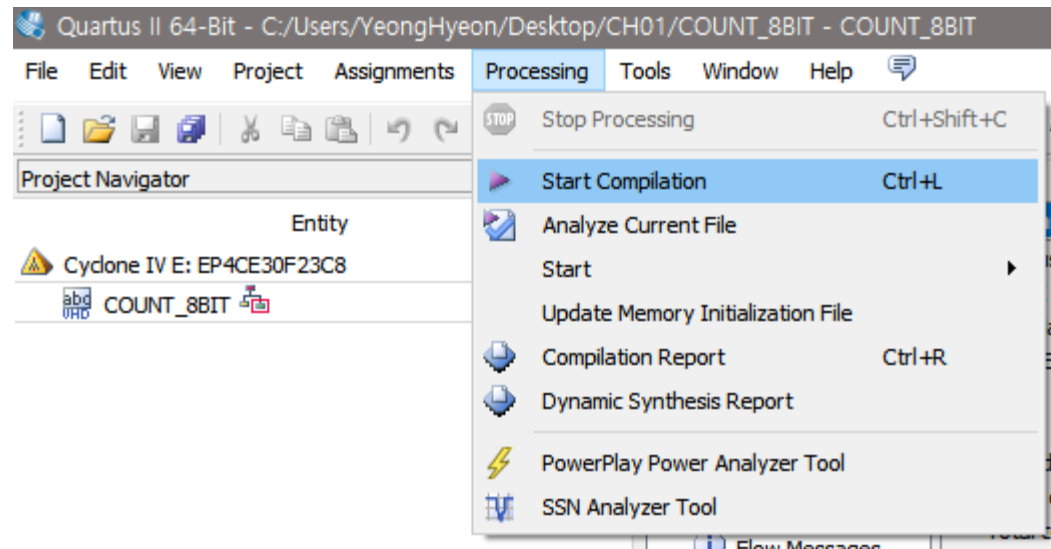
- Run Anal
- Early Pin
  - Early
- Run
- Expo
- Change
  - Show
  - Show
  - Show
- Show

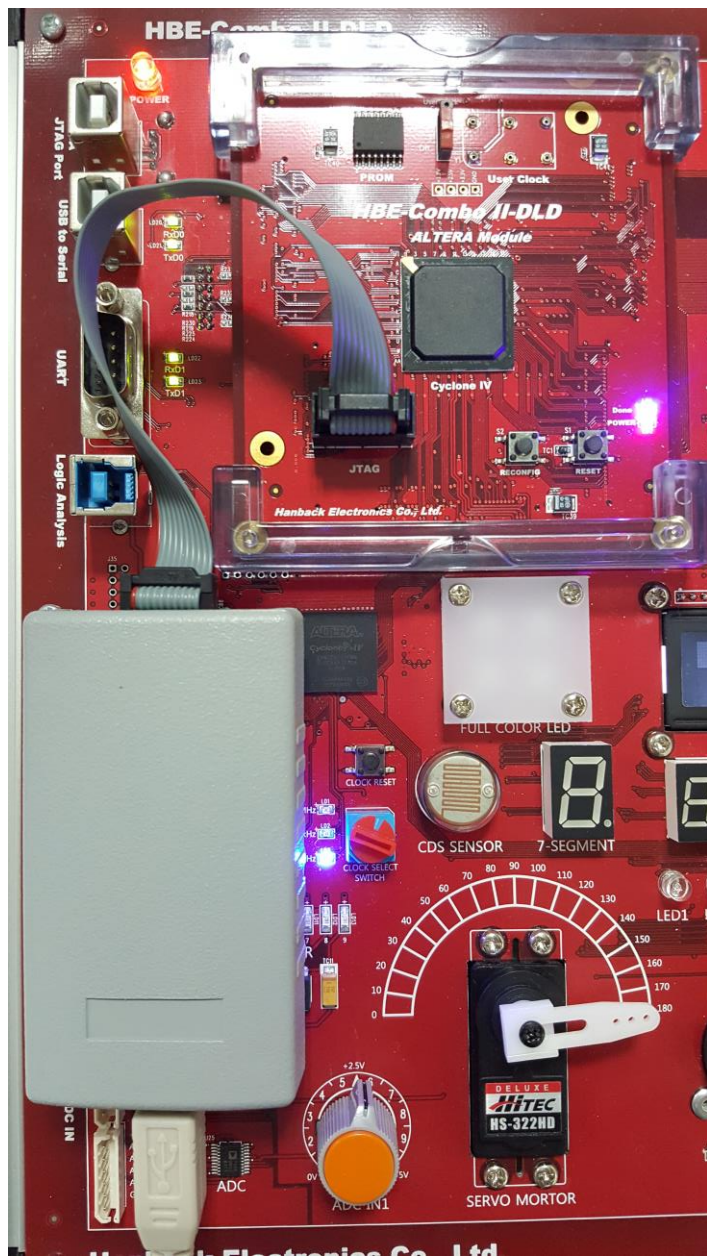
Top View - Wire Bond  
Cyclone IV E - EP4CE30F23C8

Select

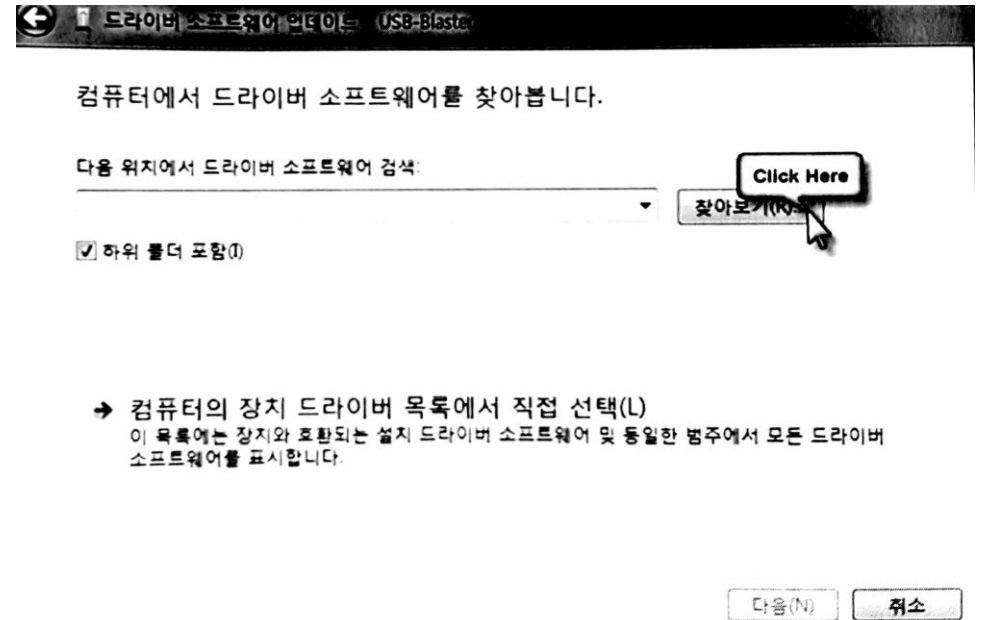
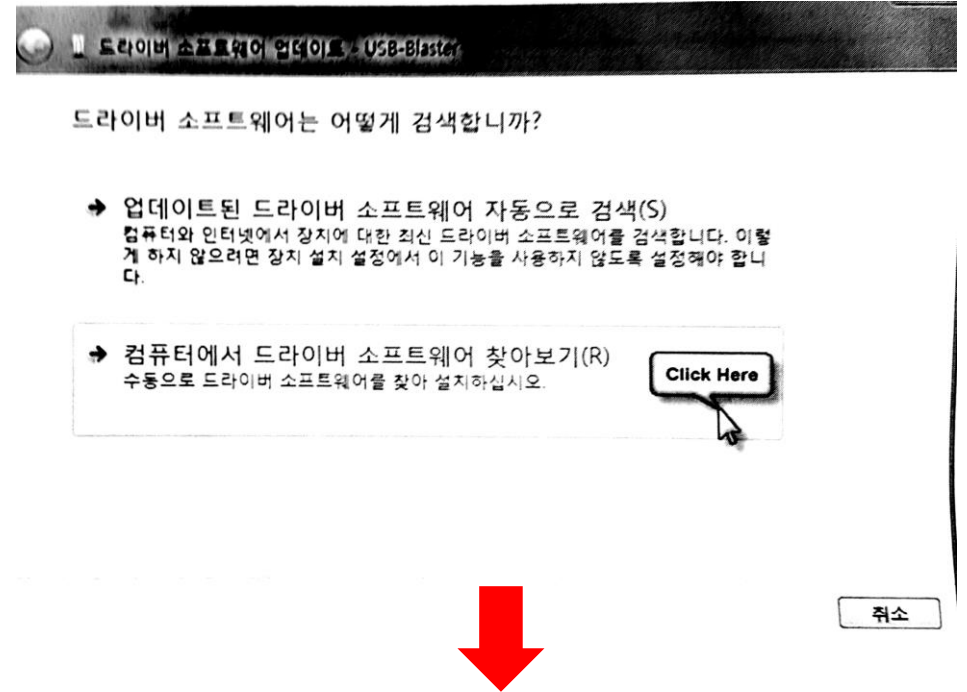
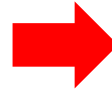
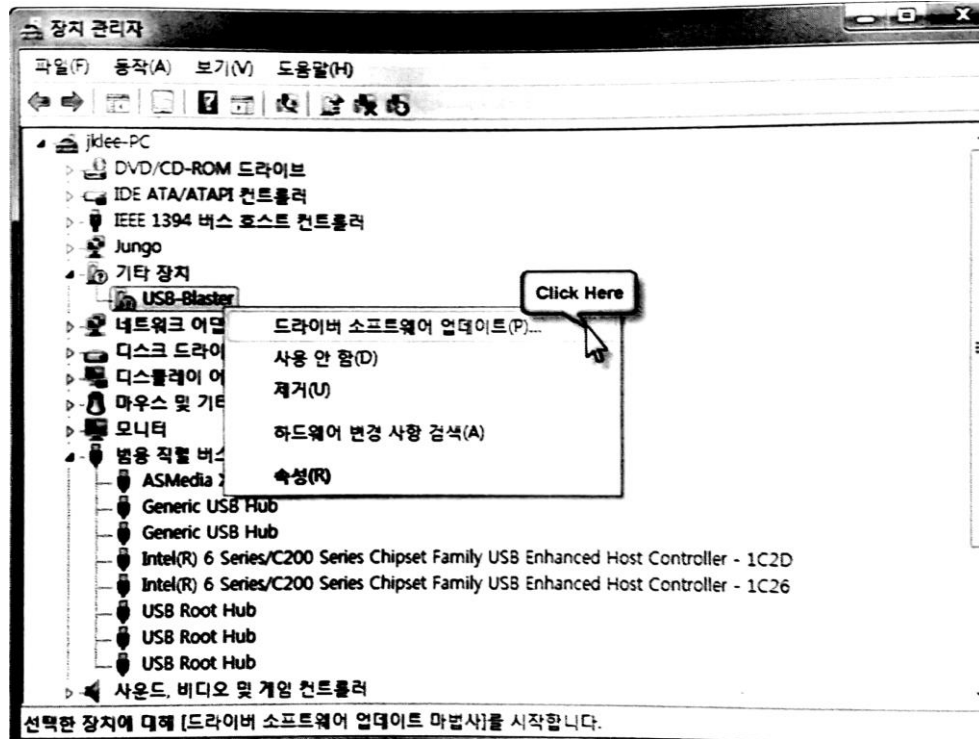
Node Name	Direction	Location	I/O Bank	VREF Group	I/O Standard	Reserved	Current Strength	Slew Rate	Differential Pair
in CLK	Input	PIN_M6	2	B2_N0	2.5 V (default)		8mA (default)		
out COUNT_OUT[7]	Output	PIN_K7	1	B1_N2	2.5 V (default)		8mA (default)	2 (default)	
out COUNT_OUT[6]	Output	PIN_M8	2	B2_N1	2.5 V (default)		8mA (default)	2 (default)	
out COUNT_OUT[5]	Output	PIN_N8	2	B2_N2	2.5 V (default)		8mA (default)	2 (default)	
out COUNT_OUT[4]	Output	PIN_N6	2	B2_N1	2.5 V (default)		8mA (default)	2 (default)	
out COUNT_OUT[3]	Output	PIN_L8	1	B1_N2	2.5 V (default)		8mA (default)	2 (default)	
out COUNT_OUT[2]	Output	PIN_M7	2	B2_N1	2.5 V (default)		8mA (default)	2 (default)	
out COUNT_OUT[1]	Output	PIN_N7	2	B2_N2	2.5 V (default)		8mA (default)	2 (default)	
out COUNT_OUT[0]	Output	PIN_P7	2	B2_N2	2.5 V (default)		8mA (default)	2 (default)	
in RESETN	Input	PIN_J3	1	B1_N3	2.5 V (default)		8mA (default)		

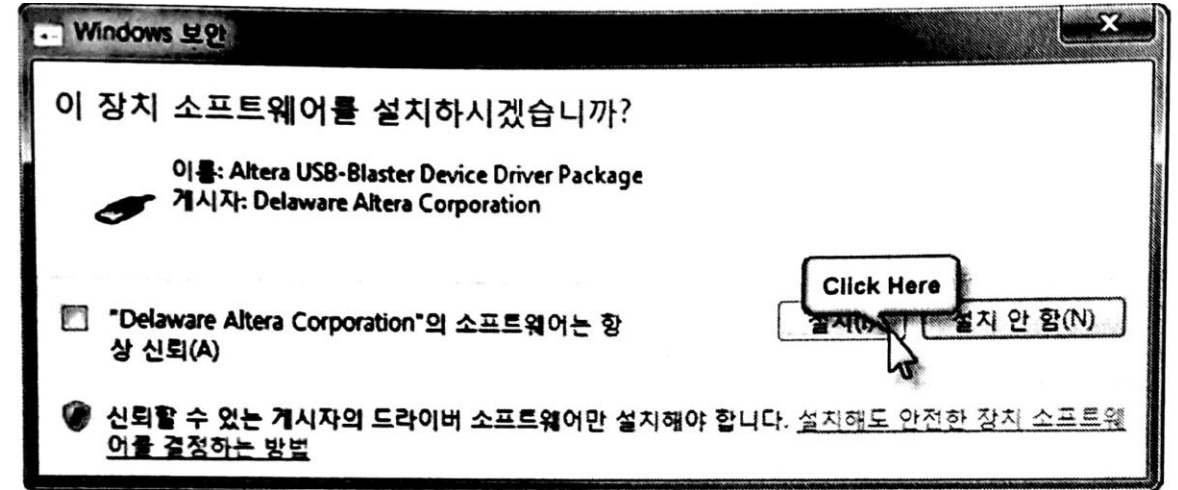
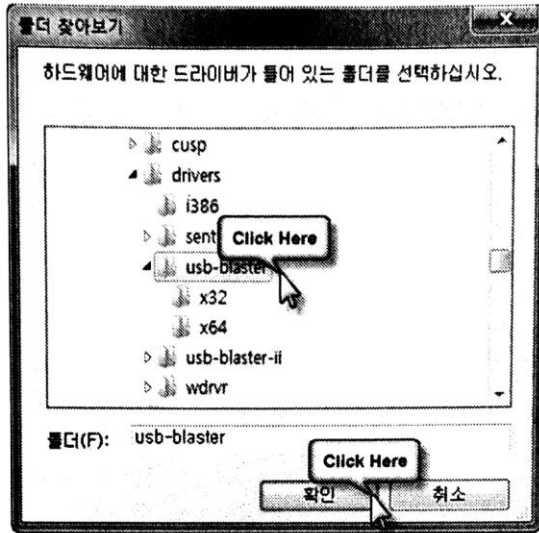
0% 00:00:00











컴퓨터에서 드라이버 소프트웨어를 찾아봅니다.

다음 위치에서 드라이버 소프트웨어 검색:

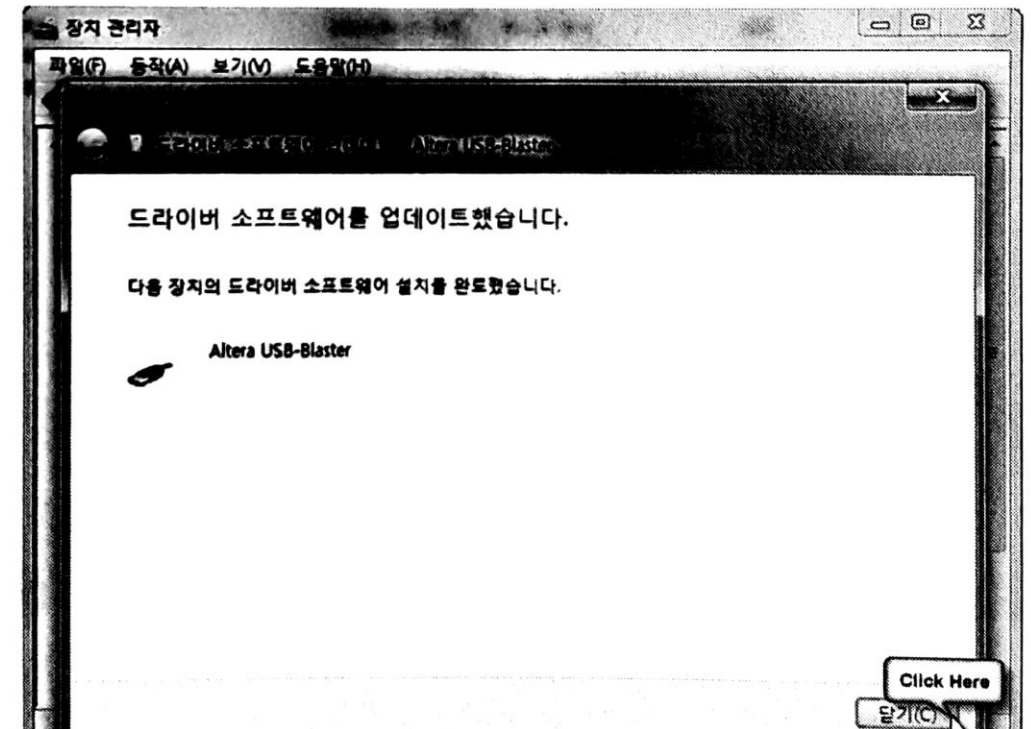
C:\Waltera\W13.0sp1\Wquartus\drivers\Wusb-blaster

☒ 하위 폴더 포함(I)

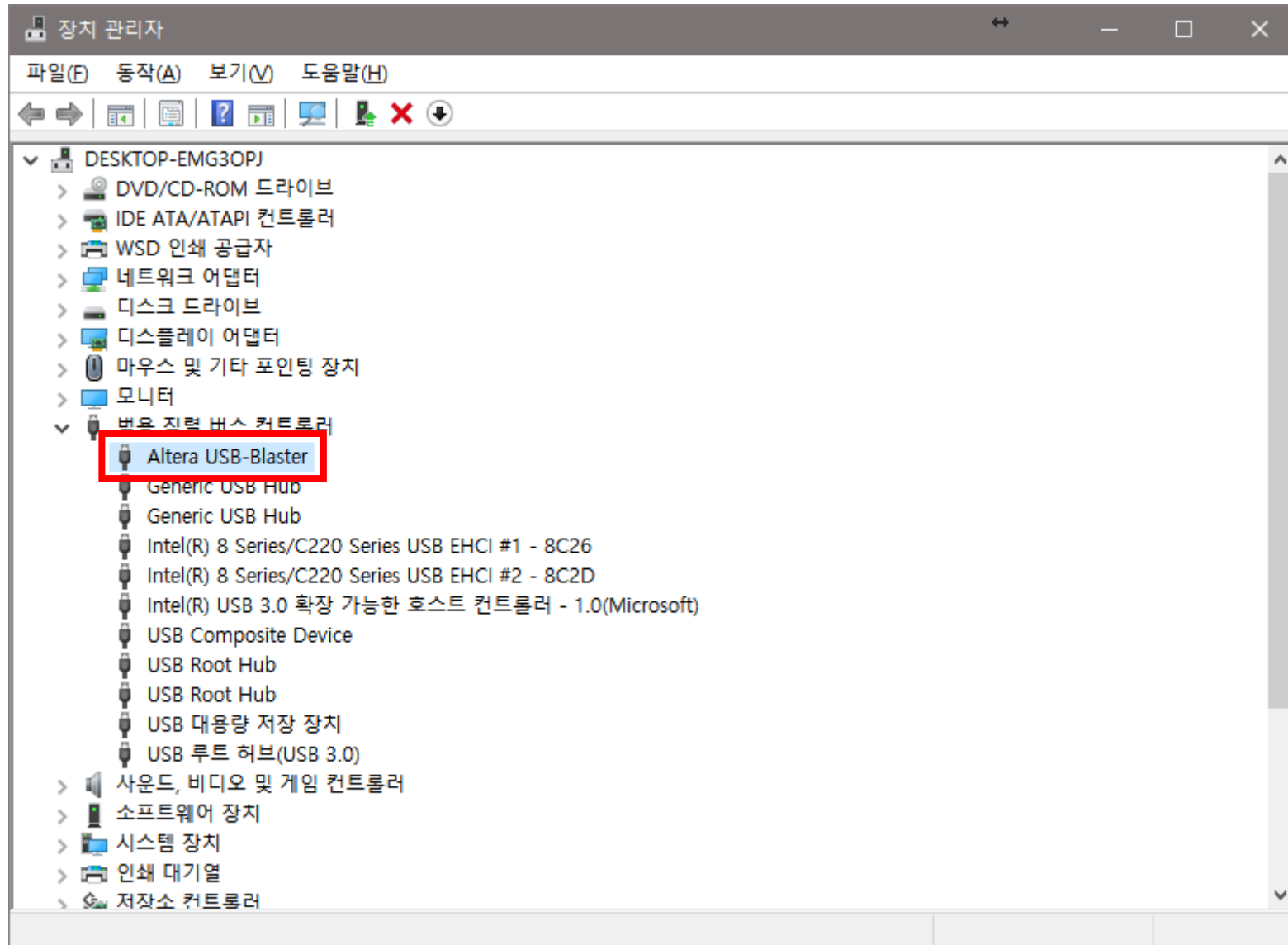
➔ 컴퓨터의 장치 드라이버 목록에서 직접 선택(L)  
이 목록에는 장치와 호환되는 설치 드라이버 소프트웨어 및 동일한 범주에서 모든 드라이버 소프트웨어를 표시합니다.



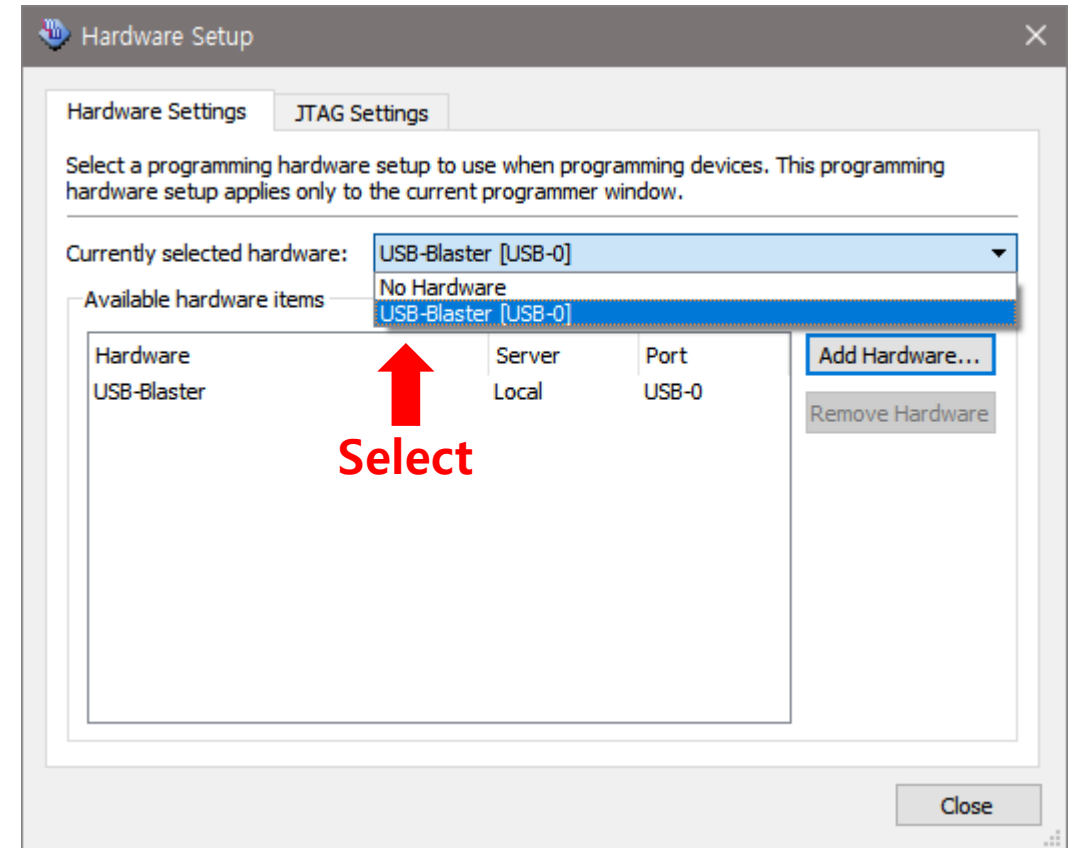
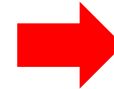
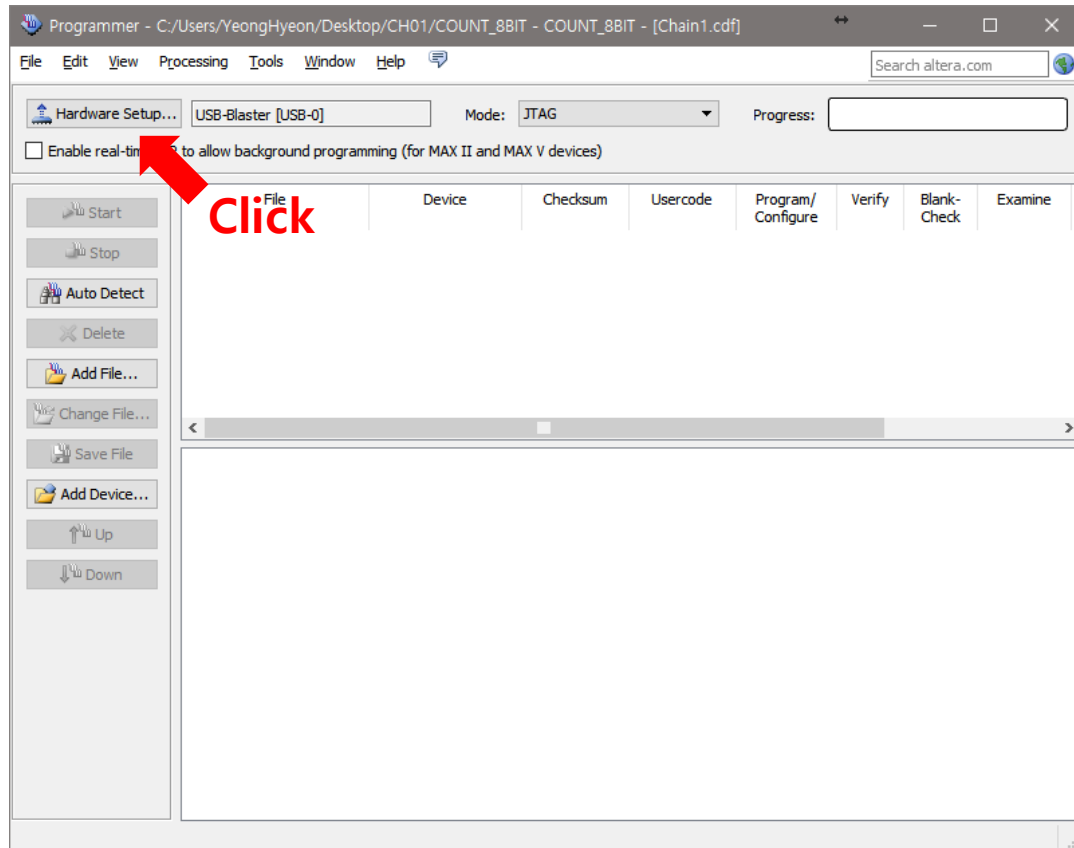
C:\Waltera\W13.0sp1\Wquartus\drivers\Wusb-blaster

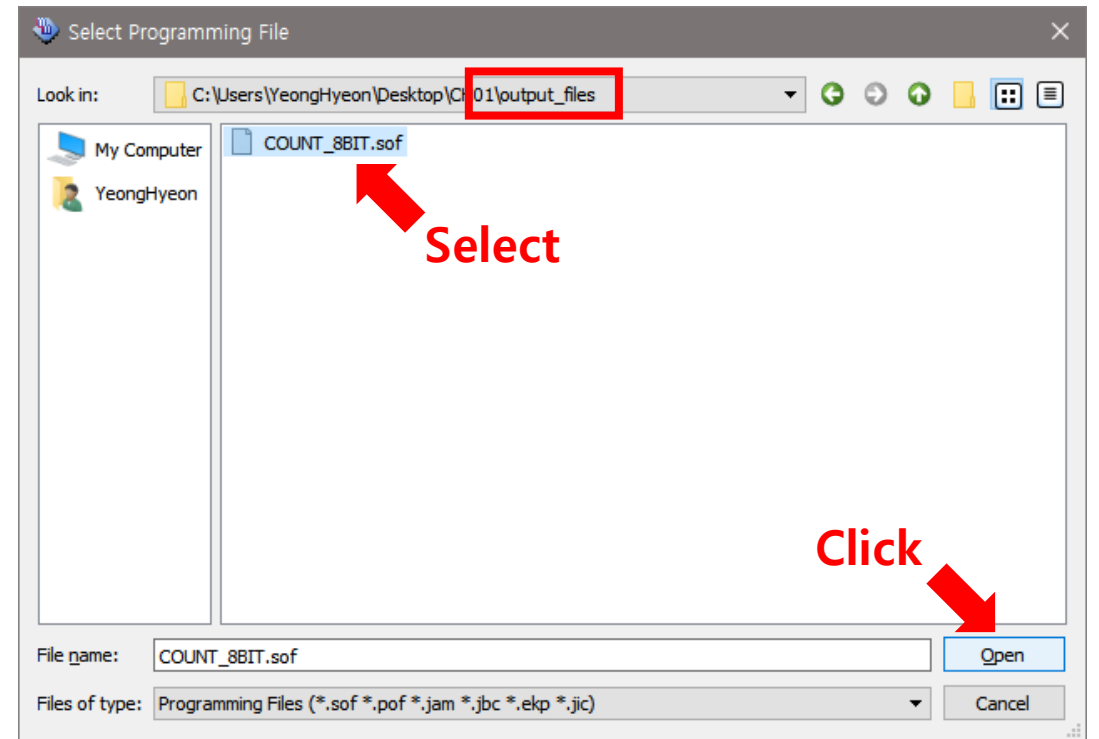
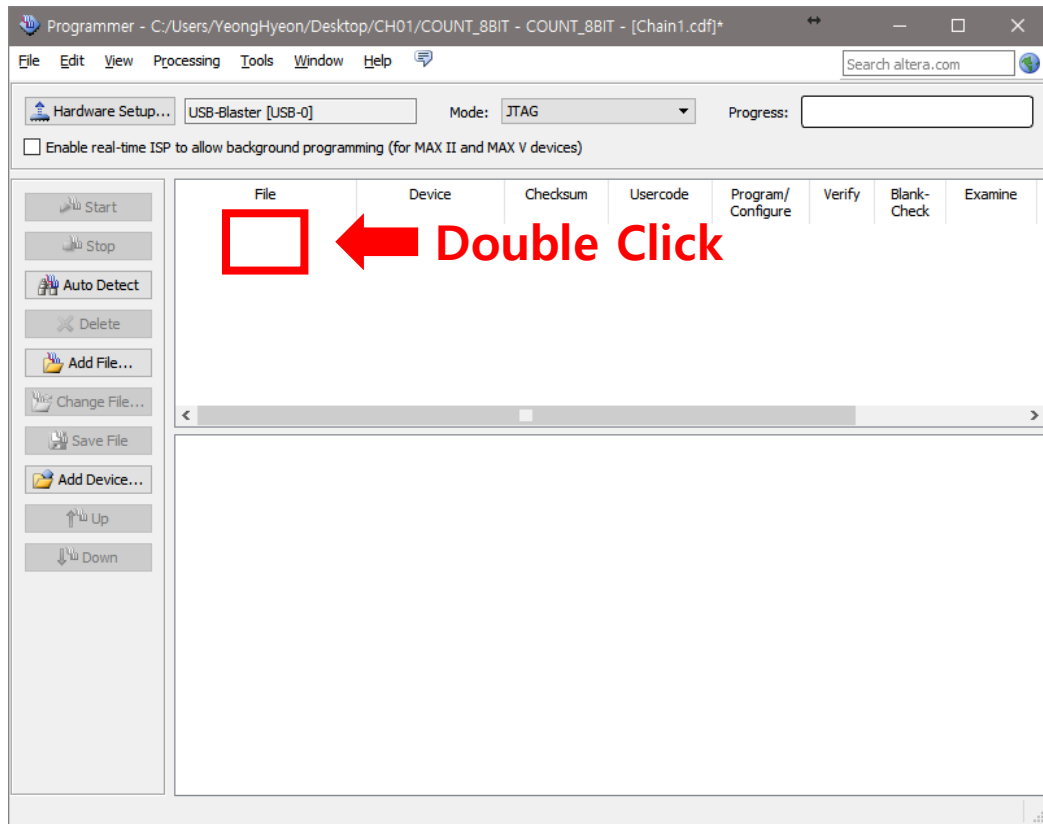


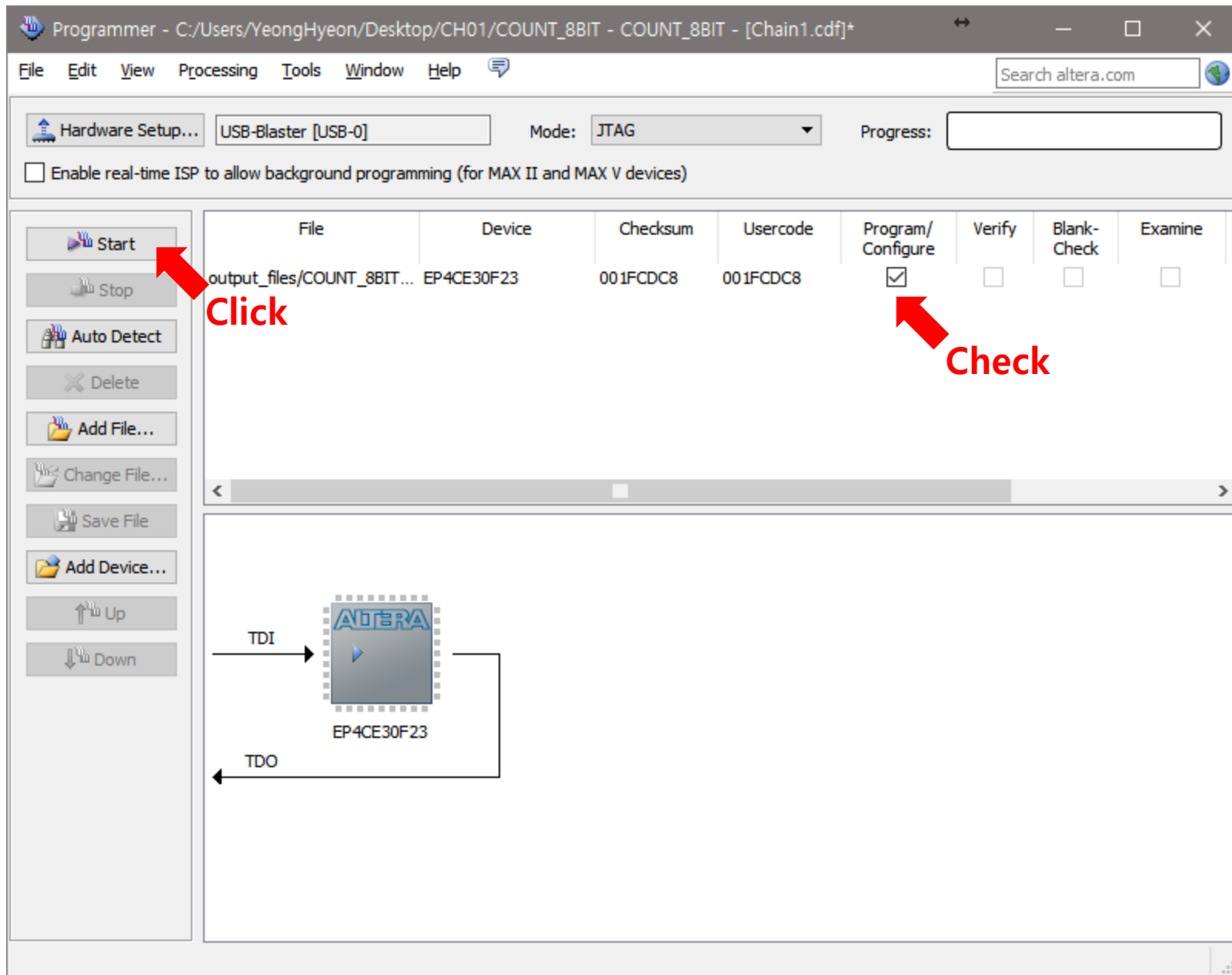




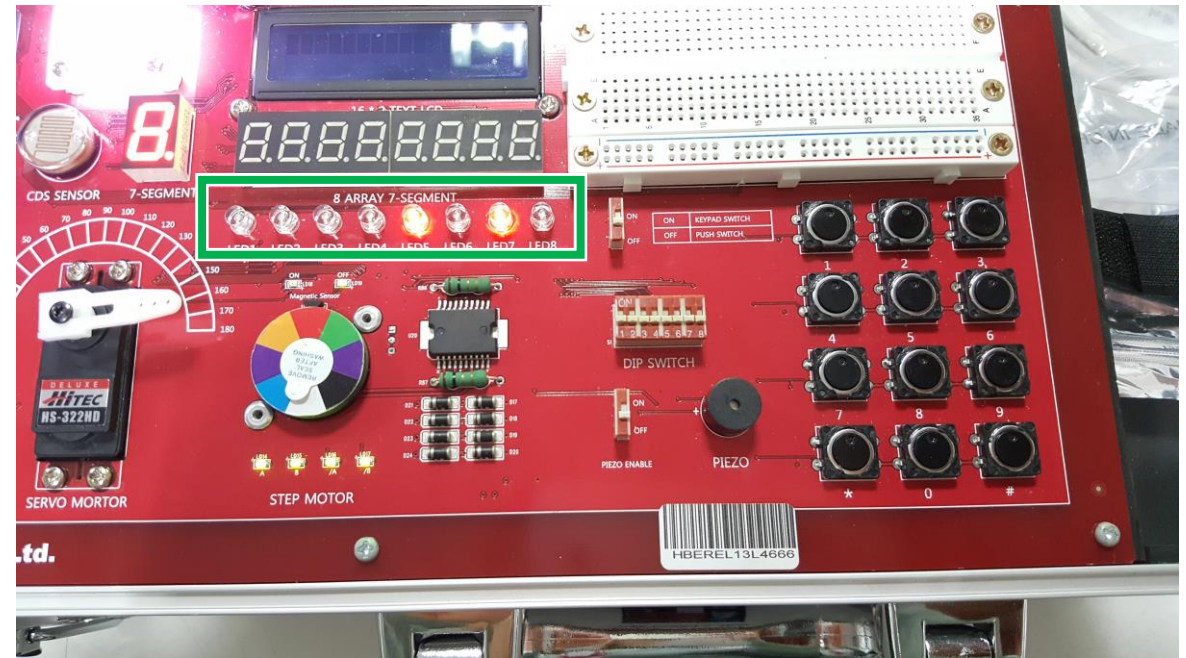
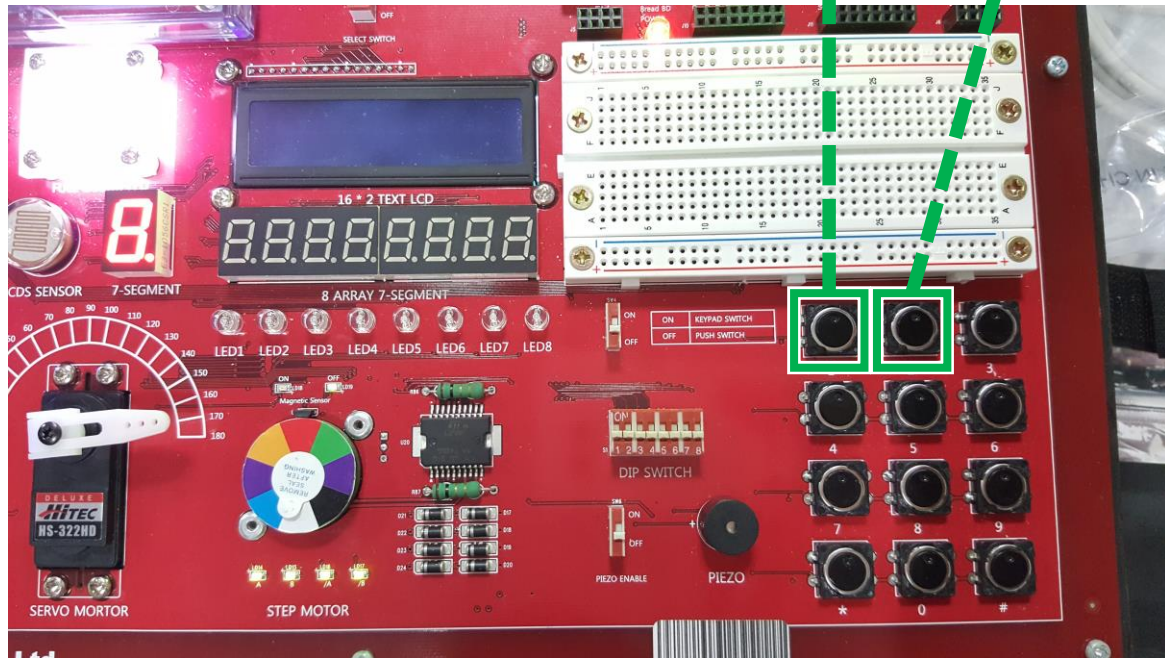
## Tools > Programmer







RESETN CLK





# Report

## 결과

- Kit 사용과정을 상세히 설명.  
(사진첨부 필수)

## 예비

- AND, OR, XOR Gate 조사
- Verilog HDL 문법 조사