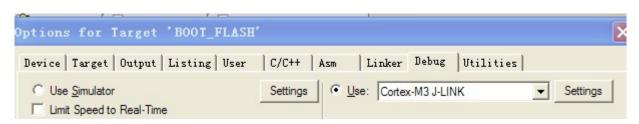
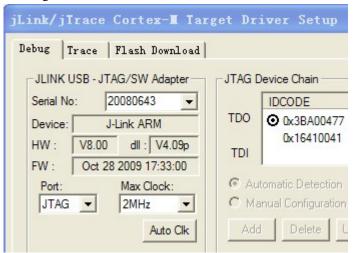


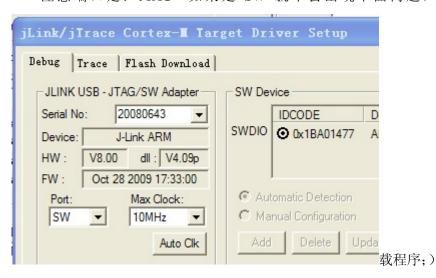
大家在装完 MDK4.02 完毕后,想用 J-LINK(SW 模式)调试 Cortex 核的话,点击调试按钮(



点击 Settings;



注意端口是: JTAG—如果是 SW 就不会出现下面问题,并且 SW 模式可以正常调试下





会发现选项中没有 J-LINK 的选项:

"J-LINK / J-TRACE"

查了一下,大家对此多有异议,不少人认为 KEIL 开始不支持 J-LINK 了。但我觉得,更像是写 KEIL 配置文件的人手滑了。

我们打开 MDK 安装目录下的 TOOLS.INI 文件, 仔细看下面这截图:

```
<u>00000 014/UFFFU3F 101F3:U1U/ 01770U UCTCO3F 107F3 1</u>
[ARM]
PATH="C:\Keil\ARM"
VERSION=4.02
PATH1="C:\Program Files\CodeSourcery\Sourcery G++ Lite\"
BOOK@=HLP\RELEASE NOTES.HTM("Release Notes")
BOOK1=HLP\ARMTOOLS.chm("Complete User's Guide Selection", C)
BOOK2=HLP\RL RELEASE NOTES.HTM("RTL-ARM Release Notes")
BOOK3=HLP\RVI.chm("RV Compiler Introduction")
BOOK4="C:\Program Files\CodeSourcery\Sourcery G++ Lite\share\doc\arm-2007q3-53-arm-none-e
BOOK5="C:\Program Files\CodeSourcery\Sourcery G++ Lite\share\doc\arm-2007q3-53-arm-none-e
BOOK6="C:\Program Files\CodeSourcery\Sourcery G++ Lite\share\doc\arm-2007q3-53-arm-none-e
BOOK7="C:\Program Files\CodeSourcery\Sourcery G++ Lite\share\doc\arm-2007q3-53-arm-none-e
TDRV0=BIN\UL2ARM.DLL("ULINK ARM Debugger")
TDRV1=BIN\UL2CM3.DLL("ULINK Cortex Debugger")
TDRV2=BIN\AGDIRDI.DLL("RDI Interface Driver")
TDRV3=BIN\ABLSTCM.dll("Altera Blaster Cortex Debugger")
TDRV4=BIN\lmidk-aqdi.dll("Luminary Eval Board")
TDRV5=Signum\SigUV3Arm.dll("Signum Systems JTAGjet")
TDRV6=Segger\JLTAgdi.dll("J-LINK / J-TRACE")
TDRU7=Segger\JL2CM3.d11("Cortex-M3 J-LINK")
TDRV8=STLink\ST-LINKIII-KEIL.dll ("ST-Link Debugger")
LICO=FYKPC-7T6IY-DKUBN-YRJ75-HWEZP-EY2Z1
TOOLPREFIX=arm-none-eabi-
CPUDLL 0=SARM.DLL(TDRV0,TDRV5,TDRV6,TDRV10)
                                                                        # Drivers for ARM7
CPUDLL1=SARMCM3.DLL(TDRU1.TDRU2.TDRU3.TDRU4.TDRU5.TDRU7.TDRU8.TDRU9)
                                                                        # Drivers for Cort
```

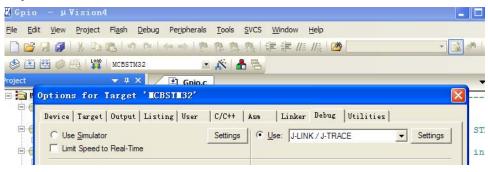
```
INUAIA-DIUJOELSUUU:NEEL OEIUN LLO UUU NEDAÄÄEL Y
[ARMADS]
PATH="C:\Keil\ARM"
PATH1="BIN40"
BOOK@=HLP\RELEASE NOTES.HTM("Release Notes")
BOOK1=HLP\ARMTOOLS.chm("Complete User's Guide Selection", C)
BOOK2=HLP\RL_RELEASE_NOTES.HTM("RTL-ARM Release Notes")
BOOK3=HLP\RVI.chm("RV Compiler Introduction")
TDRV0=BIN\UL2ARM.DLL("ULINK ARM Debugger")
TDRV1=BIN\UL2CM3.DLL("ULINK Cortex Debugger")
TDRU2=BIN\AGDIRDI.DLL("RDI Interface Driver")
TDRU3=BIN\ABLSTCM.dll("Altera Blaster Cortex Debugger")
TDRV4=BIN\lmidk-aqdi.dll("Luminary Eval Board")
TDRV5=Signum\SigVV3Arm.dll("Signum Systems JTAGjet")
TDRV6=Segger\JLTAgdi.dll("J-LINK / J-TRACE")
TDRV7=Seqqer\JL2CM3.d11("Cortex-M3 J-LINK")
TDRV8=STLink\ST-LINKIII-KEIL.dll ("ST-Link Debugger")
TDRU9=BIN\ST-LINKII-KEIL.DLL("ST LinkII Debuqqer")
RTOS@=Dummy.DLL("Dummy")
RTOS1=VARTXARM.DLL ("RTX Kernel - Viewer")
CPUDLL 0=SARM.DLL (TDRV0,TDRV5,TDRV6,TDRV10)
                                                                        # Drivers for AR
CPUDLL1=SARMCM3.DLL(TDRU1,TDRU2,TDRU3,TDRU4,TDRU5,TDRU7,TDRU8,TDRU9)
                                                                        # Drivers for Co
```

(CPUDLL1=SARMCM3.DLL(TDRV1,TDRV2,TDRV3,TDRV4,TDRV5,TDRV7,TDRV8,TDRV9) #Drivers for Corte) 很明显,其实 TDRV6 就是 J-LINK,而这个在 CPUDLL1 中并没有罗列出来。

因此,我们在配置文件的所有的 CPUDLL1 中都添加 TDRV6:

//*********************

用 jlink (JTAG 模式)调试 Cortex-3 核



现在打开 MDK4.02,就会有 J-LINK 了。