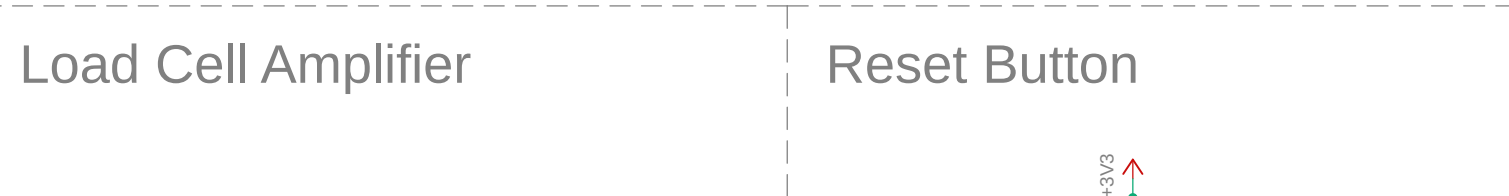
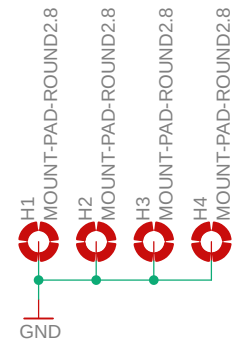
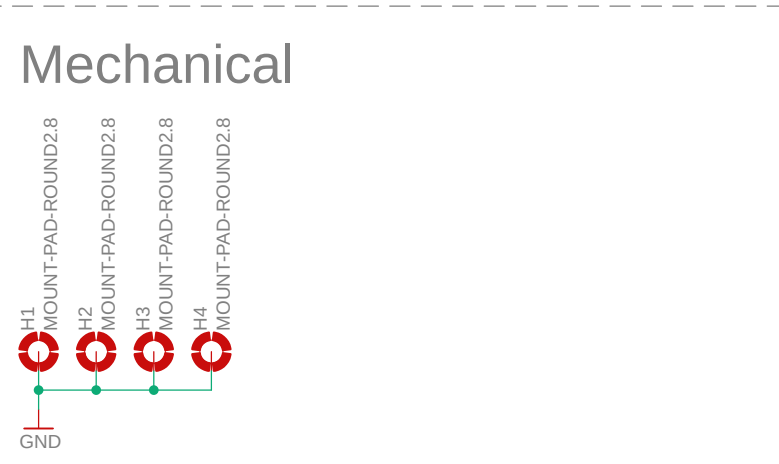


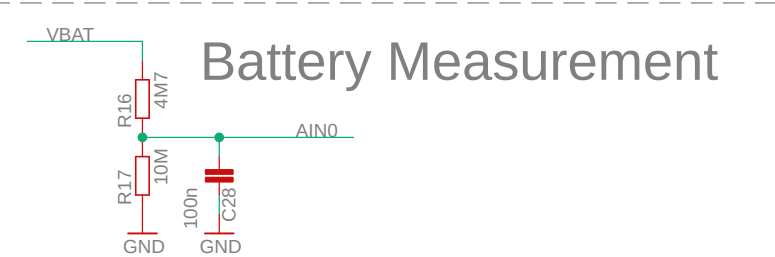
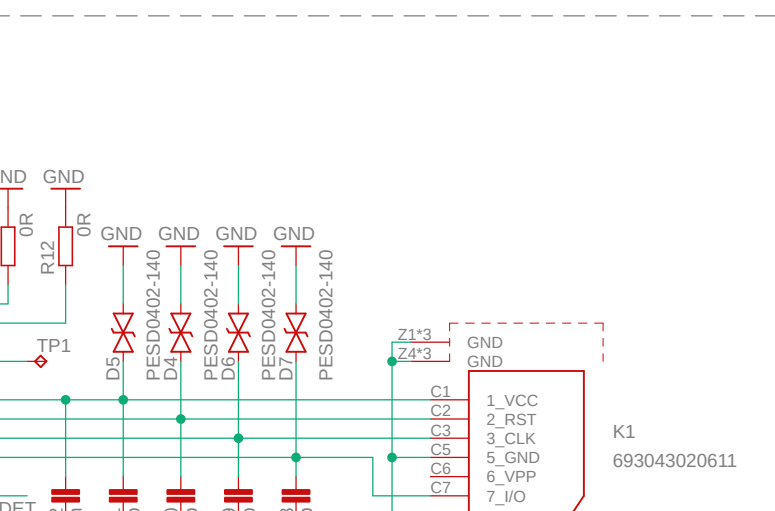
Power Management & Regulator



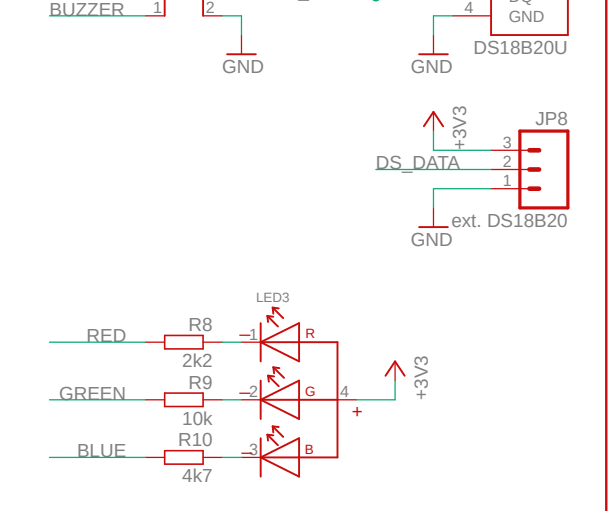
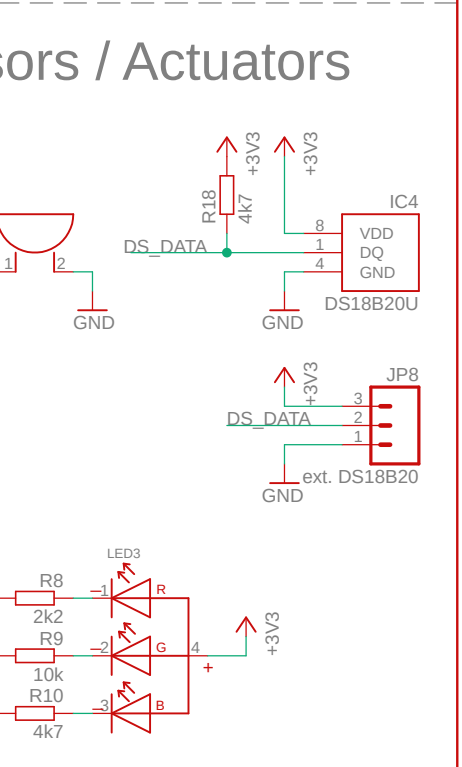
The schematic diagram illustrates the internal wiring of the ADS1232 module. The central component is the ADS1232 IC (IC3). Power is supplied via +3V3 and GND pins. Analog inputs Sens1 and Sens2 are connected to the AINP1/AINN1 and AINP2/AINN2 pins, respectively, through 100nF capacitors (C15 and C14). Digital signals are output through DATA, SCLK, and PDWN pins. The module is configured for a gain of 128. The pin list on the right includes AVDD, REFP, CAP1, CAP2, AINP1, AINN1, AINP2, AINN2, REFN, AGND, DVDD, GAIN1, GAIN0, DRDY/DOUT, SCLK, PDWN, XTAL2, CLKIN/XTAL1, SPEED, A0, TEMP, and DGND.



Microcontroller SAM

[illegible]

Connectors



Sheet: 1/1